

A	B	C
- - = = UNRELEASED DRAWING	= = - - = = NOT FOR MANUFACTURE	= = - -

1

2

3

4

Adaptive, Inc. = CONFIDENTIAL =



PARALLELLA – RF BOARD

B L O C K D I A G R A M

DRAWN :
09/02/2014

DRAWN BY :
F. HUETTIG

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REV :

*

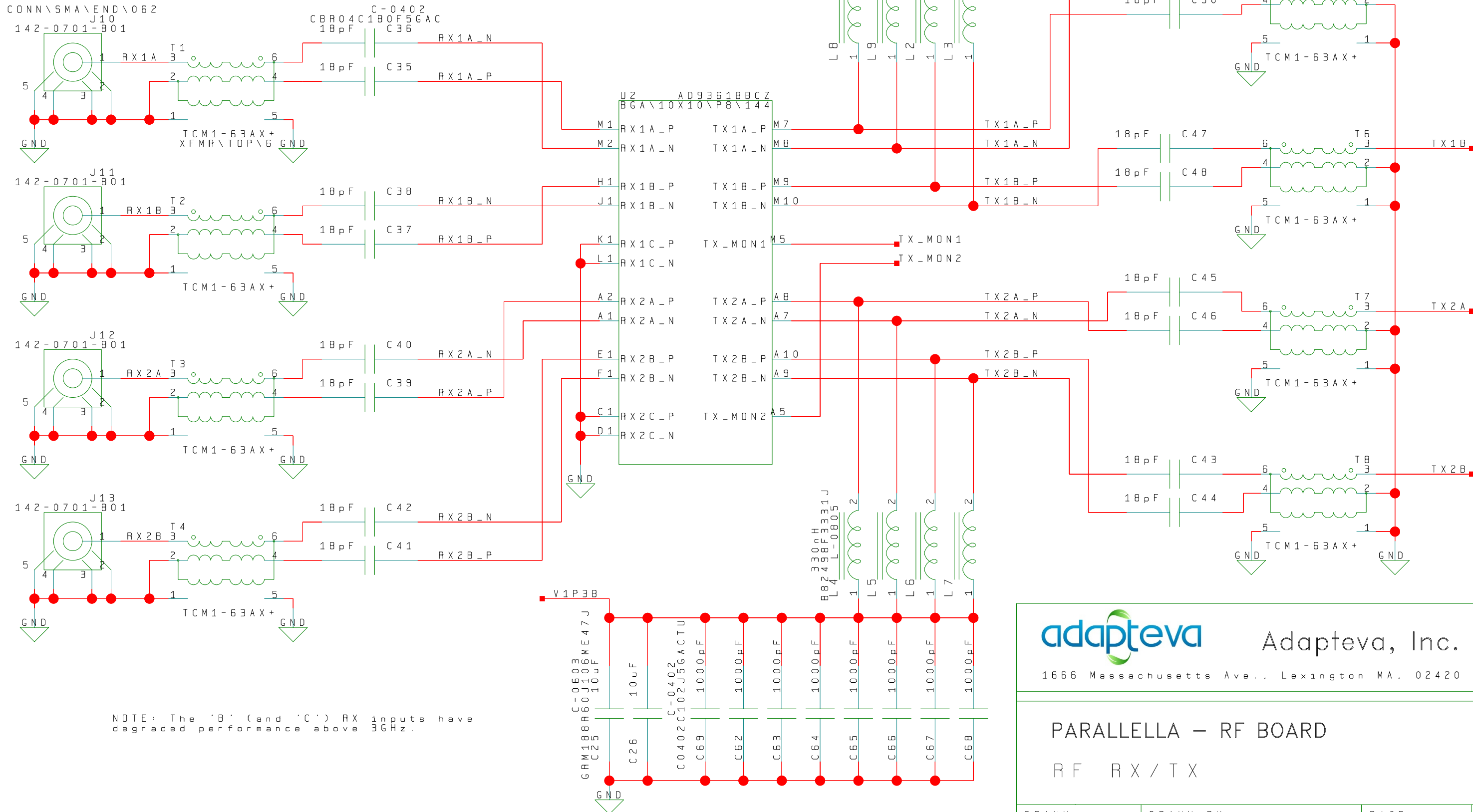
A


B

C

D

NOTE: These values were taken from the FMCOMMS3 board, but for both RX and TX 18pF seems too low to work in the lower part of the band. OTOH, I've used 0402s instead of 0201s (0602 metric) for ease of assembly, which may not be as good at the high end.



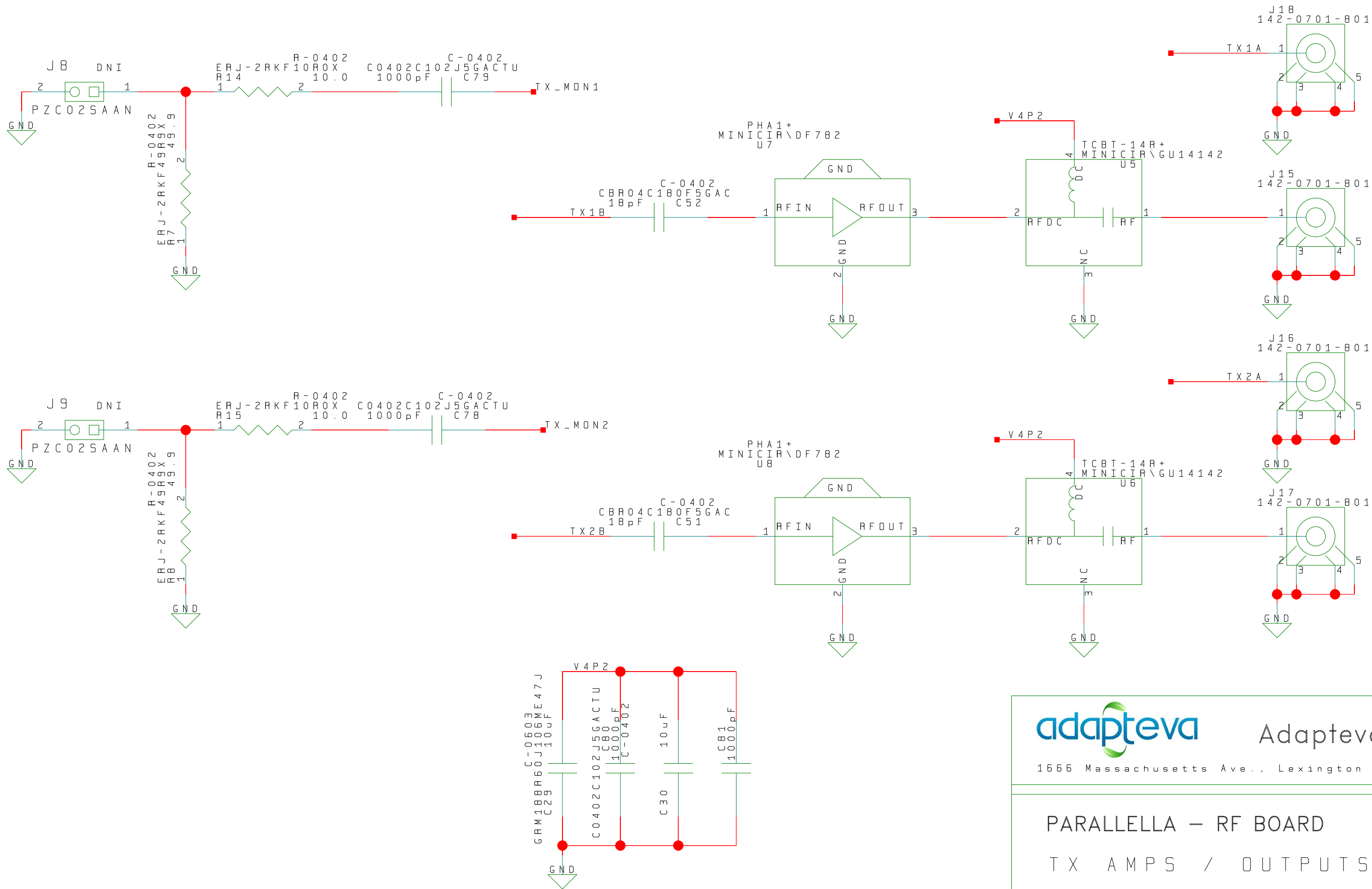


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PARALLELLA – RF BOARD

RF RX / TX

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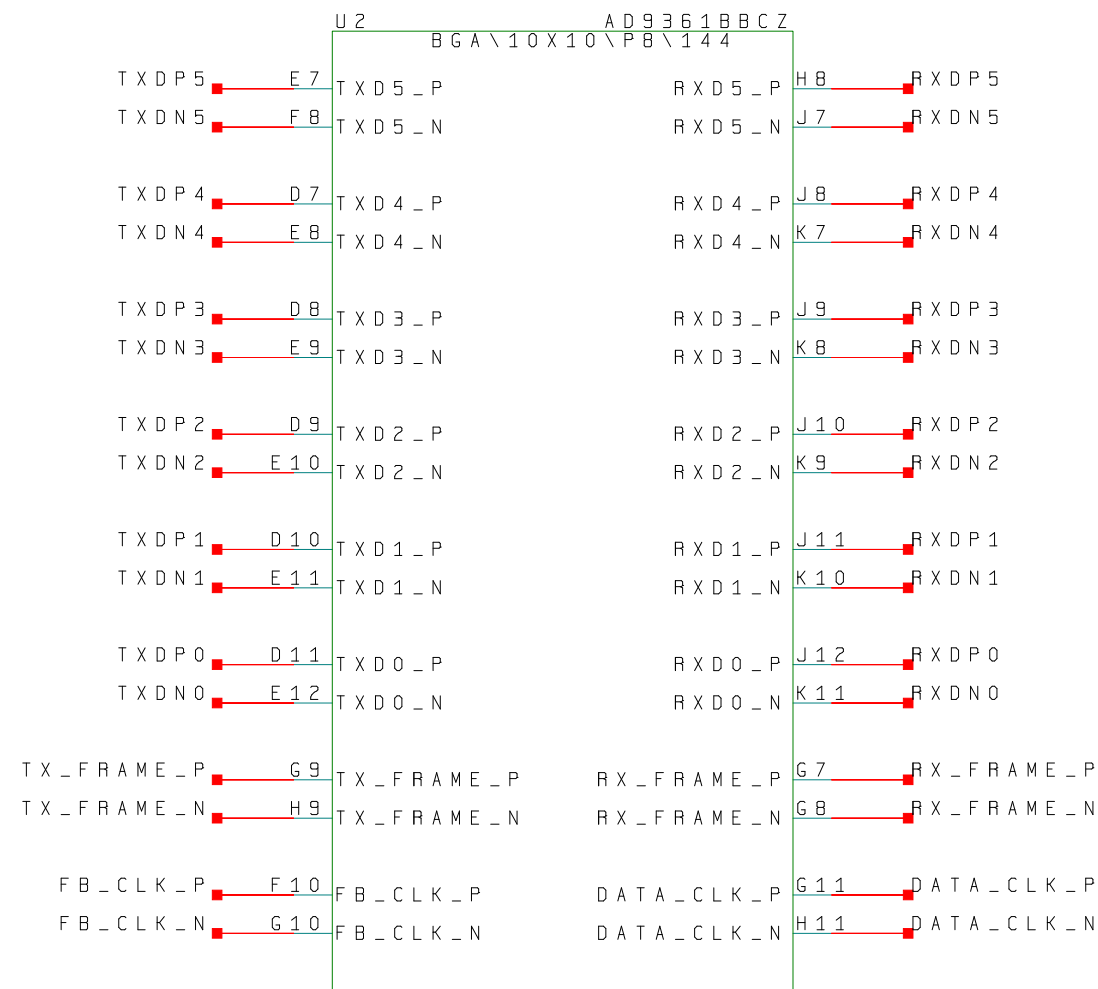
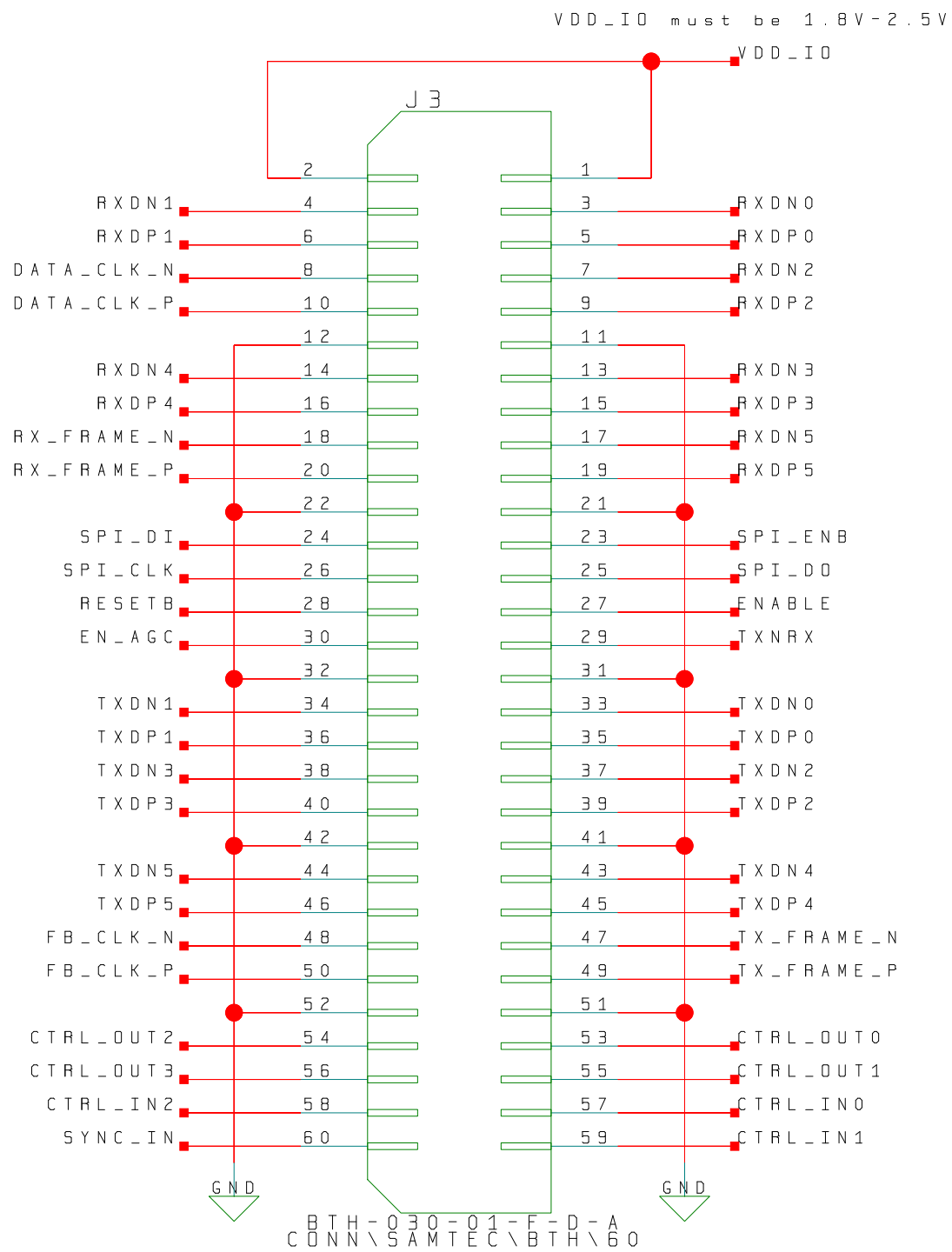
TX AMPS / OUTPUTS

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If/when reassigning RX/TX pins, keep RX in first 12 GPIO pairs with DATA_CLK on lane 3 or 6. Keep TX in second bank of 12 lanes.



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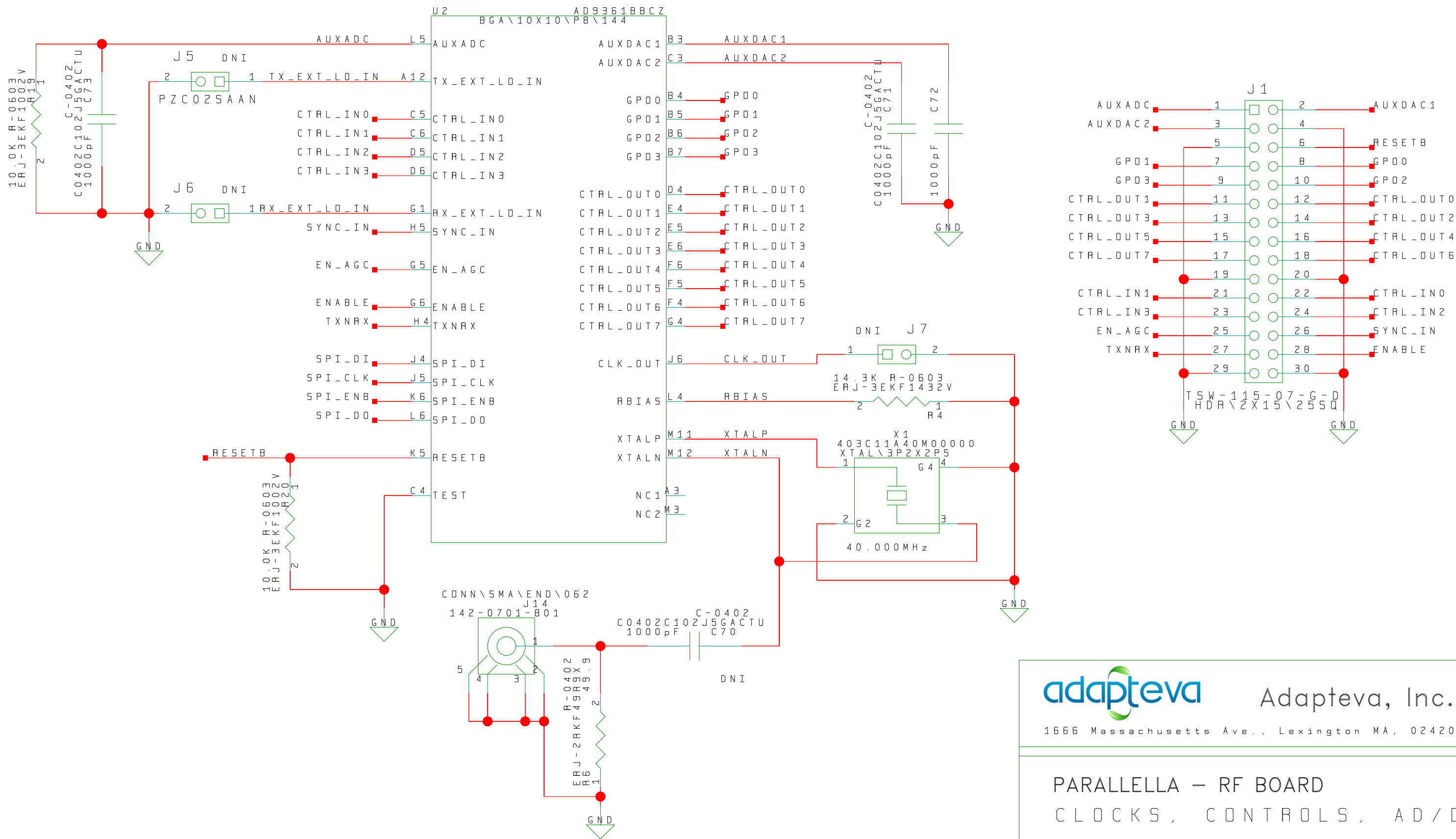
PARALLELLA I/O

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CLOCKS, CONTROLS, AD/DA

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09/08/2014

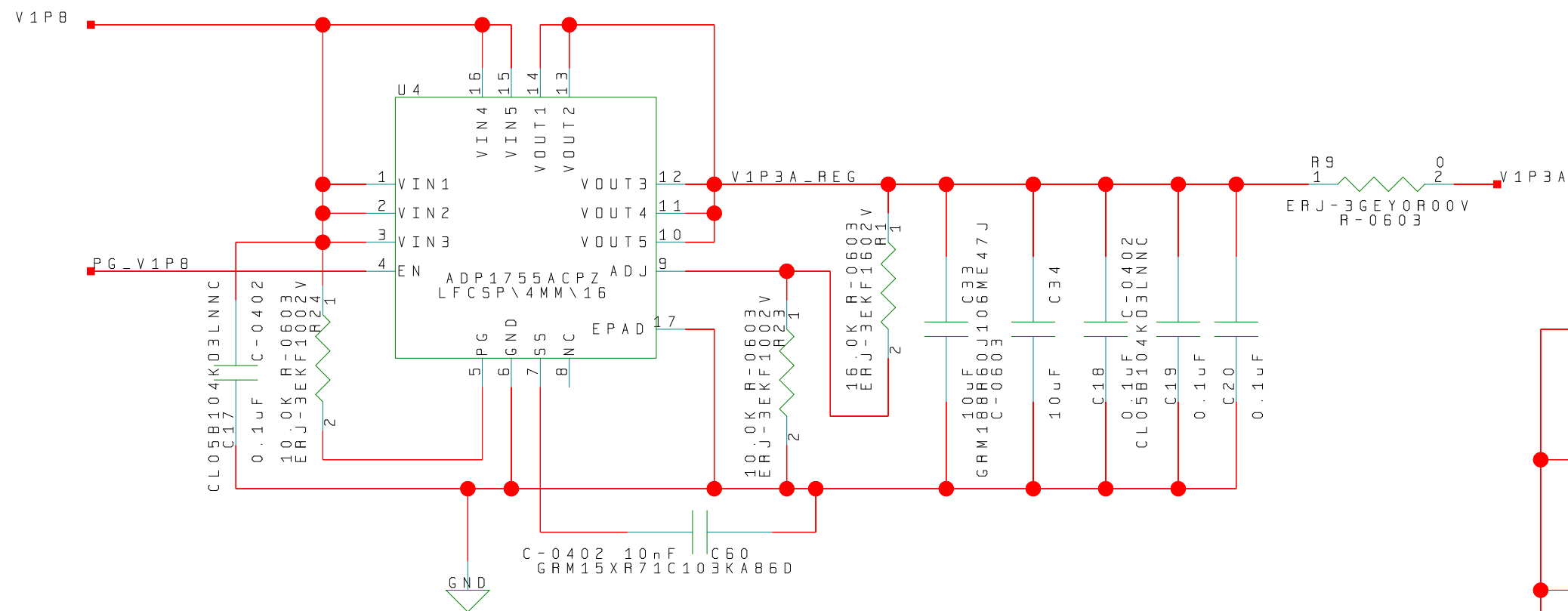
DRAWN BY:
F. HUETTIG

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REV:
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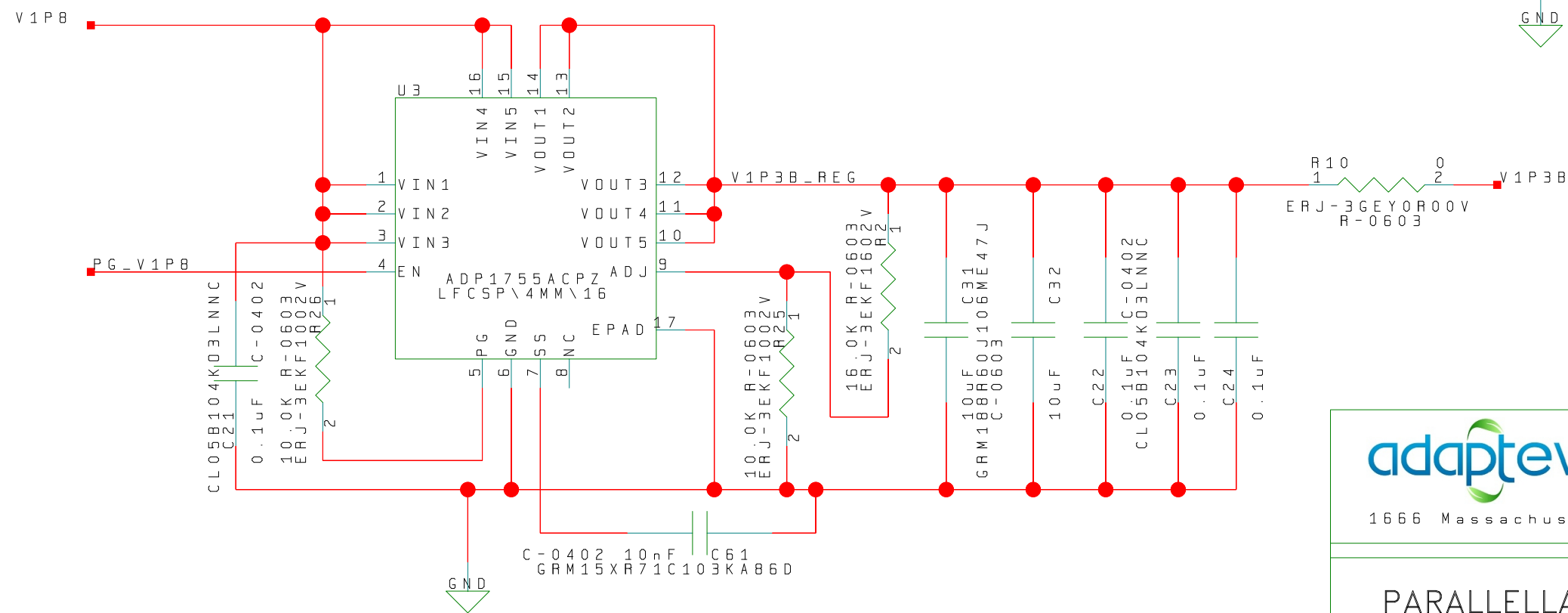


MTHOLE\PTH\125X250\SQ
MH1

MTHOLE\PTH\125X250\SQ
MH2

MTHOLE\PTH\125X250\SQ
MH3

MTHOLE\PTH\125X250\SQ
MH4



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