

PIN	Label	USE
1	Vss	CPU
2	A5	CPU
3	A11	CPU
4	A4	CPU
5	WRB	CPU
6	OEB	CPU
7	A3	CPU
8	A10	CPU
9	A2	CPU
10	AudioOut	Music
11	ROMB	CPU
12	RAMB	CPU
13	A1	CPU
14	D7	CPU
15	A0	CPU
16	D6	CPU
17	D0	CPU
18	D5	CPU
19	D1	CPU
20		
21	D2	CPU
22	D3	CPU
23	WEB	CPU
24	Alarm Data Clock	Head
25	Radioln	Body
26	Lamp0	Body
27	Lamp1	Body
28	Lamp2	Body
29	ArmLamp	Body
30	D4	CPU
31	SPBusyB	Speech
32	PhotoDetector	Head
33	SPStartB	Speech
34	Master CLK	Motion
35	VDD	CPU
36	BigSpaceB	Robot
37	COPData	Motion
38	C0	Speech
39	C1	Speech
40	C2	Speech
41	C3	Speech
42	C4	Speech
43	AlarmB	Head
44	PowerB	Head
45	ResetSwB	Head
46	ResetSysB	CPU
47	PWR Switch B	Speech
48	MNIMB	CPU
49	SPMORESTB	Robot
50	COPAckB	Motion
51	MoCOP Data Out	Motion
52	MoCOP Done	Motion
53	COPRWB	Motion
54	F244 Hz	Head
54	MoDataClk	Motion
55	XTAL0	CPU
56	XTAL1	CPU
57	F499 kHz	Head
58		
59	F2	Robot
60	PowerSenseB	Robot
61	A15	CPU
62	A14	CPU
63	A13	CPU
64	A12	CPU
65	A7	CPU
66	A8	CPU
67	A6	CPU
68	A9	CPU

This is a pinout table based on the Schematic.

It is known to have errors such as duplicated and mislabeled pins

This is just added as a placeholder until the Pins can be retraced and reverified.