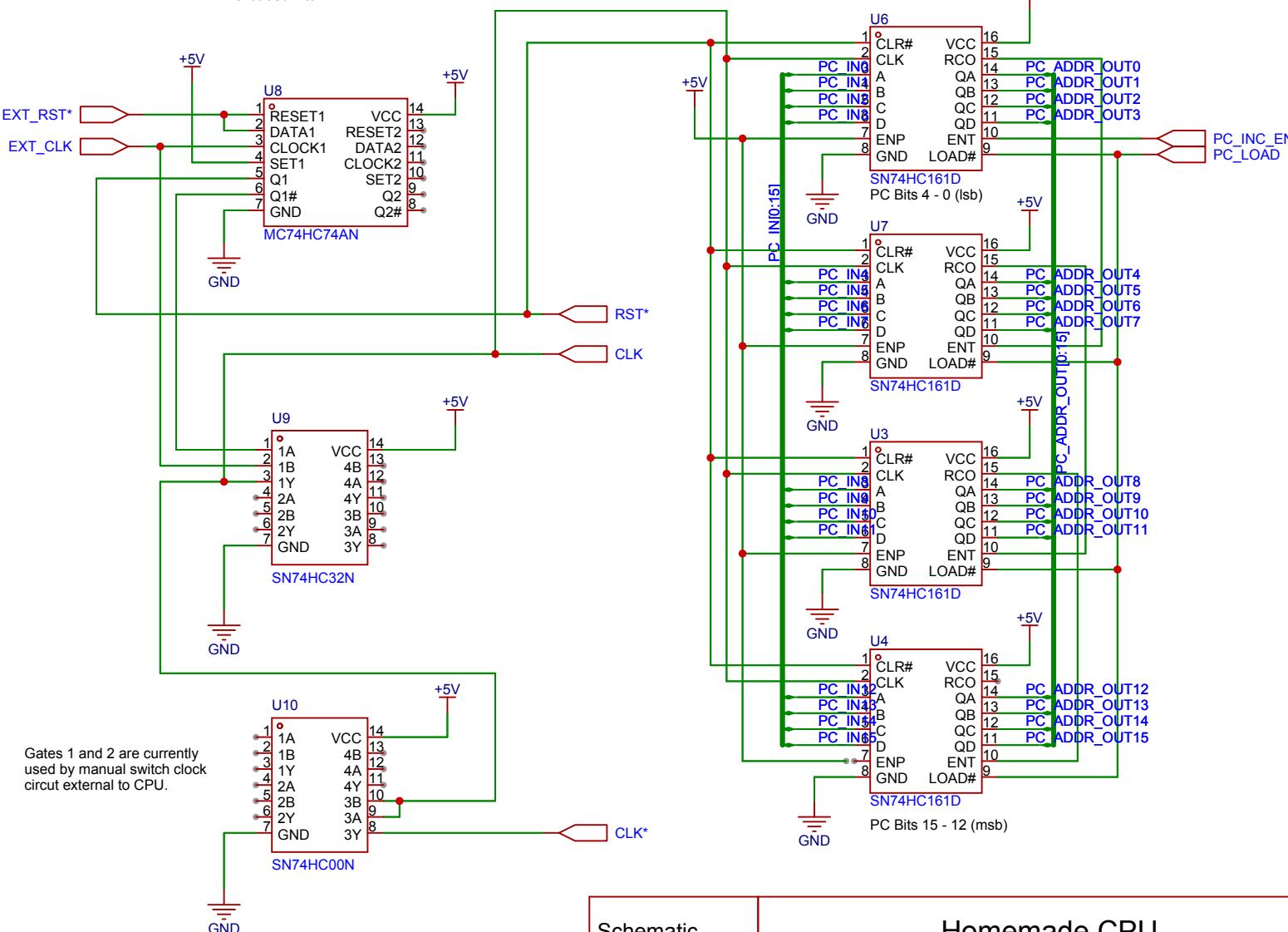


Delay incrementing PC the first clock cycle out of reset.
This is to allow memory location \$0000 to be read. Otherwise,
PC would be incremented to \$0001 before instruction at \$0000
is loaded into IR.



Schematic	Homemade CPU			Create at	2025-11-05
Board				Update at	2025-11-17
Drawn				Page	Reset-Pgm Counter
Reviewed					
	Homemade CPU				
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