

```

1  module Mixing {
2      //////////// ADC ////////////
3      inout          ADC_CS_N ,
4      output         ADC_DIN ,
5      input          ADC_DOUT ,
6      output         ADC_SCLK ,
7
8      //////////// ADC ////////////
9      input          AUD_ADCDAT ,
10     inout          AUD_ADCLRCK ,
11     inout          AUD_BCLK ,
12     output         AUD_DACDAT ,
13     inout          AUD_DACLCK ,
14     output         AUD_XCK ,
15
16     //////////// clocks ////////////
17     input          clock2_50 ,
18     input          clock3_50 ,
19     input          clock4_50 ,
20     input          clock_50 ,
21
22     //////////// HEX ////////////
23     output         [6:0]  HEX0 ,
24     output         [6:0]  HEX1 ,
25     output         [6:0]  HEX2 ,
26     output         [6:0]  HEX3 ,
27     output         [6:0]  HEX4 ,
28     output         [6:0]  HEX5 ,
29
30     //////////// F00 ////////////
31     output         [2]    F00 ,
32 }

```