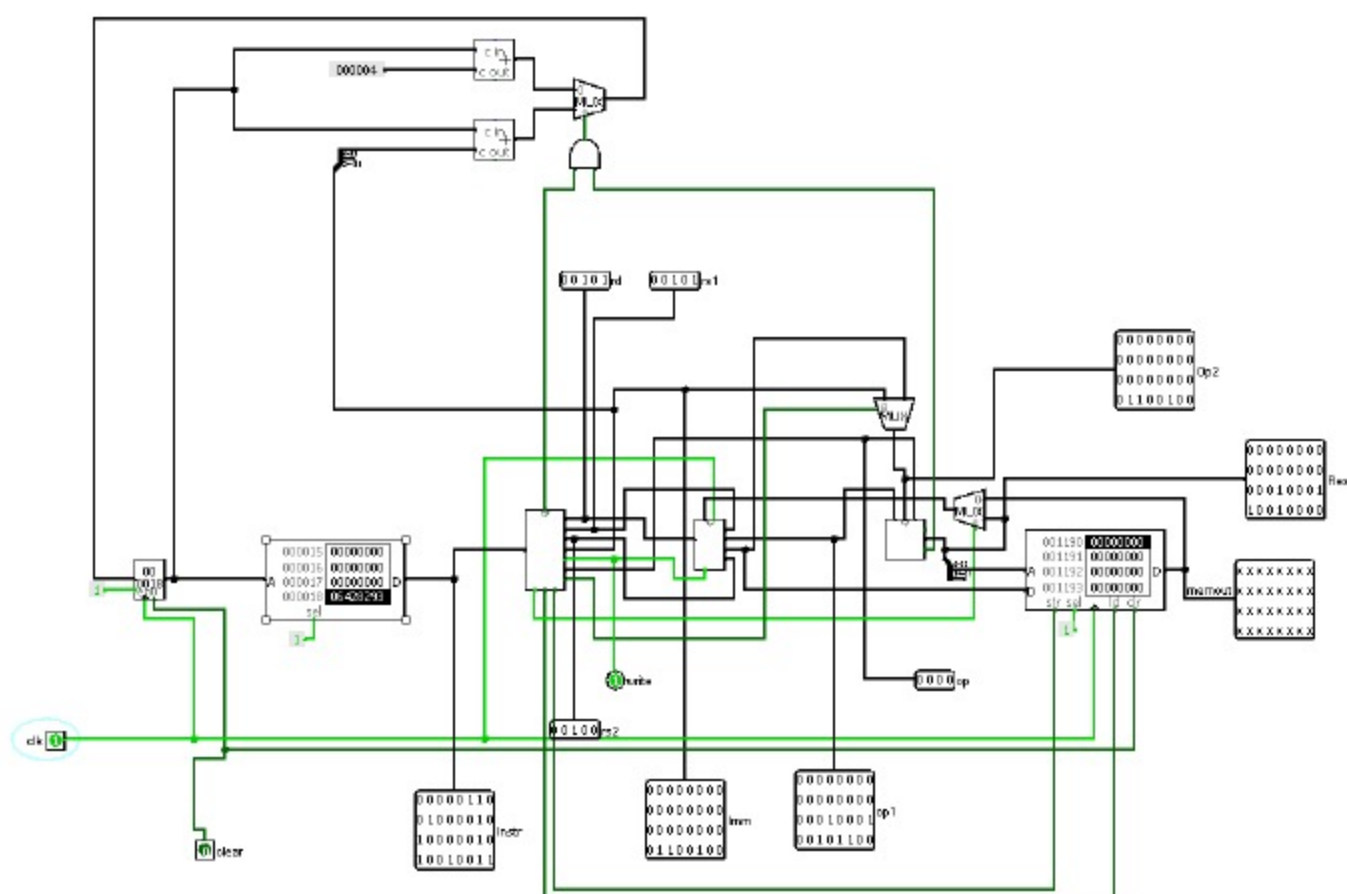
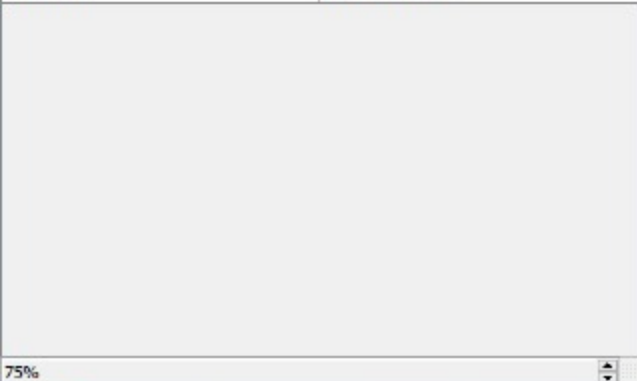


- processor*
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 - imm
 - aluop
 - processor
 - 2x4 dec
 - 3x8 dec
 - 5x32 dec
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 - Wiring
 - Gates
 - Plexers
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 - S-R Flip-Flop
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 - Counter
 - Shift Register
 - Random Generator
 - RAM
 - ROM
 - Input/Output
 - Base

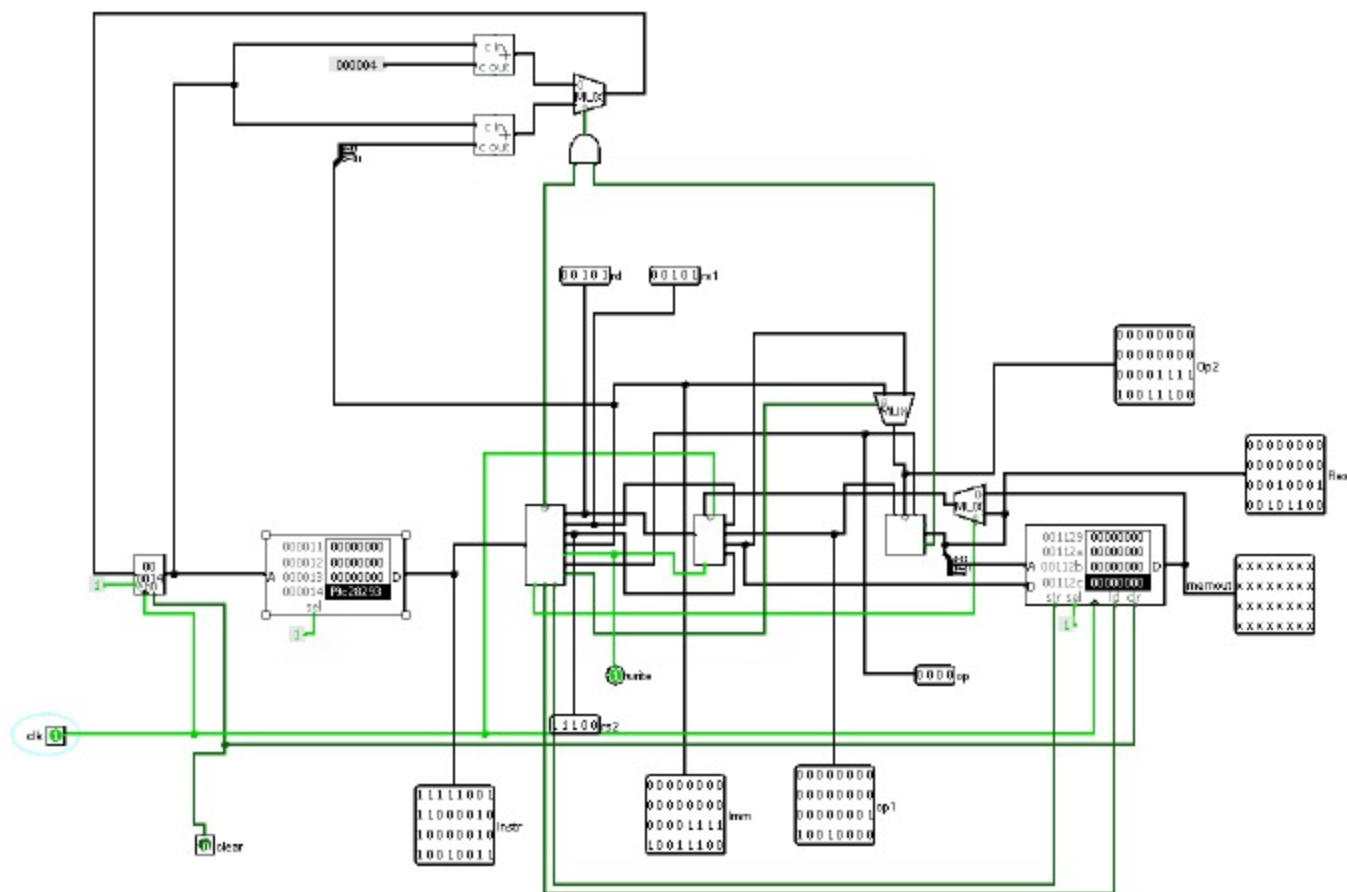
Pin	
Facing	East
Output?	No
Data Bits	1
Three-state?	No
Pull Behavior	Unchanged
Label	clk
Label Location	West
Label Font	SansSerif Plain 12

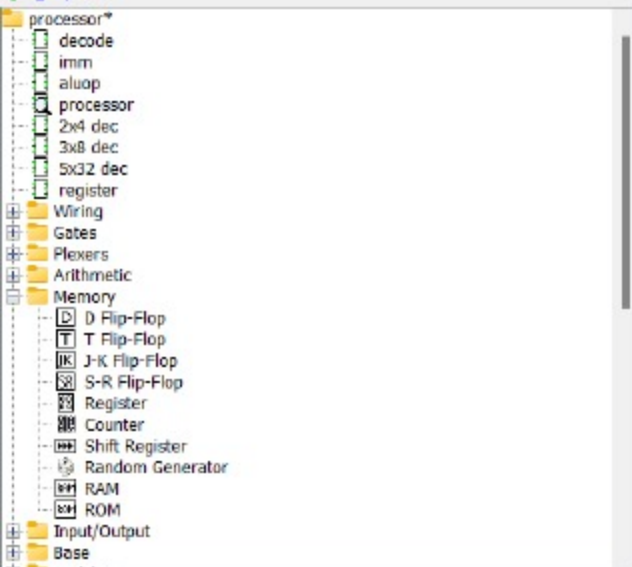


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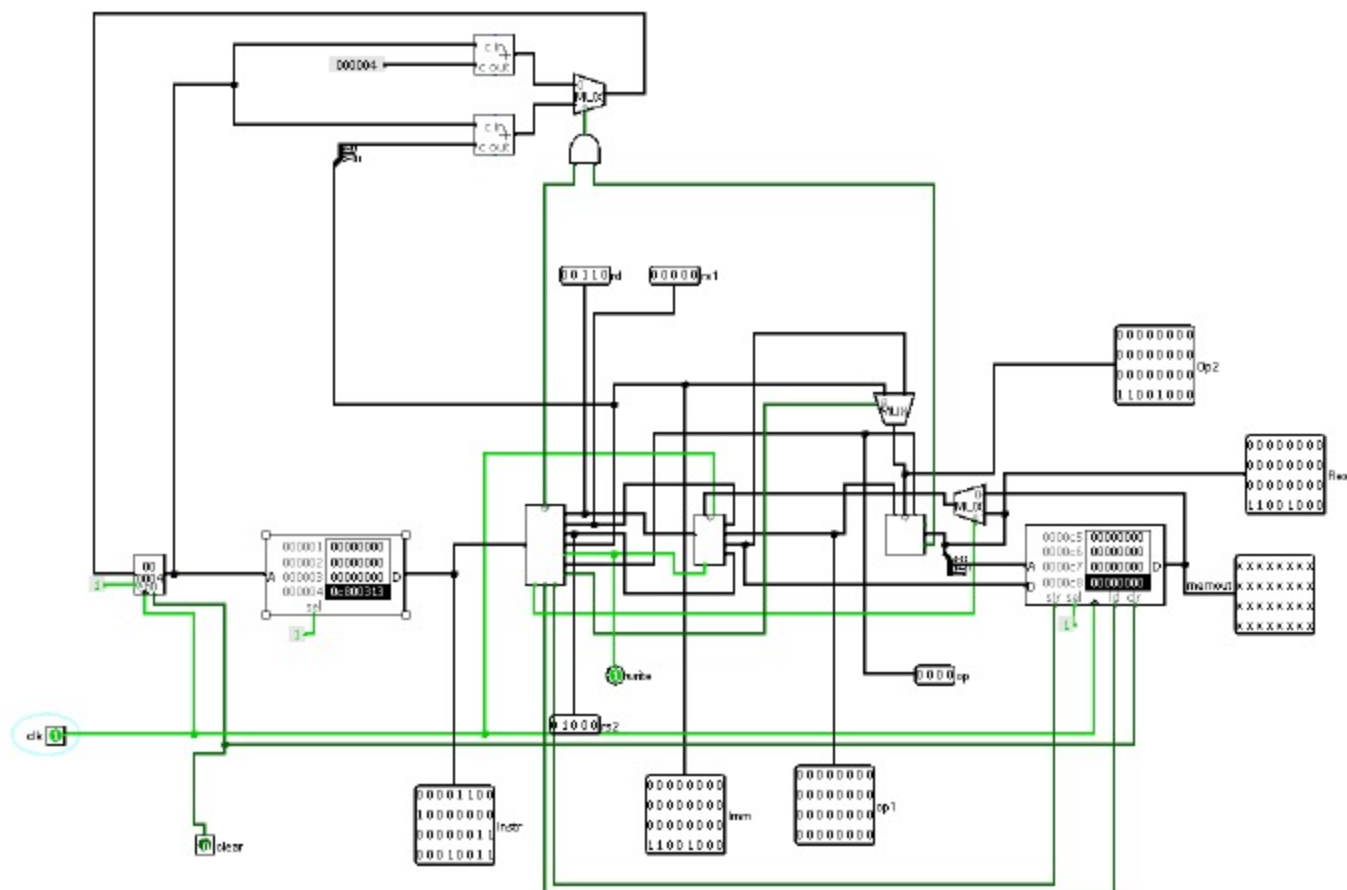




processor*

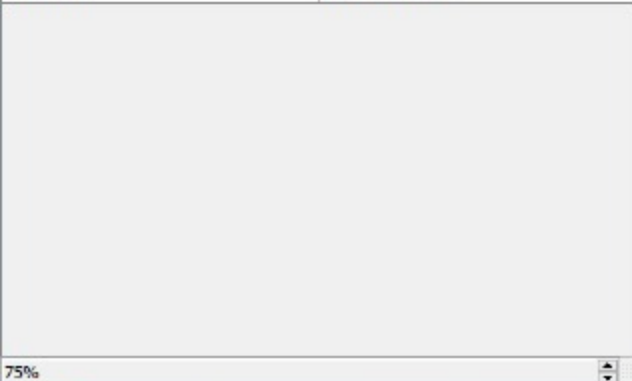
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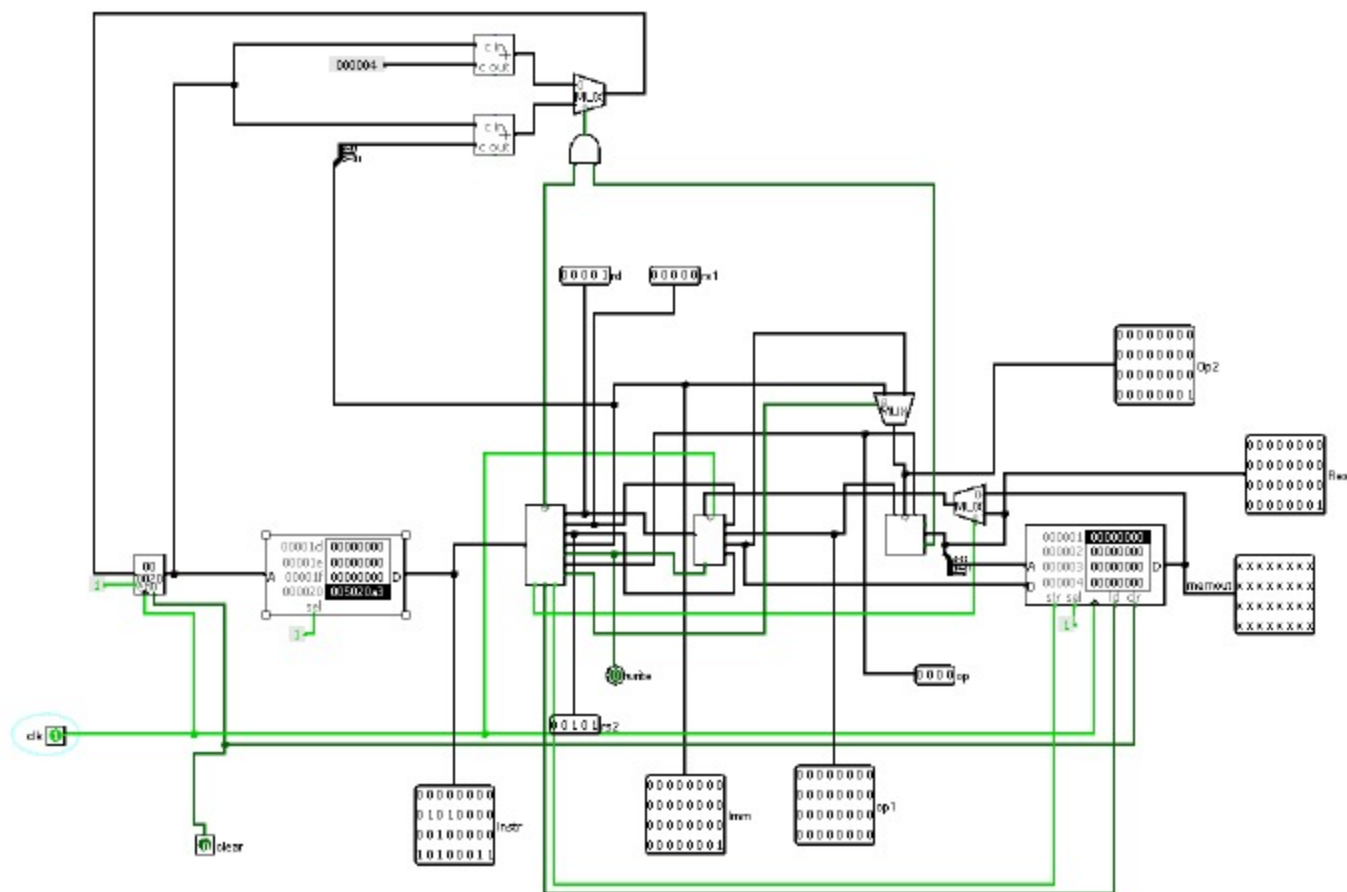


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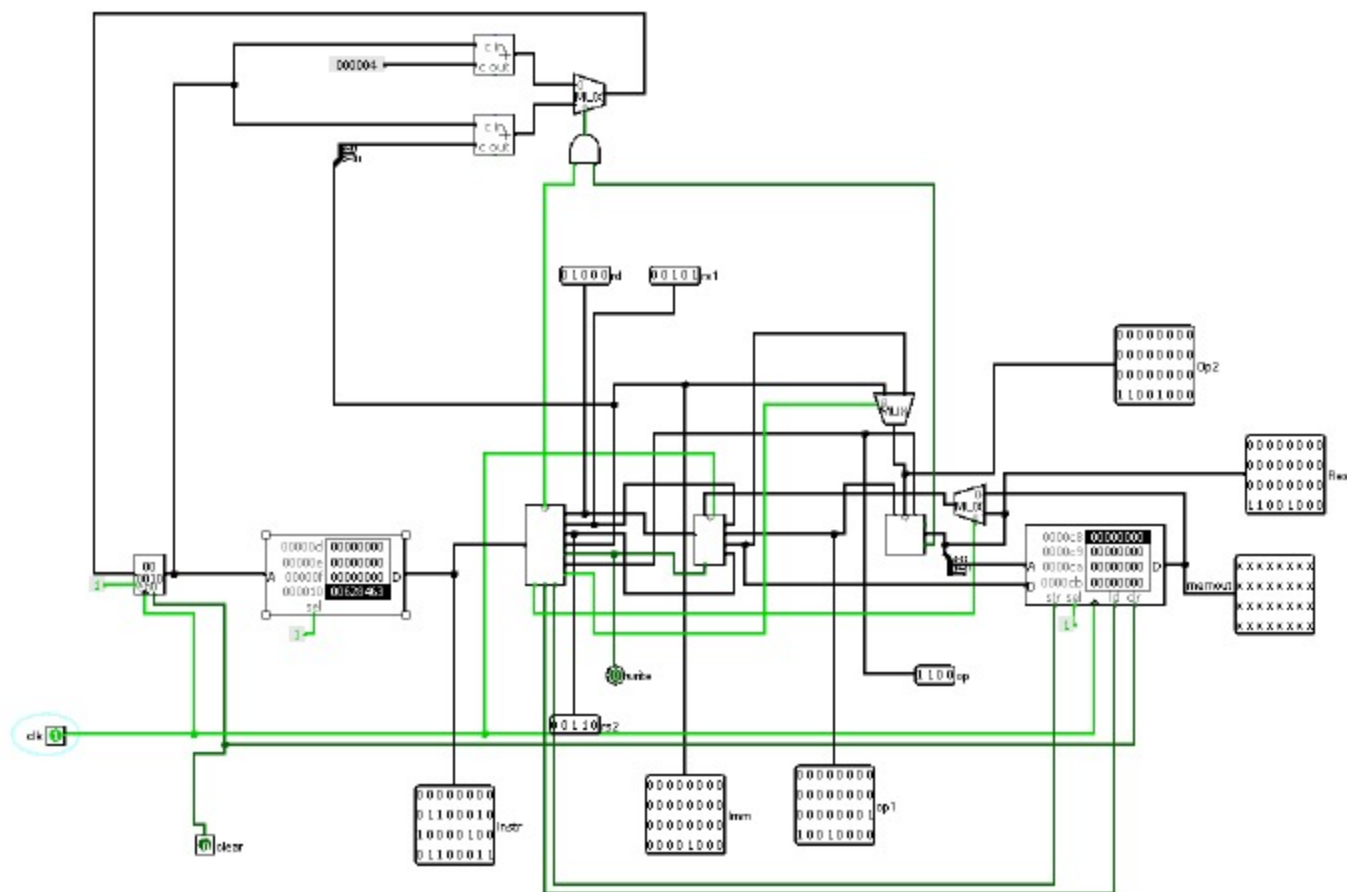
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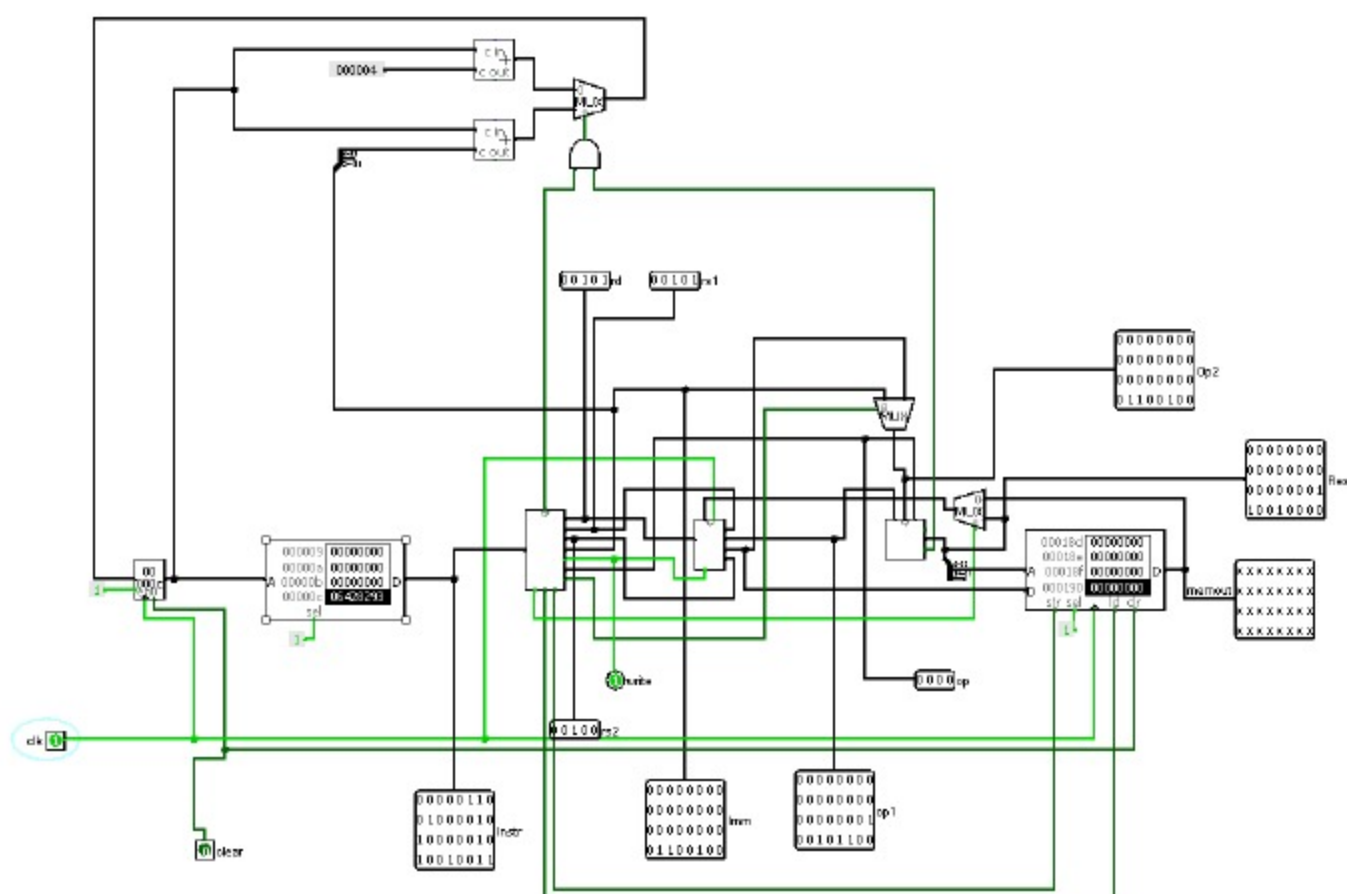
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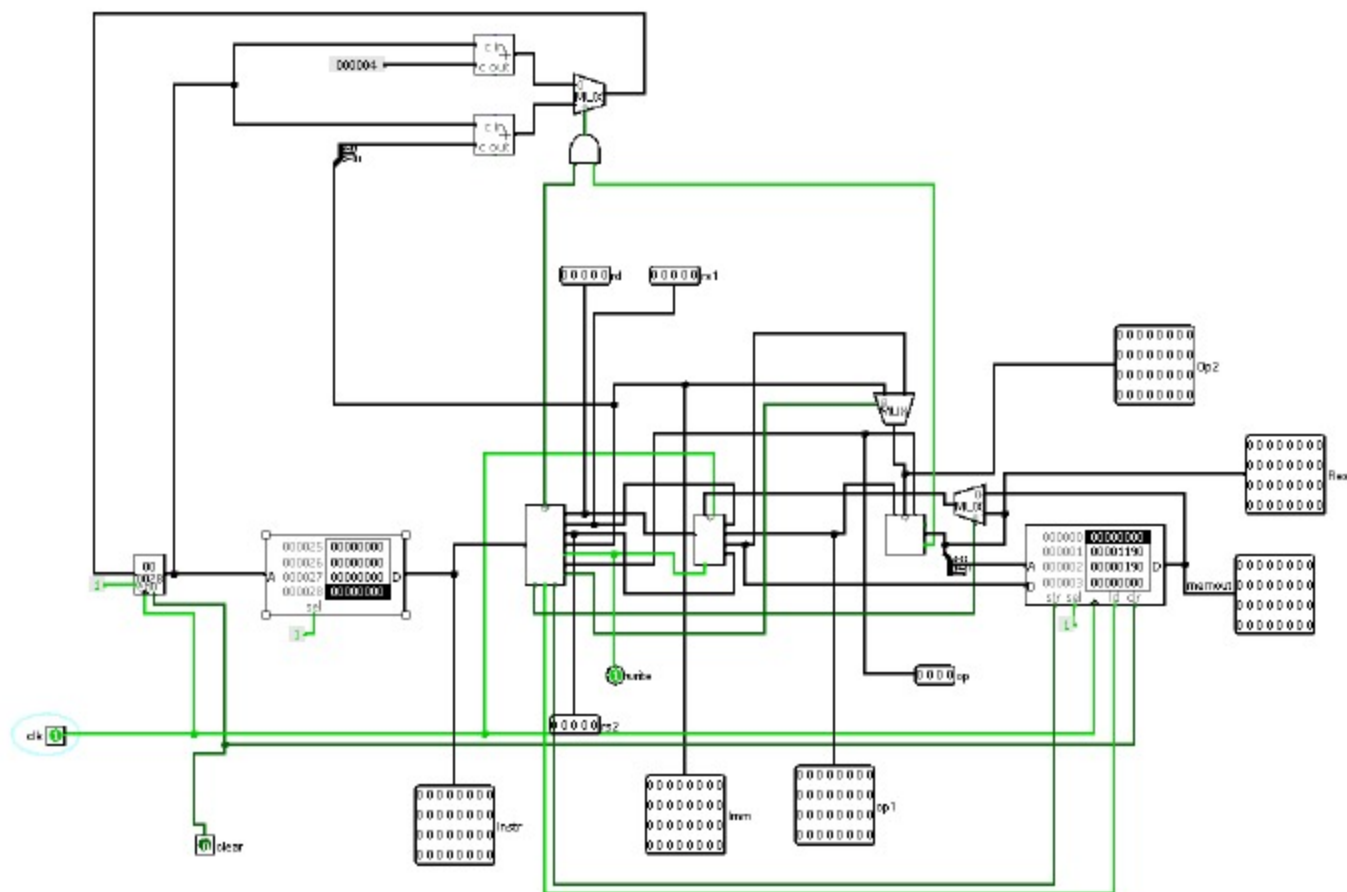
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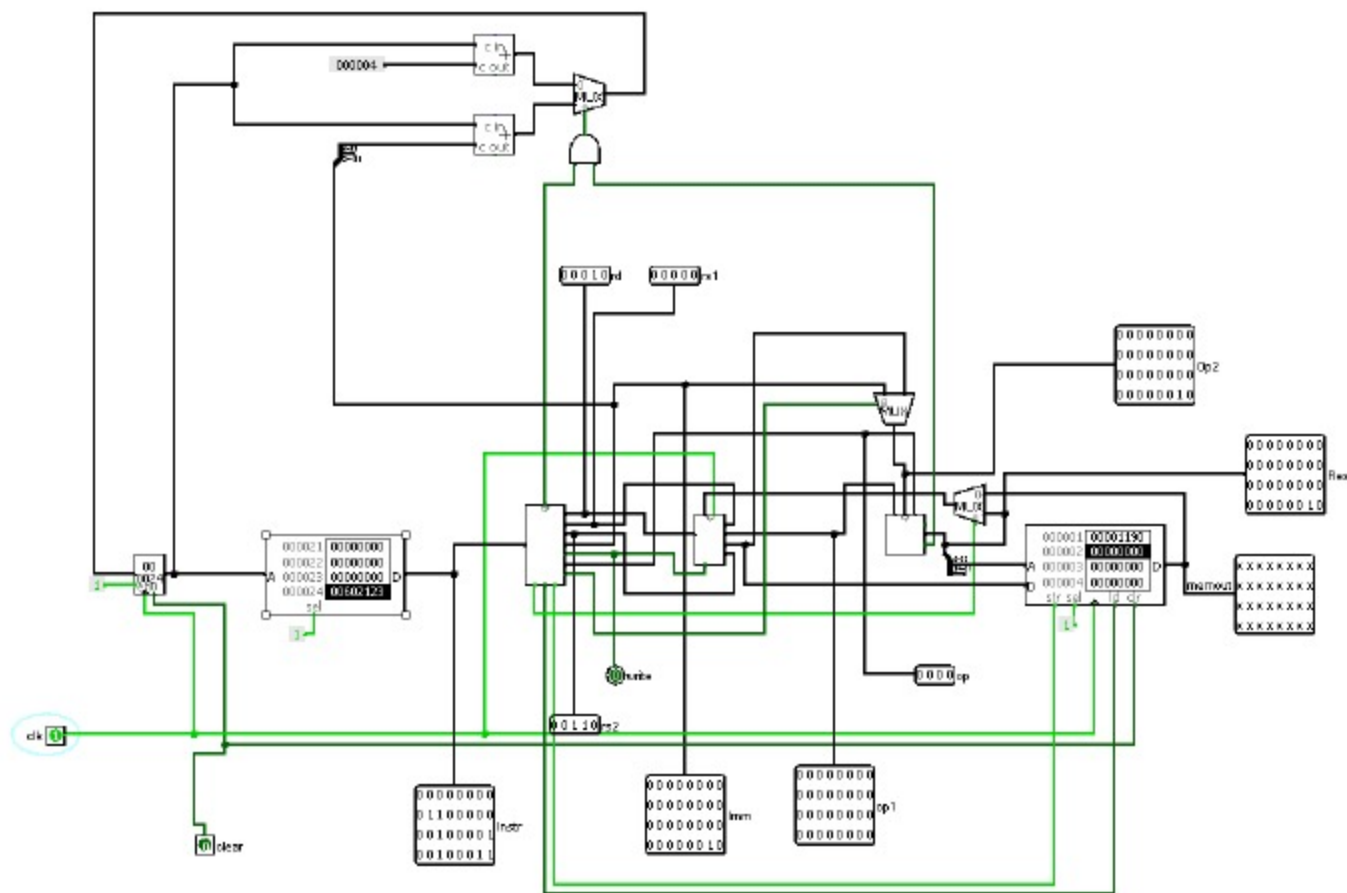


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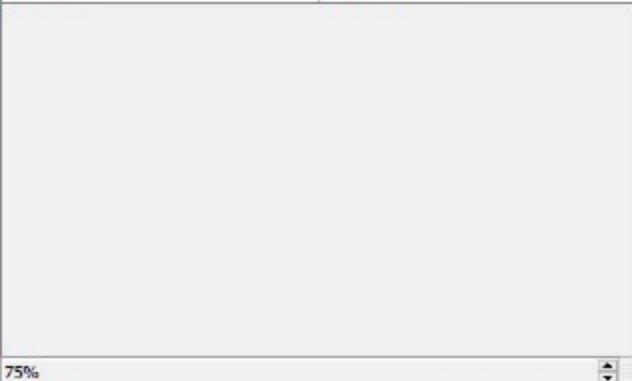
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Text Segment

Address	Code	Basic	Source
0x00400000	0x06400293	addi x5,x0,100	4: addi x5,x0,100
0x00400004	0x0c800313	addi x6,x0,200	5: addi x6,x0,200
0x00400008	0x0062b3b3	sltu x7,x5,x6	6: sltu x7,x5,x6
0x0040000c	0x06428293	addi x5,x5,100	7: addi x5,x5,100
0x00400010	0x00628463	beq x5,x6,8	8: beq x5,x6,loop
0x00400014	0xf9c28293	addi x5,x5,-100	9: addi x5,x5,-100
0x00400018	0x06428293	addi x5,x5,100	11: addi x5,x5,100
0x0040001c	0x0c830313	addi x6,x6,200	12: addi x6,x6,200
0x00400020	0x005020a3	sw x5,1(x0)	13: sw x5,1(x0)
0x00400024	0x00602123	sw x6,2(x0)	14: sw x6,2(x0)