

ANET GEORGE

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SUMMARY

- Electronics and Communication Engineering student with a minor in Computer Science Engineering, and leadership experience as Chair of IEEE SIGHT AG NSSCE.
- Experienced as a Python Developer intern at Siemens, specializing in code creation, optimization, and developing responsive, user-centric web applications.
- Actively seeking opportunities in Electronics and IT to apply academic knowledge to innovative, practical solutions for professional growth.

EDUCATION

- **Bachelor of Technology in Electronics and Communication Engineering**
 - University : *A P J Abdul Kalam Technological University*
 - NSS College of Engineering, Palakkad , Kerala,India - (2025)
 - CGPA : 8.83
- **Higher Secondary** ,St.Pauls's Public School , Thrissur
 - *Central Board of Secondary Education (C B S E)*
 - Grade : 95.5%
- **High School and Elementary School** , St. Paul's Public School , Thrissur
 - *Central Board of Secondary Education (C B S E)*
 - Grade : 97%

SKILLS

Technical Skills

- **Programming** : Python ,Flask ,HTML,CSS ,C Programming ,SQL,JavaScript
- **Technologies & Tools** : Microsoft Visual Studio Code ,Proteus ,Arduino ,MATLAB ,Spyder , LTSpice , Keil microvision ,LaTeX typesetting and publishing .

Soft skills

:Leadership , Adaptability , Interpersonal Skills , Effective Communication

INTERNSHIP EXPERIENCE

Python Developer Intern | Siemens

Bangalore - June 2024

- Developed Web Applications: Utilized Flask, Python, HTML, CSS, and JavaScript to build and maintain web applications.
- UI Design and Implementation: Contributed to the design and implementation of user interfaces, significantly enhancing user experience.
- Debugging and Testing: Assisted in debugging, testing, and improving the performance of applications, ensuring robustness and reliability.
- **OCR GenAI Project**: Involved in integrating Optical Character Recognition (OCR) with **Generative AI** to enhance text recognition, data extraction, and automation capabilities.

Intern | Indian Railways

Erode - January 2024

- Assisted in the maintenance and inspection of railway infrastructure and equipment.
- Participated in the analysis and optimization of railway operational systems.
- Supported senior engineers in implementing safety and efficiency improvements.

PROJECT

FPGA-Based Superpixel Segmentation for Hyperspectral Imaging

Final Year Project : Ongoing at IIT Palakkad

- Developing an FPGA-based hardware architecture for superpixel segmentation in hyperspectral imaging using MATLAB and Verilog HDL.
- Focused on real-time anomaly detection for remote sensing and aerospace applications, aligning with core VLSI design principles.

PLAGIA-GUARD – A Plagiarism Detection System

Technologies used: Python, Flask, TF-IDF, BERT, Sentence Transformers

- Built a web-based plagiarism detection tool leveraging TF-IDF, cosine similarity, and sentence embeddings for accurate semantic analysis.
- Integrated advanced machine learning models like Sentence Transformers (BERT) to improve detection accuracy.
- Deployed as a Flask web application, enabling users to upload and analyze text documents for content similarity.

INTERACTIVE LIVE CODE EDITOR – Your Personal CodePen Experience

Technologies used: HTML, CSS, JavaScript

- Developed a real-time interactive code editor that allows users to write and preview HTML, CSS, and JavaScript code simultaneously.
- Designed an intuitive, user-friendly interface to streamline coding and testing experiences in the browser.
- Enabled seamless front-end code experimentation for learners and developers.

IEEE Volunteer Projects:

SOURAJALA – Solar-Based Drinking Water Initiative

Funded by IEEE Humanitarian Technologies Board – \$5,296

- Contributed to the development and implementation of a solar-powered clean water system for underserved rural areas.
- Involved in project coordination, planning, and community engagement to ensure sustainable impact.

MOVE INDIA PROJECT

- Developed and produced 50 solar-powered mobile chargers for the MOVE India Project.
- Improved disaster resilience and emergency electricity access in 10+ remote areas.

POSITIONS OF RESPONSIBILITY (POR)

- **Chair, IEEE SIGHT AG NSSCE (2024-2025):** Led a team to secure funding for humanitarian technology projects, including a solar-based drinking water initiative, and organized tech workshops.
- **Vice Chair, IEEE SIGHT NSSCE (2023-2024):** Planned sustainable energy projects and fostered student collaboration.
- **Electronics and Communication Coordinator, IEEE SIGHT Kerala Section (2023-2024):** Managed electronics solutions and organized statewide events.
- **Organizer, Chakravyuha Hackathon (2023):** Coordinated NSSCE's first 24-hour hackathon, driving student innovation.
- **Core Member, E-FENSE Project:** Core member of \$4100 funded project E-FENSE

CERTIFICATIONS / ACHIEVEMENTS

- IASC-INSANA-NASI Summer Research Fellow at the International Institute of Information Technology (IIIT) Bhubaneswar, India.
 - Essential Mathematics for Machine Learning : Certified under Indian institute of Technology (IIT) Roorke (NPTEL)
 - Deep learning : Certified for deep learning under Indian institute of Technology (IIT) Kharagpur (NPTEL)
 - Flask and Python Framework (Udemy)
 - Best Student Award 2020
 - Core member of \$4100 funded project E-FENSE
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