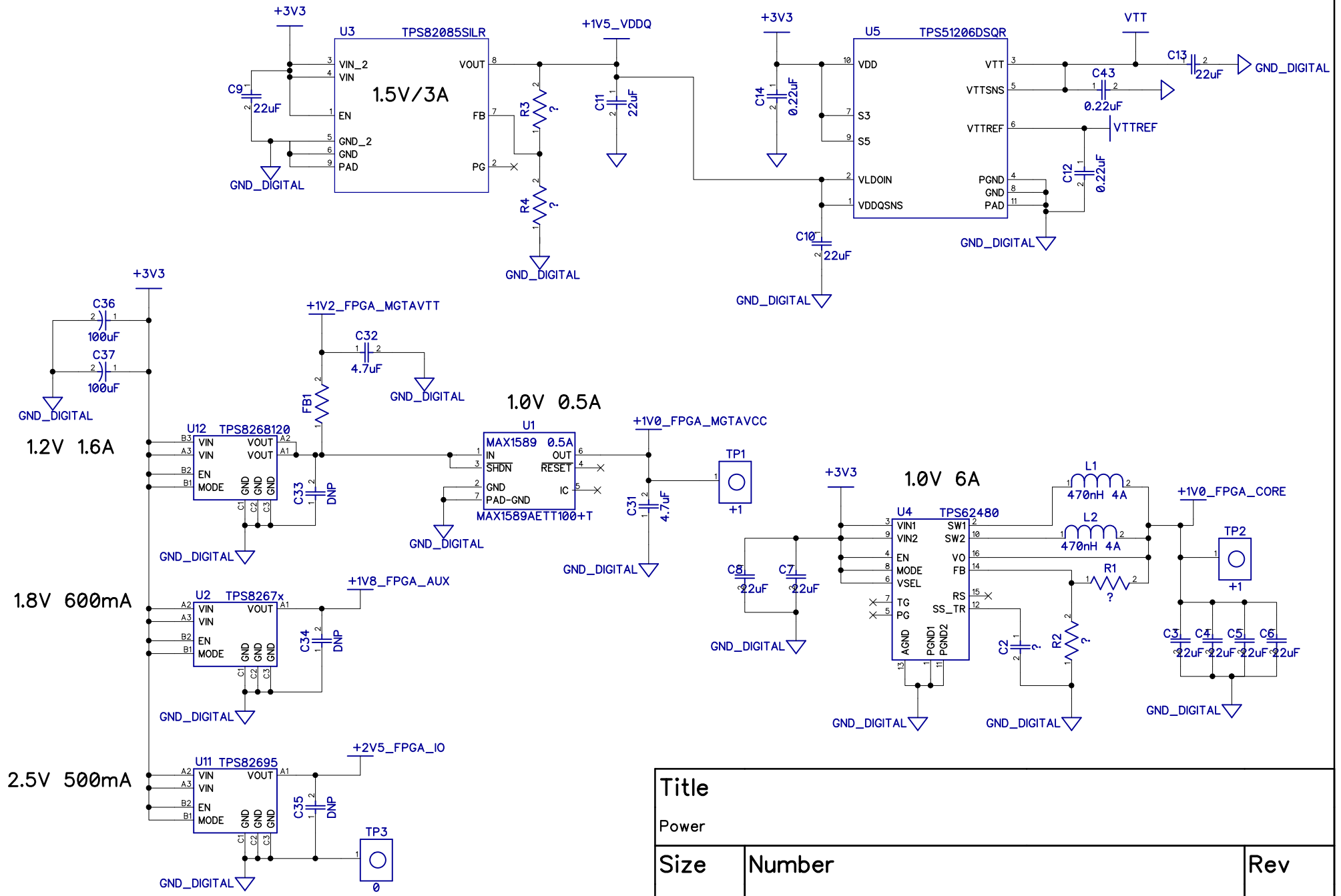


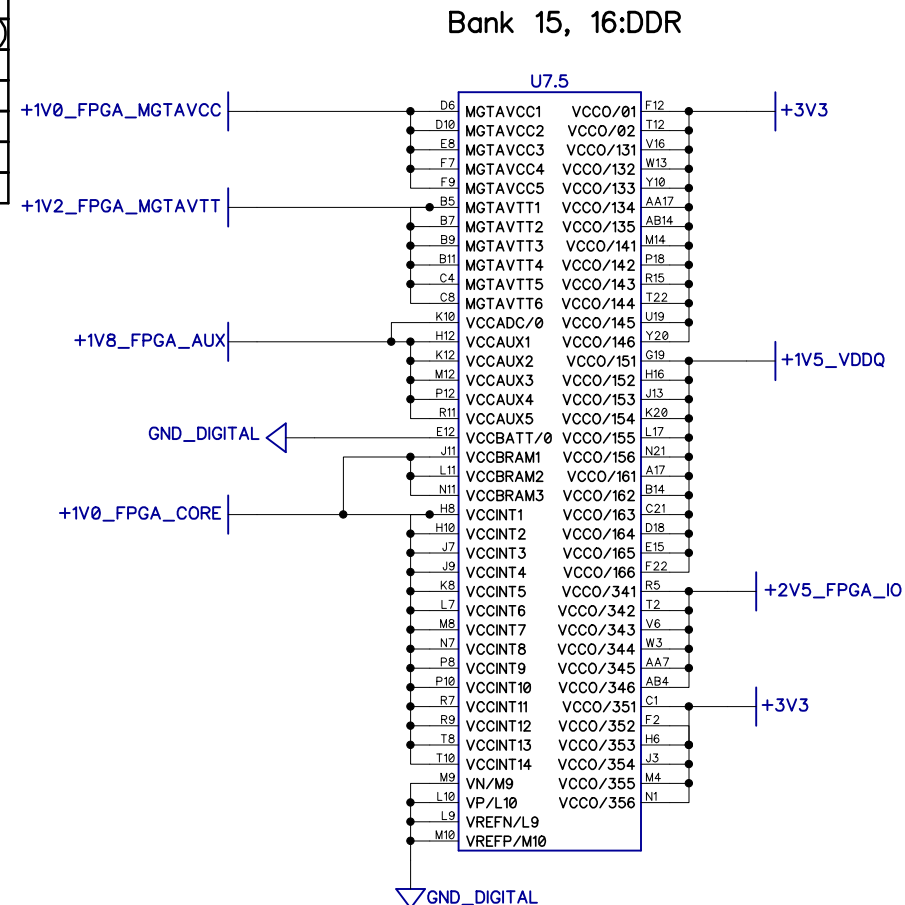
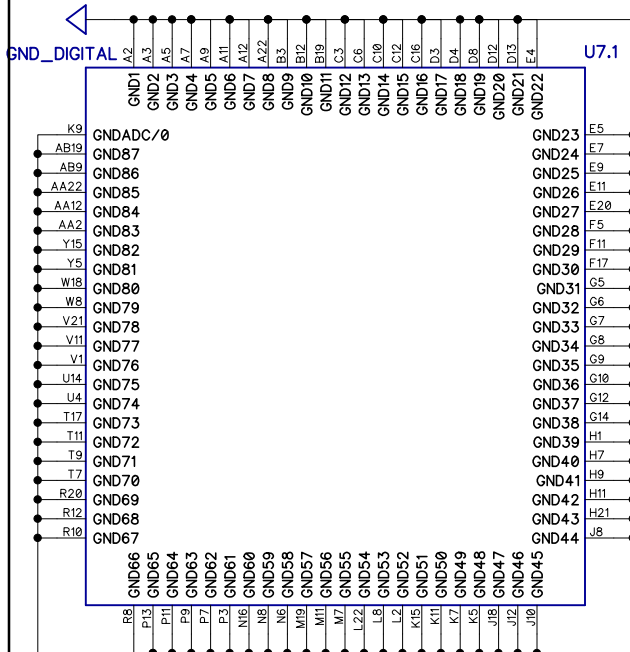
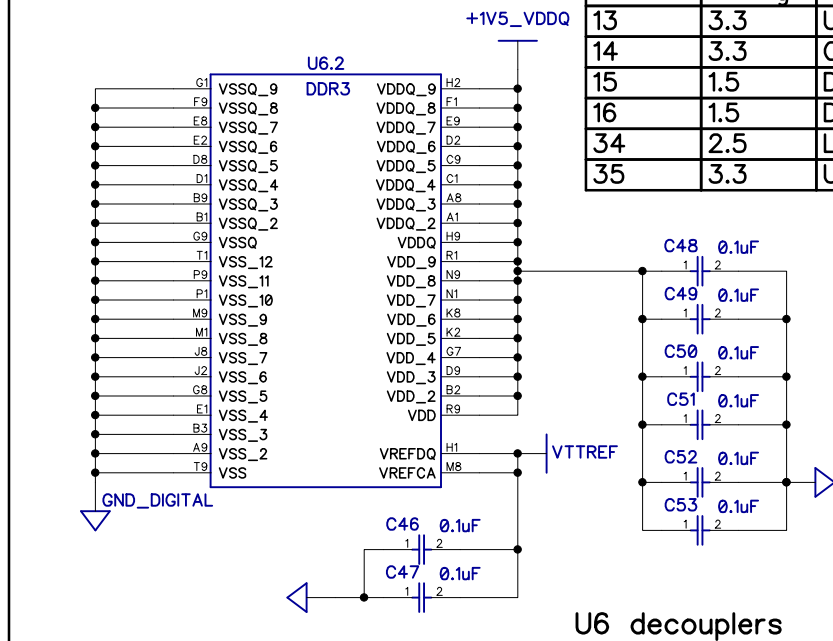
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Title	
Block Diagram	
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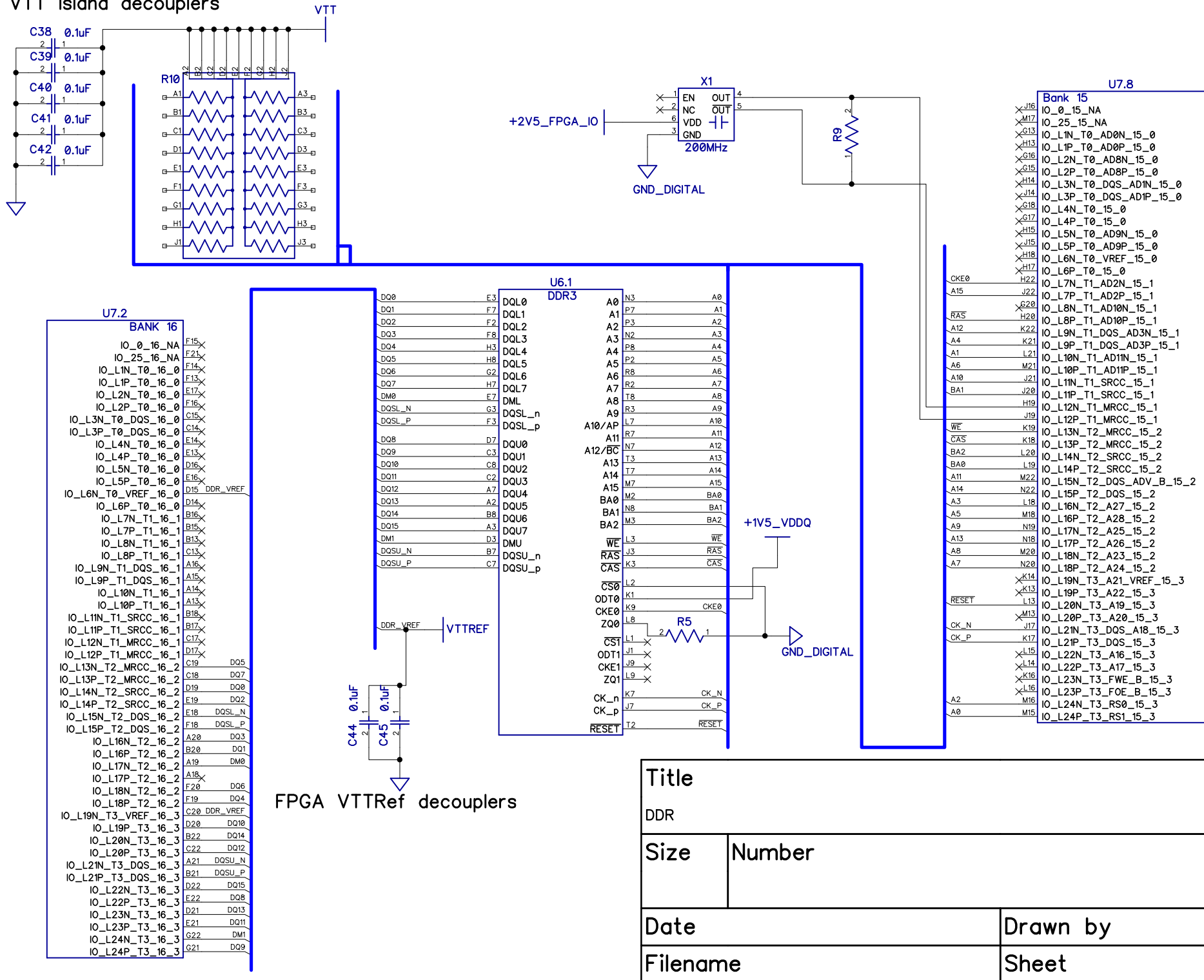
Title		
Power		
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Bank	Voltage	Purpose
13	3.3	Unused (NA on A50)
14	3.3	Config & 3.3V IO
15	1.5	DDR Addr/CTL
16	1.5	DDR data
34	2.5	LVDS I/O
35	3.3	Unused



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FPGA and DRAM power and decoupling		
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VTT Island decouplers



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DDR		
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		Rev

U7.10

Bank 35	
IO_0_35_NA	F4
IO_25_35_NA	L6
IO_L1N_T0_AD4N_35_0	A1
IO_L1P_T0_AD4P_35_0	B1
IO_L2N_T0_AD12N_35_0	B2
IO_L2P_T0_AD12P_35_0	C2
IO_L3N_T0_DQS_AD5N_35_0	D1
IO_L3P_T0_DQS_AD5P_35_0	E1
IO_L4N_T0_35_0	D2
IO_L4P_T0_35_0	E2
IO_L5N_T0_AD13N_35_0	F1
IO_L5P_T0_AD13P_35_0	G1
IO_L6N_T0_VREF_35_0	E3
IO_L6P_T0_35_0	F3
IO_L7N_T1_AD6N_35_1	J1
IO_L7P_T1_AD6P_35_1	K1
IO_L8N_T1_AD14N_35_1	G2
IO_L8P_T1_AD14P_35_1	H2
IO_L9N_T1_DQS_AD7N_35_1	J2
IO_L9P_T1_DQS_AD7P_35_1	K2
IO_L10N_T1_AD15N_35_1	H5
IO_L10P_T1_AD15P_35_1	J5
IO_L11N_T1_SRCC_35_1	G3
IO_L11P_T1_SRCC_35_1	H3
IO_L12N_T1_MRCC_35_1	G4
IO_L12P_T1_MRCC_35_1	H4
IO_L13N_T2_MRCC_35_2	J4
IO_L13P_T2_MRCC_35_2	K4
IO_L14N_T2_SRCC_35_2	K3
IO_L14P_T2_SRCC_35_2	L3
IO_L15N_T2_DQS_35_2	L1
IO_L15P_T2_DQS_35_2	M1
IO_L16N_T2_35_2	M2
IO_L16P_T2_35_2	M3
IO_L17N_T2_34_2	T6
IO_L17P_T2_35_2	J6
IO_L17P_T2_35_2	K6
IO_L18N_T2_35_2	L4
IO_L18P_T2_35_2	L5
IO_L19N_T3_VREF_35_3	N3
IO_L19P_T3_35_3	N4
IO_L20N_T3_35_3	P1
IO_L20P_T3_35_3	R1
IO_L20P_T3_35_3	R1
IO_L21N_T3_DQS_35_3	P4
IO_L21P_T3_DQS_35_3	P5
IO_L22N_T3_35_3	N2
IO_L22P_T3_35_3	P2
IO_L23N_T3_35_3	M5
IO_L23P_T3_35_3	M6
IO_L24N_T3_35_3	N5
IO_L24P_T3_35_3	P6

U7.6

Bank 13	
IO/AA9	AA9
IO/AA10	AA10
IO/AA11	AA11
IO/AA13	AA13
IO/AA14	AA14
IO/AA15	AA15
IO/AA16	AA16
IO/AB10	AB10
IO/AB11	AB11
IO/AB12	AB12
IO/AB13	AB13
IO/AB15	AB15
IO/AB16	AB16
IO/AB17	AB17
IO/T14	T14
IO/T15	T15
IO/T16	T16
IO/U15	U15
IO/U16	U16
IO/V10	V10
IO/V13	V13
IO/V14	V14
IO/V15	V15
IO/W10	W10
IO/W11	W11
IO/W12	W12
IO/W14	W14
IO/W15	W15
IO/W16	W16
IO/Y11	Y11
IO/Y12	Y12
IO/Y13	Y13
IO/Y14	Y14
IO/Y16	Y16
IO/Y17	Y17

U7.9

Bank 34	
IO_0_34_NA	T3
IO_25_34_NA	U7
IO_L1N_T0_34_0	U1
IO_L1P_T0_34_0	T1
IO_L2N_T0_34_0	V2
IO_L2P_T0_34_0	U2
IO_L3N_T0_DQS_34_0	R2
IO_L3P_T0_DQS_34_0	R3
IO_L4N_T0_34_0	Y2
IO_L4P_T0_34_0	W2
IO_L5N_T0_34_0	Y1
IO_L5P_T0_34_0	W1
IO_L6N_T0_VREF_34_0	V3
IO_L6P_T0_34_0	U3
IO_L7N_T1_34_1	AB1
IO_L7P_T1_34_1	AA1
IO_L8N_T1_34_1	AB2
IO_L8P_T1_34_1	AB3
IO_L9N_T1_DQS_34_1	AA3
IO_L9P_T1_DQS_34_1	Y3
IO_L10N_T1_34_1	AB5
IO_L10P_T1_34_1	AA5
IO_L11N_T1_SRCC_34_1	AA4
IO_L11P_T1_SRCC_34_1	Y4
IO_L12N_T1_MRCC_34_1	W4
IO_L12P_T1_MRCC_34_1	V4
IO_L13N_T2_MRCC_34_2	T4
IO_L13P_T2_MRCC_34_2	R4
IO_L14N_T2_SRCC_34_2	U5
IO_L14P_T2_SRCC_34_2	T5
IO_L15N_T2_DQS_34_2	W5
IO_L15P_T2_DQS_34_2	W6
IO_L16N_T2_34_2	V5
IO_L16P_T2_34_2	U6
IO_L17P_T2_34_2	R6
IO_L18N_T2_34_2	AA6
IO_L18P_T2_34_2	Y6
IO_L19N_T3_VREF_34_3	W7
IO_L19P_T3_34_3	V7
IO_L20N_T3_34_3	AB6
IO_L20P_T3_34_3	AB7
IO_L21N_T3_DQS_34_3	V8
IO_L21P_T3_DQS_34_3	V9
IO_L22N_T3_34_3	AB8
IO_L22P_T3_34_3	AA8
IO_L23N_T3_34_3	Y7
IO_L23P_T3_34_3	Y8
IO_L24N_T3_34_3	Y9
IO_L24P_T3_34_3	W9

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FPGA-IO		
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