BLOCK NAME

Verification Plan Author: Katrin Nekhin - 318647906 Yuval Saar - 304999261

Revision History

Rev	Description	Done By	Date
1.0	First Draft	Katrin Nekhin	

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1. GENERAL

The purpose of this document is to describe the Verification Plan of the ENC_DEC_ECC device, including all of its sub blocks and components, including DUT description, Testbench diagram, detailed mechanism description as well as coverage and checkers tables.

The Verification Plan will especially try to address the following guidelines:

- Allow as much as possible Test / Pattern generation to be re-usable from platform to platform
- Verification will be coverage driven, as to allow smart & fast bring up as well as allow visibility & measurability of the verification status

2. BLOCK DESCRIPTION

2.1. DUT High Level Description

ECC_ENC_DEC is a device with the capabilities of encoding and decoding data.

- Features:
 - o Encode Operation
 - o Decode Operation
 - o Full Channel Operation (encode and decode with added noise)

2.2. DUT Block Diagram

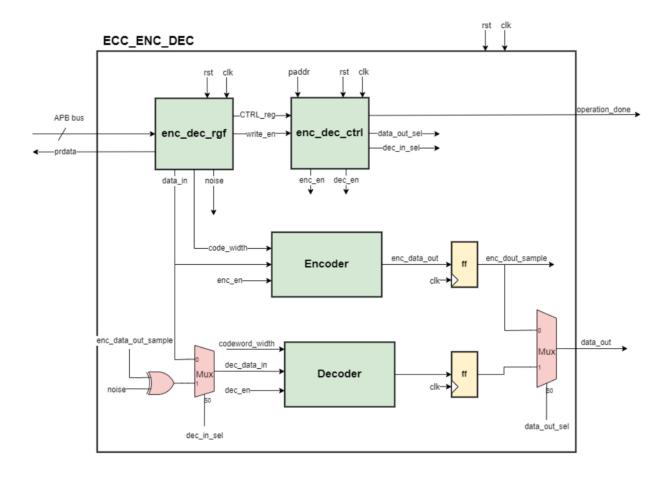


Figure 1: Block Diagram

2.3. Interface

Interface Parameters:

- AMBA_ADDR_WIDTH
 - Width of the APB address lines
 - o Default value 32
- AMBA WORD
 - Width of the data being sent by APB
 - o Default value 32
- DATA WIDTH
 - Width of a codeword
 - o Default value 32

Interface Signals:

- operation_done
 - o 1 bit
 - Goes from the DUT to the checker
- data out
 - o 8/16/32 bit
 - Goes from the DUT to the checker
- Clk. rst
 - o 1 bit each
 - o Goes into every sub-module
- Paddr, pwdata, penable, psel, pwrite
 - o APB input signals, with different sizes
 - o Goes from stimulus into the DUT
- Prdata
 - o APB output signals, with size AMBA_WORD
 - O Goes from the DUT to the checker
- Num of errors
 - o 2 bit
 - O Goes from the DUT to the checker
- Type_of_work
 - 2 bit
 - O Goes from the stimulus to the checker
 - o Indicates what mode is being used (encode, decode, full channel)

3. TESTBENCH

3.1. Testbench Concept

The TB handles the following main tasks:

- 1. Data
 - a. For every work mode (Encode/Decode/Full), randomize 10 data inputs and their noise inputs (if in full channel, distributed equally between 0, 1 or 2-bit noise).
- 2. Configuration
 - a. Given every data input, configure the registers via APB.
 - b. Send the data input to the golden model.
 - c. The DUT handles the input and sends the output to the checker.
- 3. Checker:
 - a. Compare the output with that of the golden model.
 - b. Write report.
- 4. Reset event

3.2. Testbench Diagram

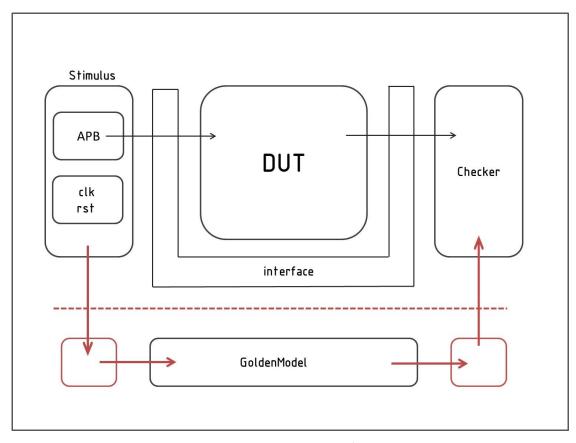


Figure 2: TestBench Active Diagram

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3.3. TB Blocks detailed description

3.3.1. Stimulus

Upon initialization the stimulus generates a list of input data vectors using randomization. The list is composed of 10 vectors for each mode.

For full channel mode also the noise register vector is randomized between 0, 1 and 2 bit error vectors.

The stimulus then writes the list to a file for the golden model to read, waits for a while for the golden model to finish calculation and then starts sending the vectors to the DUT, one input vector at a time. Once all of them are sent the stimulus is terminated.

3.3.2. Checker

The checker waits for the golden model to finish calculations and then reads from the output file, one output at a time.

It then compares the output read to the output received from the DUT and writes the result to a report file.

Checker Table:

Condition	Event	Expected Result	Scenario
		data_out == 0 &&	
rst == 1	rst	num_of_errors == 0	Standard
type_of_work == 1 && operation_done ==			
1	Encode Operation	data_out == encoded data_in	Standard
type_of_work == 2 && operation_done ==	Decode Operation	data_out == decoded data_in	
1 && stim_num_of_errors == 0	No Noise	&& num_of_errors == 0	Standard
type_of_work == 2 && operation_done ==	Decode Operation	data_out == decoded data_in	
1 && stim_num_of_errors == 1	With 1-bit Noise	&& num_of_errors == 1	Standard
type_of_work == 2 && operation_done ==	Decode Operation		
1 && stim_num_of_errors == 2	With 2-bit Noise	num_of_errors == 2	Standard
type_of_work == 3 && operation_done ==	Full Channel		
1	Operation	data_out == data_in	Standard

3.3.3. Golden Model

The golden model is a MATLAB application which reads data vectors from a text file, performs encoding/decoding on the files according to the operation needed, and writes the results to an output file.

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3.4. Coverage

Coverage Table:

Function	Event	Coverage Point	Bins	Scenario
Reset	rst	rst	0,1	Standard
Control	posedge clock	ctrl	0:2	Standard
Input data	posedge clock	data_in	0,[1:255],[256:65535],[65536:\$]	Standard
CODEWORD_WIDTH	posedge clock	codeword_width	0:2	Standard
Add noise	posedge clock	add_noise	0,1	Standard
Num of errors	posedge clock	err_num	0,1,2	Extreme

3.5. Resets & clocks

3.5.1. Reset

- rst_n
 - $\circ \quad \text{Active low "cold_reset", synchronized to apb_clk, resets registers (and datapath?)}.$

3.5.2. Clocks

- APB clock
 - o "always on"
 - Operational clock
 - o Register configuration

3.6. Pass / Fail mechanism

Simulation will fail if one of the following events will occur:

- Data does not match expected data Either by checker compare or by post run script
- Assertions fail

3.7. Start / End test mechanisms

- Start of test will be out of reset
- End of test will be one of:

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- No more transactions
- o Error
- o Timeout by built in TB timeout