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Traductores de Lenguajes I

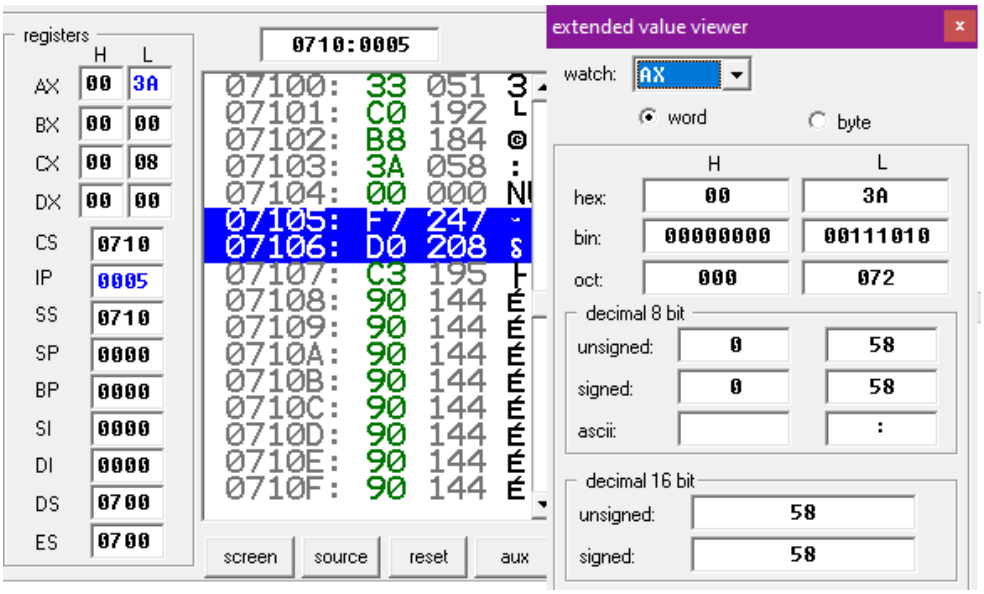
I7025 D05

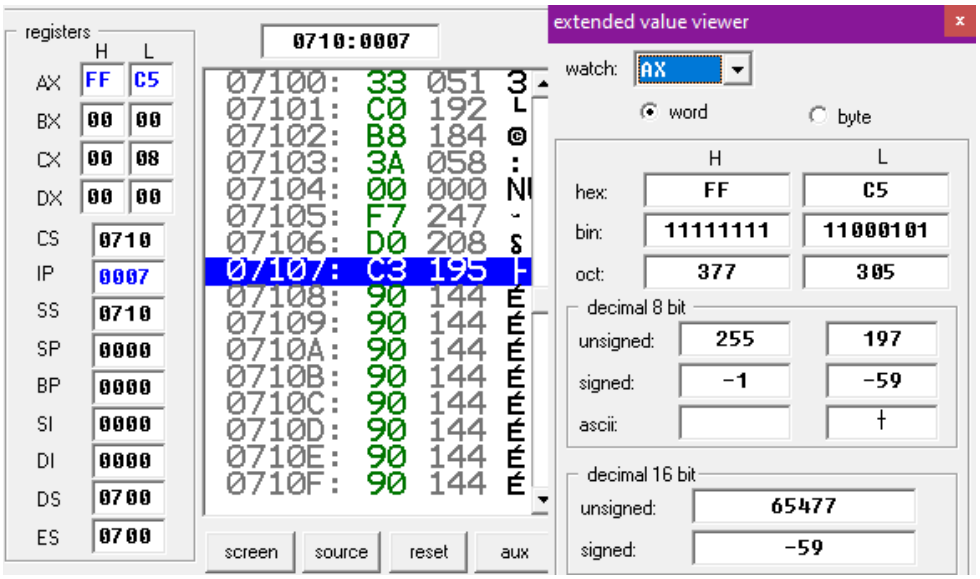
Actividad 2

Uso de las instrucciones lógicas

NOT

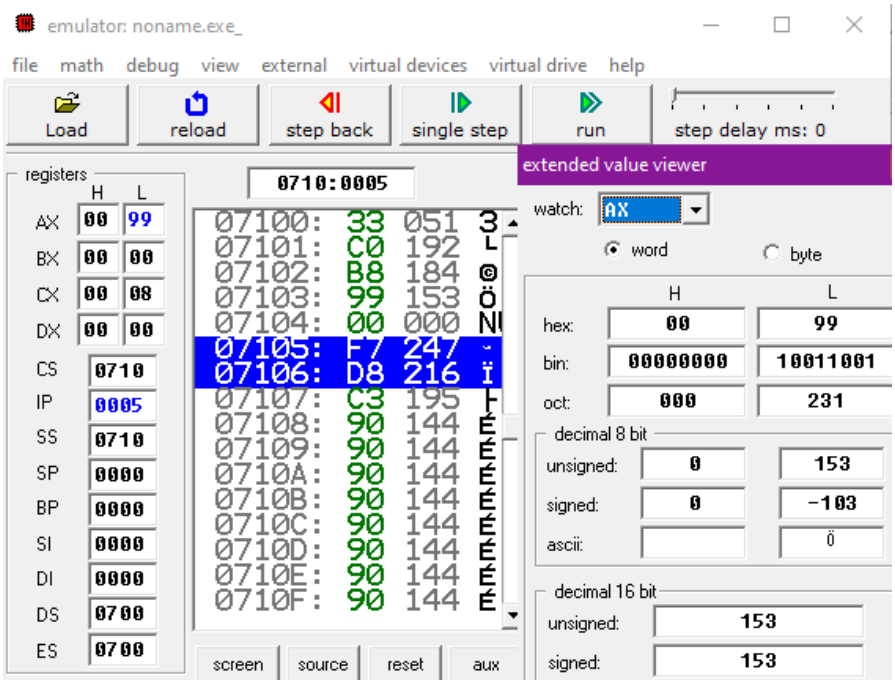
```
01 ;Programa Intruccion NOT
02 .model small
03
04      .code
05      xor ax,ax          ;limpiar regitro AX
06      mov ax,3Ah         ;AX = 0011 1010
07      not ax             ;AX = 1100 0101 = FFC5
08
09 ret
```

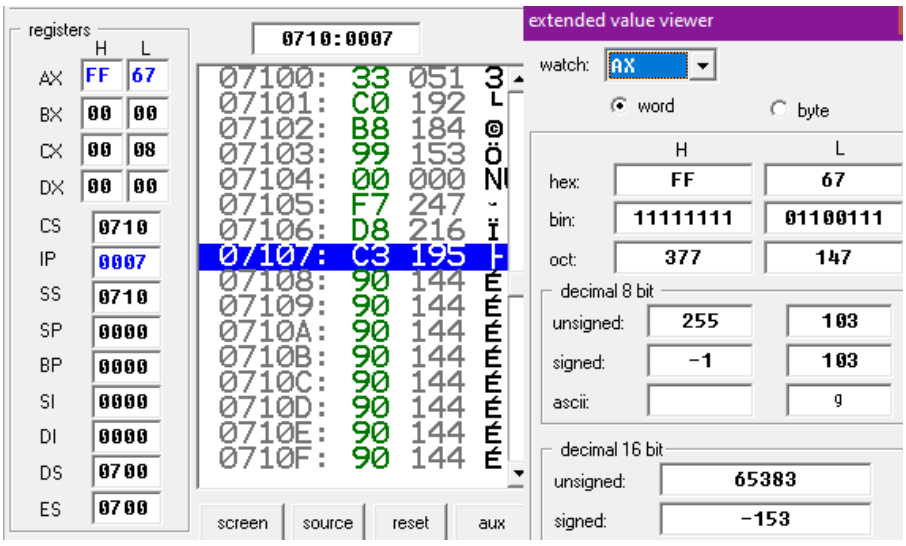




NEG

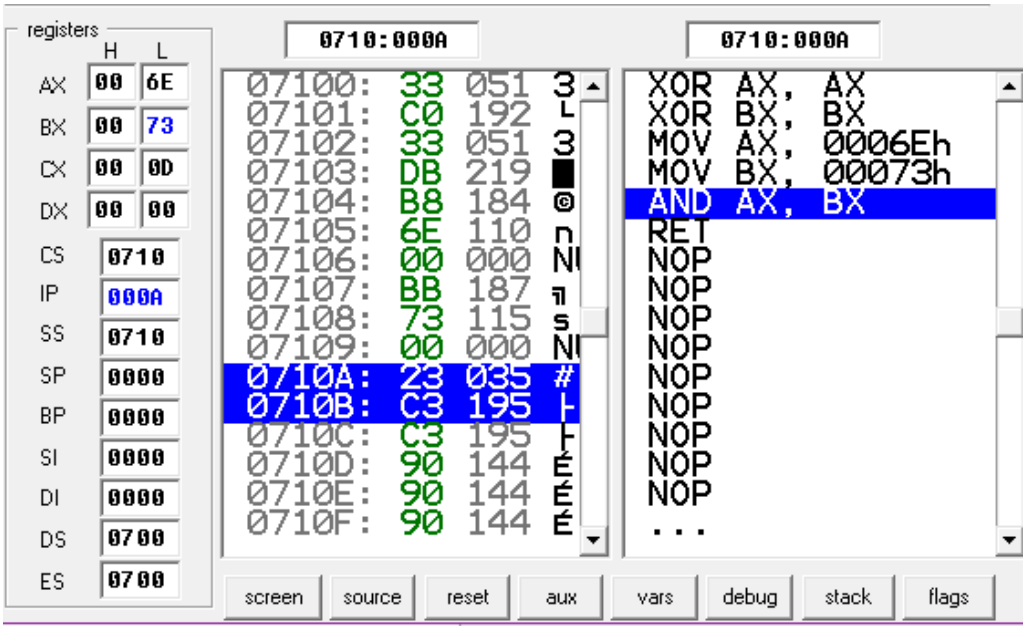
```
01 ;Programa Intruccion NEG
02 .model small
03
04      .code
05      xor ax,ax          ;limpiar regitro AX
06      mov ax,99h         ;AX = 1001 1001
07      neg ax             ;AX = 0110 0111 = FF67
08
09 ret
```

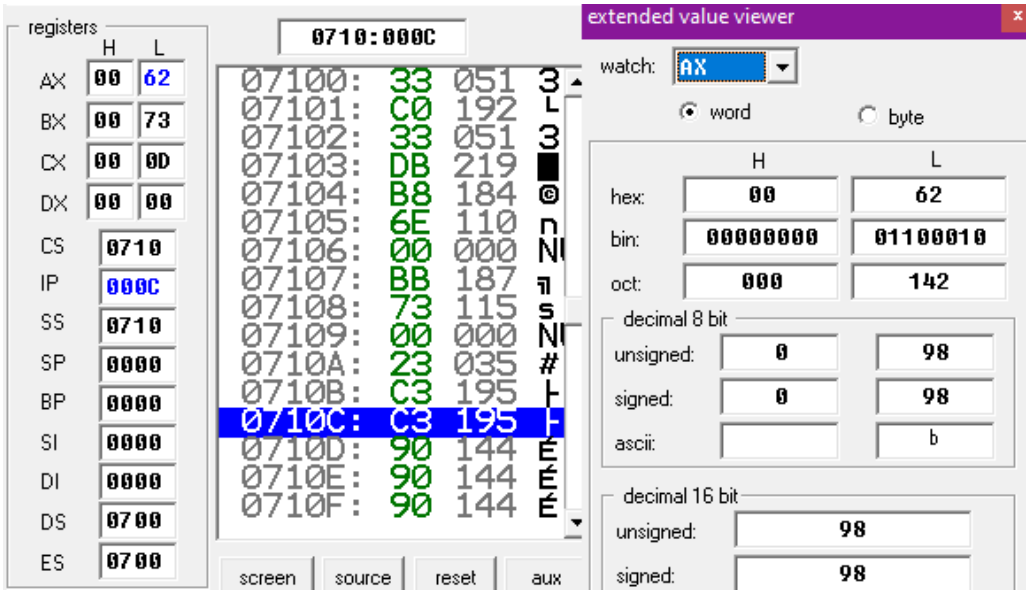




AND

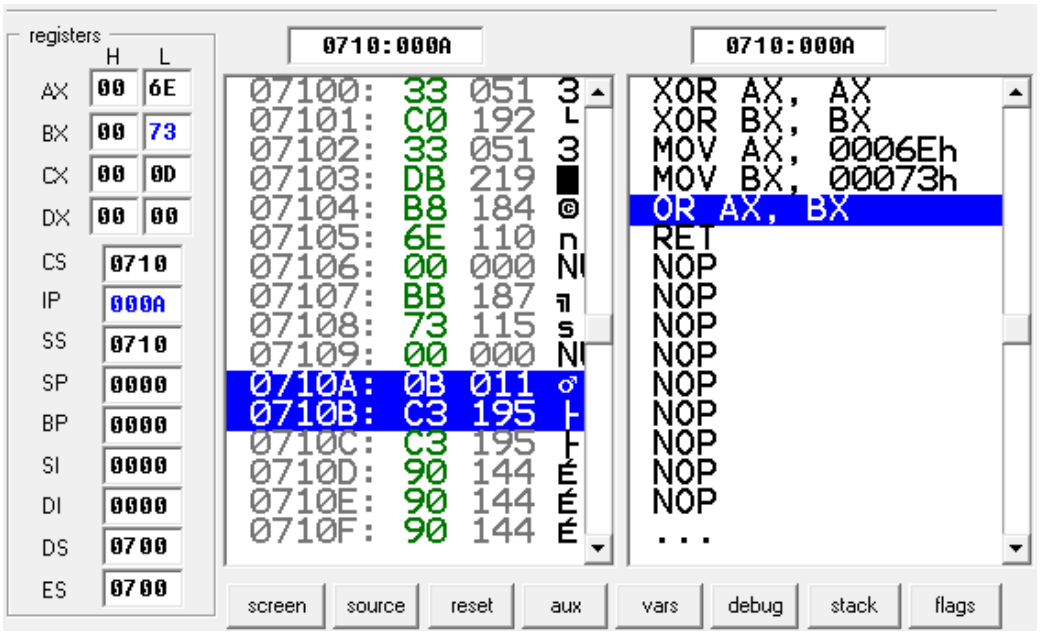
```
01 ;Programa Intruccion AND
02 .model small
03      .code
04      xor ax,ax          ;limpiar regitro AX
05      xor bx,bx          ;limpiar regitro BX
06      mov ax,6Eh         ;AX = 0110 1110
07      mov bx,73h         ;AX = 0111 0011
08      and ax,bx          ;AX = 0110 0010 = 62h
09 ret
```

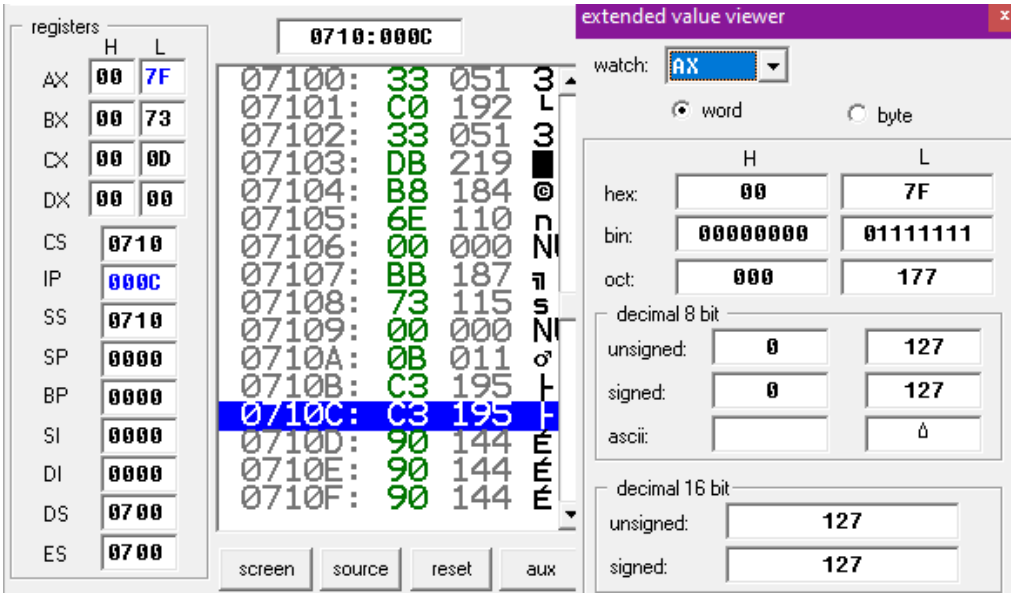




OR

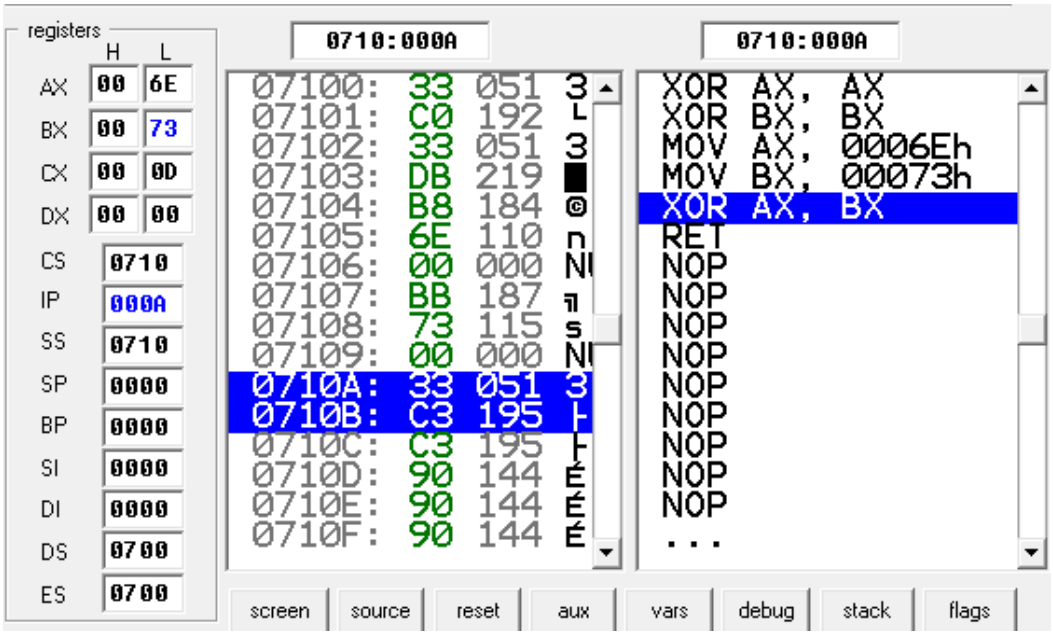
```
01 ;Programa Intruccion OR
02 .model small
03      .code
04      xor ax,ax          ;limpiar regitro AX
05      xor bx,bx          ;limpiar regitro BX
06      mov ax,6Eh         ;AX = 0110 1110
07      mov bx,73h         ;AX = 0111 0011
08      or  ax,bx          ;AX = 0111 1111 = 7Fh
09 ret
```

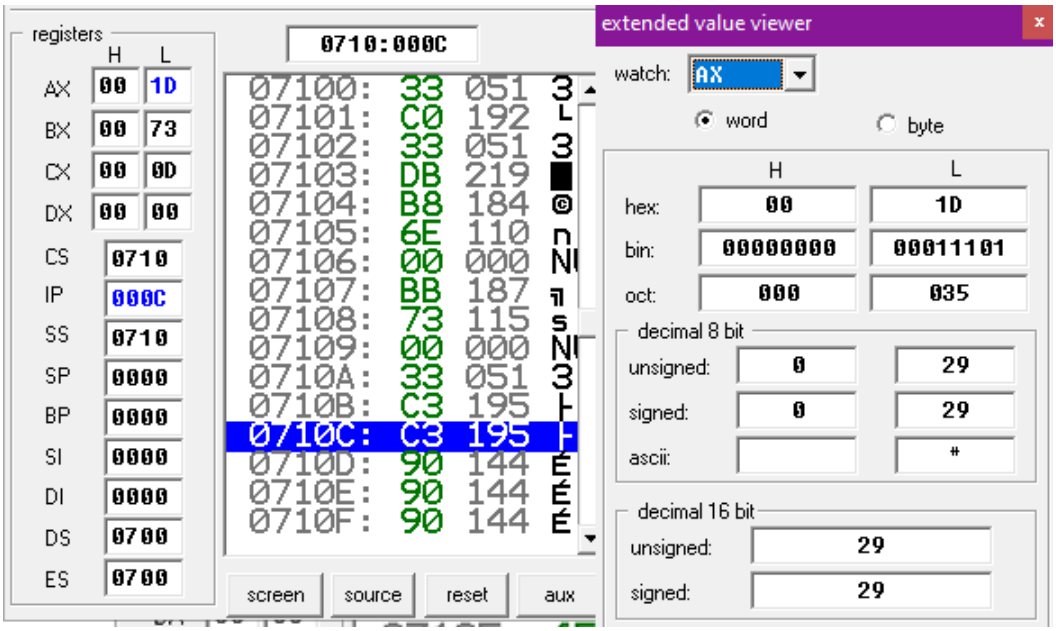




XOR

```
01 ;Programa Intruccion XOR
02 .model small
03      .code
04      xor ax,ax          ;limpiar regitro AX
05      xor bx,bx          ;limpiar regitro BX
06      mov ax,6Eh         ;AX = 0110 1110
07      mov bx,73h         ;AX = 0111 0011
08      xor ax,bx          ;AX = 0001 1101 = 1Dh
09 ret
```





Conclusiones

Esta no fue mi primera vez programando en ensamblador, pero si en la arquitectura del 8086 aun así me pareció una excelente introducción al tema y aunque solo sean operaciones lógicas simples no dejo de pensar en las infinitas maneras en las que las podría implementar en futuros programas.

Biografía

- Microprocesadores de Intel, séptima edición de Barry B. Brey.