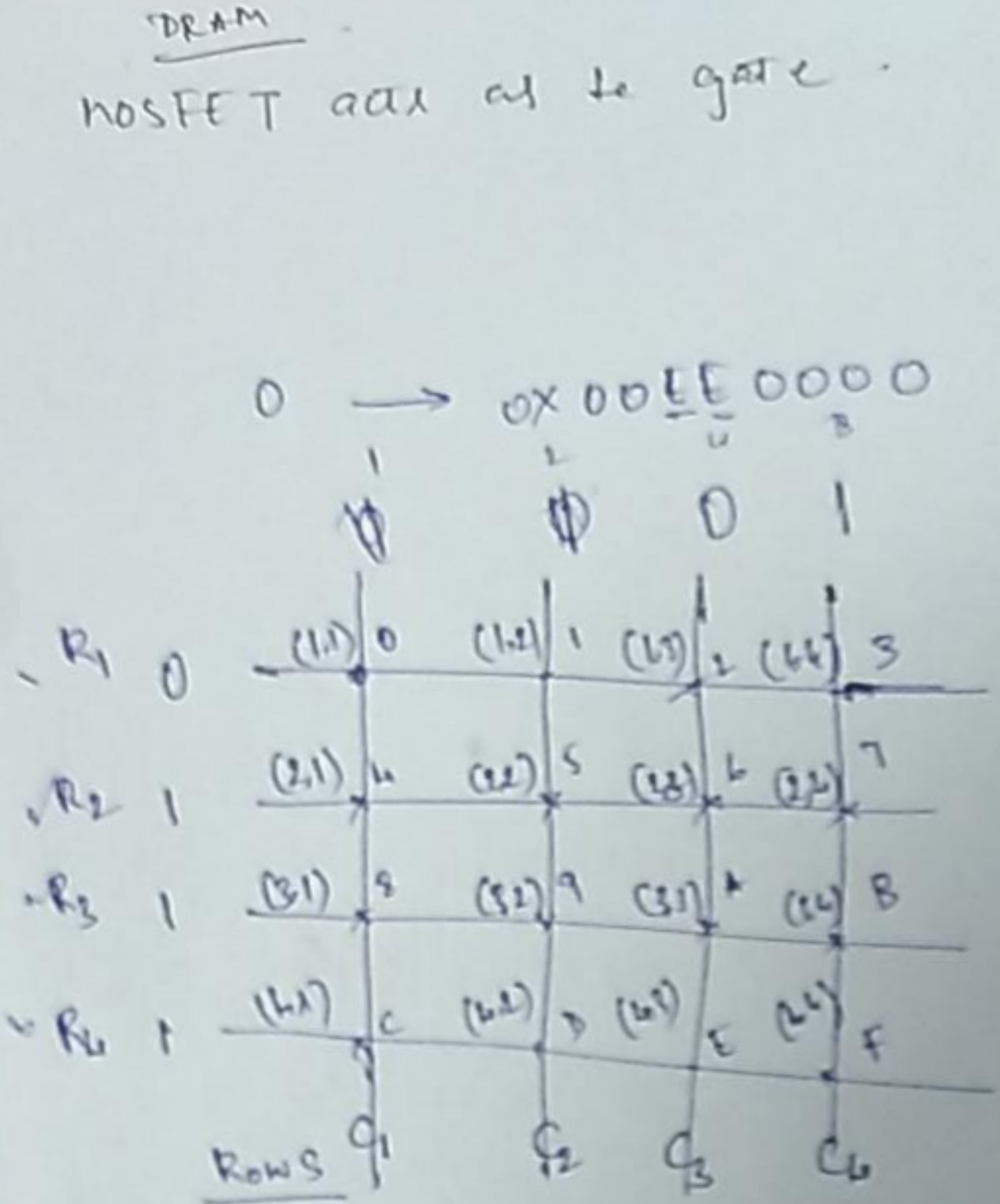
DRAM

MOSFET acts as the gate.

8 4 2 1
1 1 1 0
1 1 1 0

0 → 0X00EE0000
1 2 3



R C
EE (1,1) – 1
E7 (1,2) – 2
EB (1,3) – 3
E7 (1,4) – 4

Rows
8 4 2 1
R4 R3 R2 R1
1 1 1 0 = R1 = E
1 1 0 1 = R2 = D
1 1 0 1 = R3 = B
0 1 1 1 = R4 = 7

Columns
8 4 2 1
C4 C3 C2 C1
1 1 1 0 = C1 = E
1 1 0 1 = C2 = D
1 0 1 1 = C3 = B
0 1 1 1 = C4 = 7

0 = (R1, C1) = (E, E)
1 = (R1, C2) = (E, D)
2 = (R1, C3) = (E, B)
3 = (R1, C4) = (E, 7)
4 = (R2, C1) = (D, E)
5 = (R2, C2) = (D, D)
6 = (R2, C3) = (D, B)
7 = (R2, C4) = (D, 7)
8 = (R3, C1) = (B, E)
9 = (R3, C2) = (B, D)
10 = (R3, C4) = (B, B)
11 = (R3, C4) = (B, 7)