Time

clk=1

Memory Clock=1

rst=1

ALU

ex_alu_port_a[31:0]=00000000

ex_alu_port_b[31:0]=00000000

ex_alu_operation[4:0]=00

ex_stall=0

ex_flush=0

ex_request_stall=0

ex_alu_result[31:0]=00000000

ex_b_is_zero=1

exc_overflow=0

hilo[63:0]=0000000000000000

Multiplier

input_a[31:0]=00000000

input_b[31:0]=00000000

signed_operation=0

enable_op=0

stall=0

flush=0

mult_result[63:0]=xxxxxxxxxxxxxxxx

active=0

active0=0

active1=0

active2=0

ready=0

Divider

op_divs=0

op_divu=0

dividend[31:0]=00000000

divisor[31:0]=00000000

quotient[31:0]=00000000

remainder[31:0]=00000000

stall=0