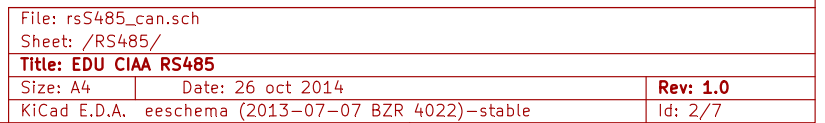
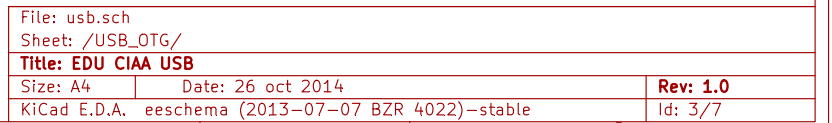




RS485





Conectores de expansión LVTTL.  
En formato de pines, 2.54mm de pitch.

RESET  
ISP  
ANALOG\_GND  
ADC0\_3  
ADC0\_2  
ADC0\_1  
DAC  
VDD\_AD  
I2C\_SDA  
I2C\_SCL  
RS232\_RXD  
RS232\_TXD  
CAN\_RD  
CAN\_TD  
TEC\_COL1  
TEC\_F0  
TEC\_F3  
TEC\_F2  
TEC\_COL0

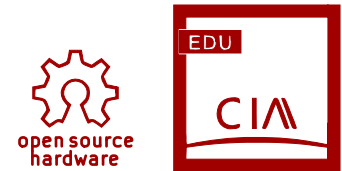
GPIO\_GND  
WAKEUP  
ANALOG\_GND  
ANALOG\_GND  
ANALOG\_GND  
ANALOG\_GND  
ANALOG\_GND  
GPIO\_GND  
GPIO\_GND  
GPIO\_GND  
GPIO\_GND  
GPIO\_GND  
TEC\_COL2  
TEC\_F1  
GPIO\_GND  
GPIO\_GND

GPIO\_GND  
GPIO\_GND  
GPIO\_GND  
ENET\_RXD0  
GPIO\_GND  
GPIO\_GND  
ENET\_REF\_CLK  
GPIO\_GND  
GPIO\_GND  
SPL\_MOSI  
LCD\_END  
GPIO\_GND  
GPIO\_GND  
GPIO0  
GPIO2  
GPIO4  
GPIO6  
GPIO\_GND  
GPIO\_GND  
GPIO\_GND

ENET\_RXD1  
ENET\_TX\_EN  
ENET\_MDC  
ENET\_CRS\_DV  
ENET\_MDIO  
ENET\_TXD0  
ENET\_TXD1  
SPL\_MISO  
SPL\_SCK  
LCD4  
LCD\_RS  
LCD3  
LCD2  
LCD1  
GPIO1  
GPIO3  
GPIO5  
GPIO7  
GPIO8

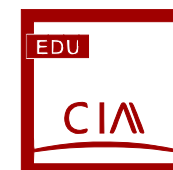
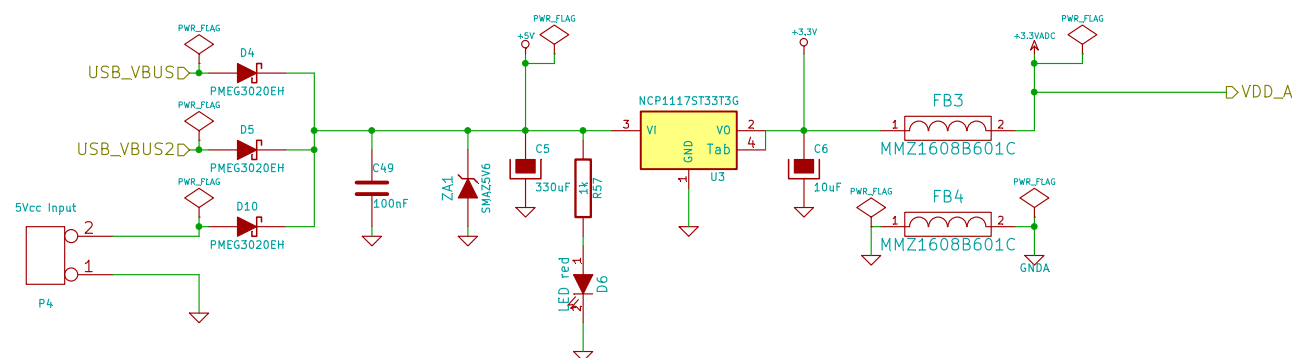
ANALOG\_GND  
GPIO\_GND

File: gpio.sch  
Sheet: /GPIO/  
Title: EDU CIAA GPIO  
Size: A4 Date: 26 oct 2014 Rev: 1.0  
KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable Id: 4/7

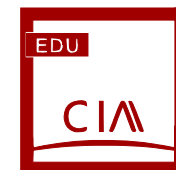
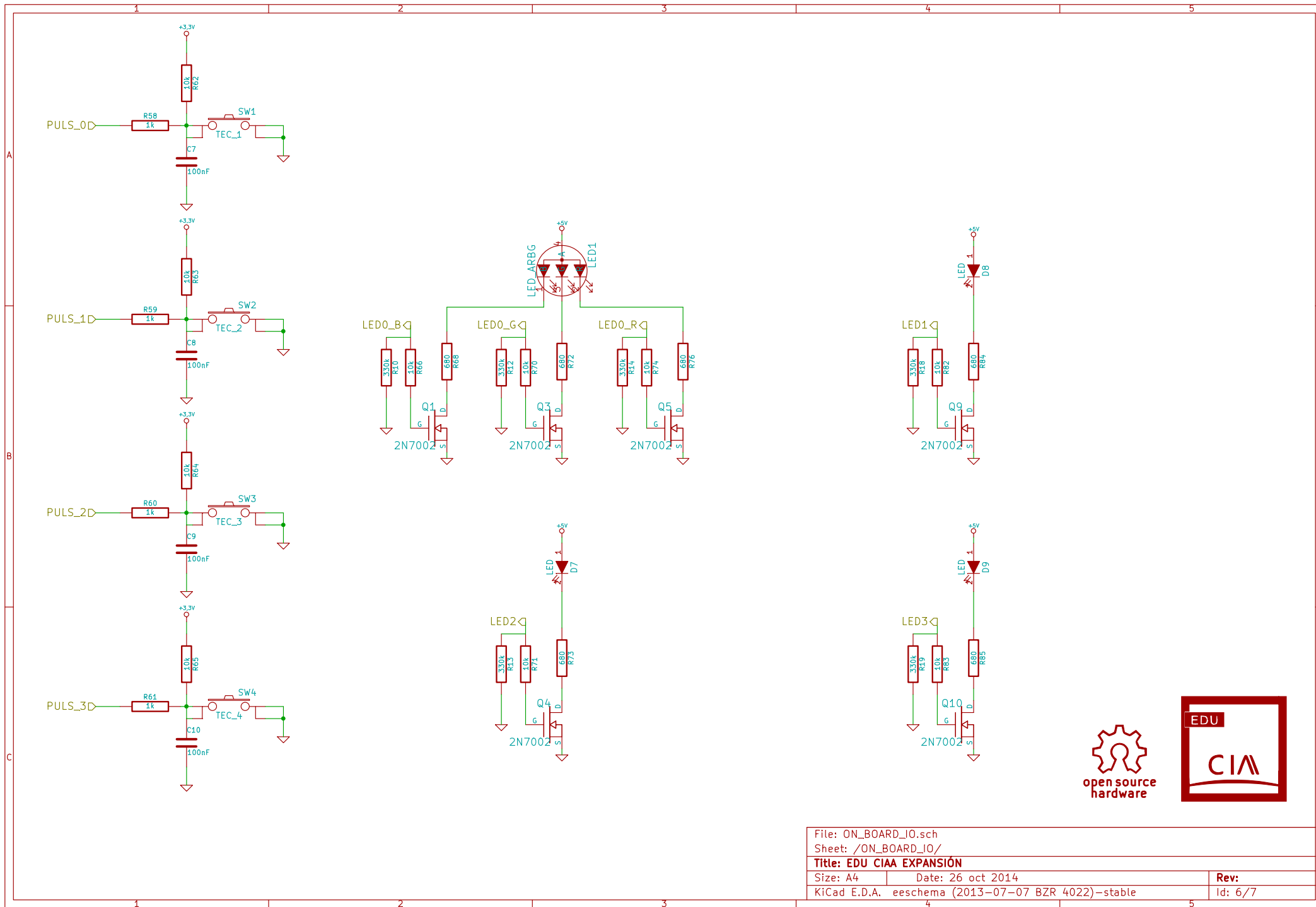


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<b>Title: EDU CIAA GPIO</b>	
Size: A4	Date: 26 oct 2014
KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable	Rev: 1.0
Id: 4/7	

Fuente de alimentación a través de los puertos USB  
o de la entrada de 5V  
Tensiones de salida: 5V y 3.3V



File: fuente.sch		
Sheet: /Fuente/		
<b>Title: EDU CIAA Fuente de alimentación</b>		
Size: A4	Date: 26 oct 2014	Rev: 1.0
KiCad E.D.A. eschema (2013-07-07 BZR 4022)-stable		Id: 5/7



File: ON_BOARD_IO.sch		
Sheet: /ON_BOARD_IO/		
Title: EDU CIAA EXPANSIÓN		
Size: A4	Date: 26 oct 2014	Rev:
KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable		Id: 6/7

