HZ0805E601R-10

PHYSICAL DIMENSIONS:

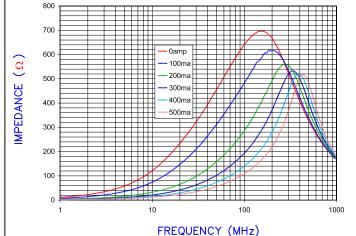
A 2.00 [.079] ± 0.20 [.008]

B 1.25 [.049] ± 0.20 [.008]

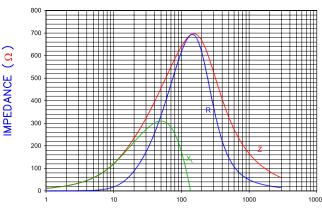
C 0.90 [.035] ± 0.20 [.008]

D 0.51 [.020] ± 0.25 [.010]

Z vs FREQUENCY IMPEDANCE UNDER DC BIAS

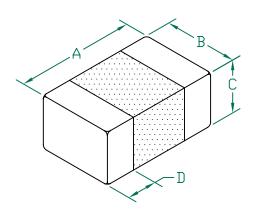


|Z| , R, AND X vs. FREQUENCY



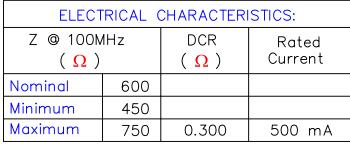
FREQUENCY (MHz)

AGILENT E4991A RF Impedance/Material Analyzer
HP 16194A Test Fixture. TEST REF. 3228



2002/95/EC

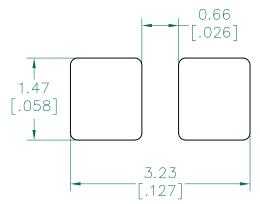
UNCONTROLLED DOCUMENT



NOTES: UNLESS OTHERWISE SPECIFIED

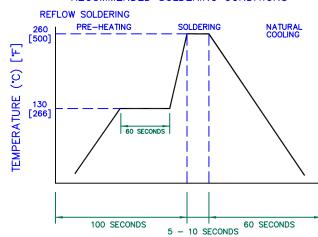
- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REEL, PAPER TAPE.
- 2. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 3. TERMINATION FINISH IS 100% TIN.

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.763 ([.030] to this dimension.)

RECOMMENDED SOLDERING CONDITIONS



DIMENSIONS ARE IN mm [INCHES].				This print is the property of Laird				
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				copies shall be made without the				
				written consent of Laird Tech. A rights to design or invention are				
				reserved.				
_	0111110F 0010F0110 TF110F01T10F	00 /07 /40		PROJECT/PART NUMBER:	REV	/ PART T	YPE:	DRAWN BY:
D	CHANGE SOLDERING TEMPERATURE	08/23/10	JUN	UZ0005E004D 40	1	D		
С	CHANGE TO PAPER TAPE	03/03/10	JUN	HZ0805E601R-10		D Co-	-FIRE JRK	
В	UPDATE COMPANY LOGO ADD ROHS	03/21/08	JRK	DATE: 03/01/04	SCALE:	NTS	SHEET:	l
Α	ORIGINAL DRAFT	03/01/04	JRK		TOOL #	NIS	2	of O
REV	DESCRIPTION	DATE	INT	HZ0805E601R-10-D	1002 #	-	2 of 2	01 2