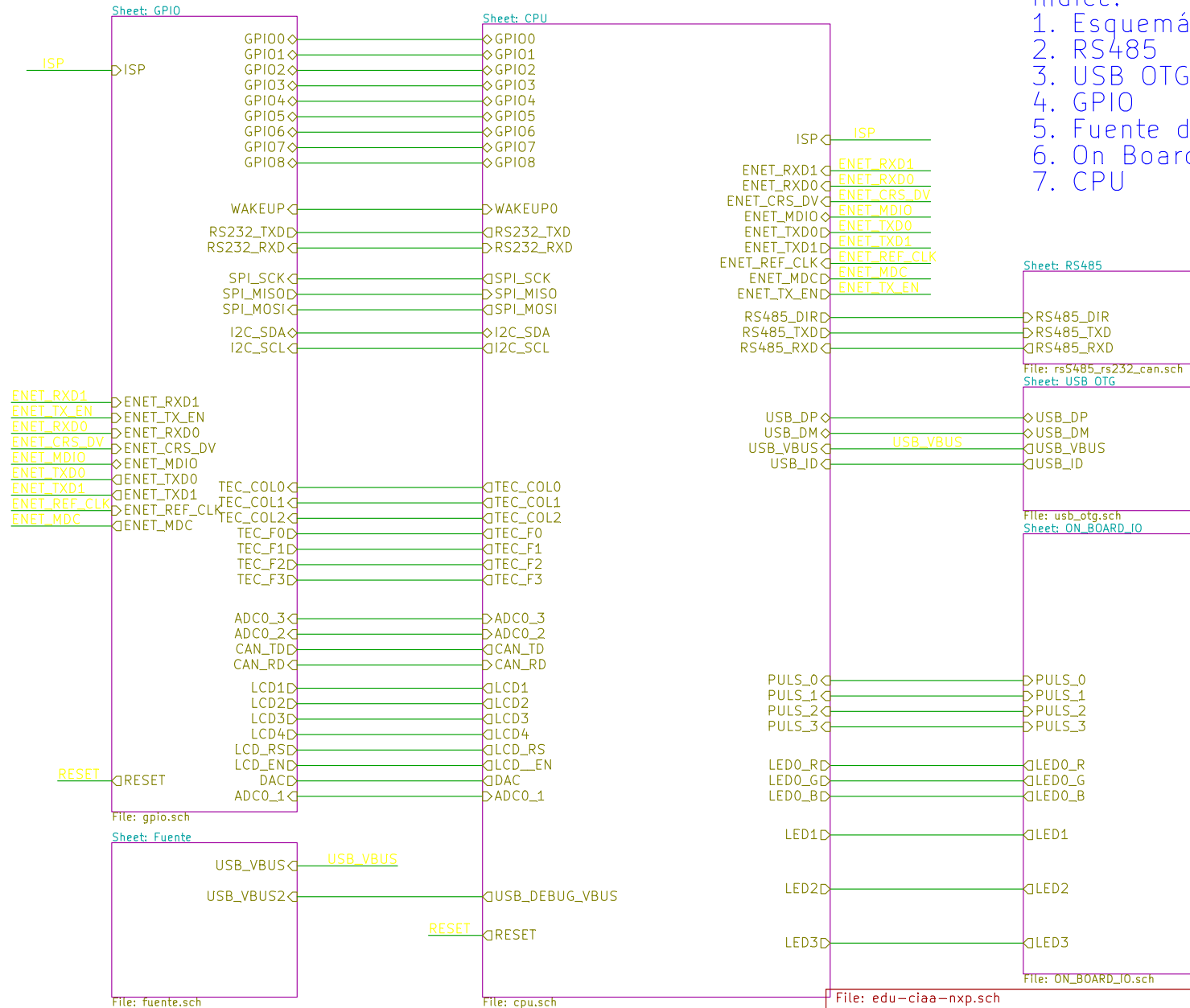


# Índice:

1. Esquemático jerárquico
2. RS485
3. USB OTG
4. GPIO
5. Fuente de alimentación
6. On Board IO
7. CPU



fiduciales

TOP BOTTOM

X F1 X F4  
 X F2 X F5  
 X F3 X F6

X H1  
 X H2  
 X H3  
 X H4



File: edu-ciaa-nxp.sch

Sheet: /

Title: EDU CIAA NXP

Size: A4 Date: 14 jul 2014

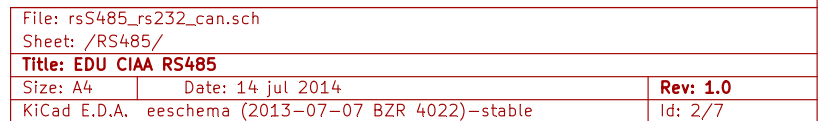
KiCad E.D.A. eschema (2013-07-07 BZR 4022)-stable

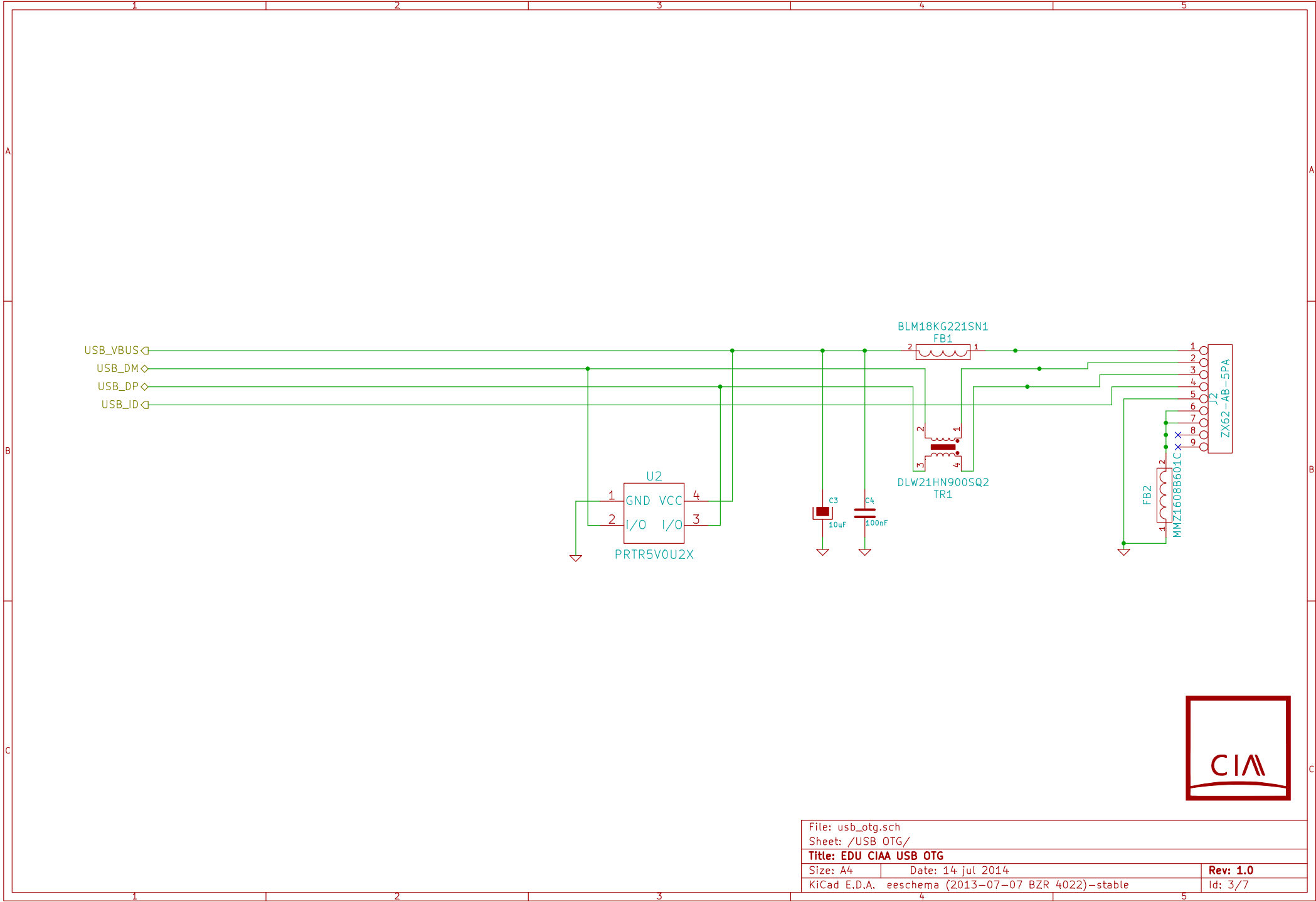
Rev: 1.0

Id: 1/7

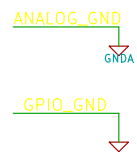
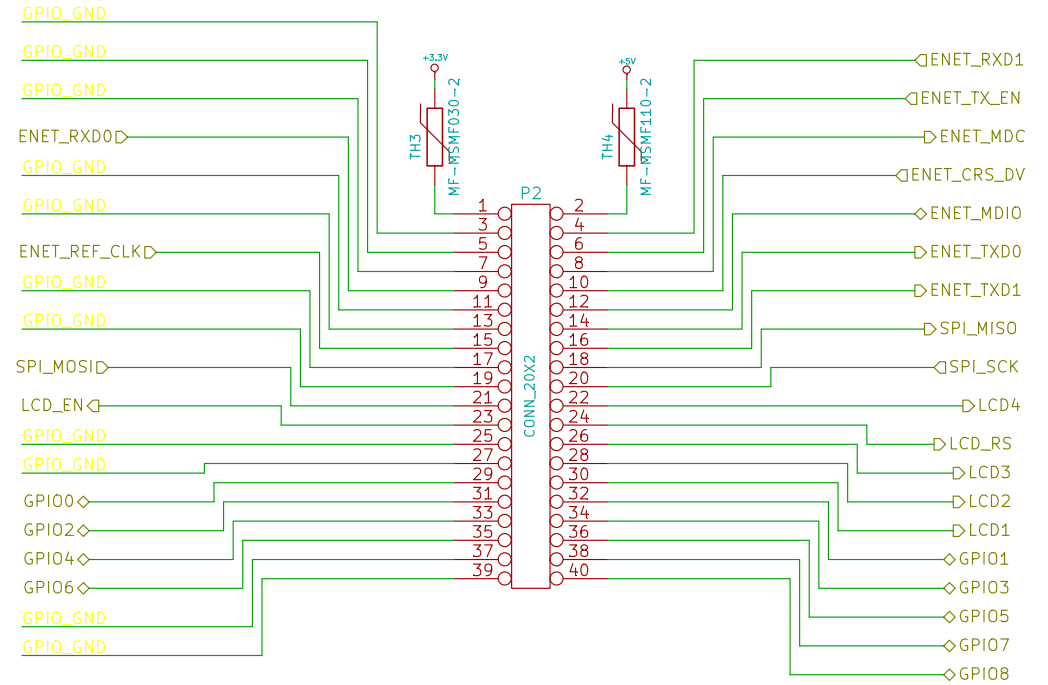
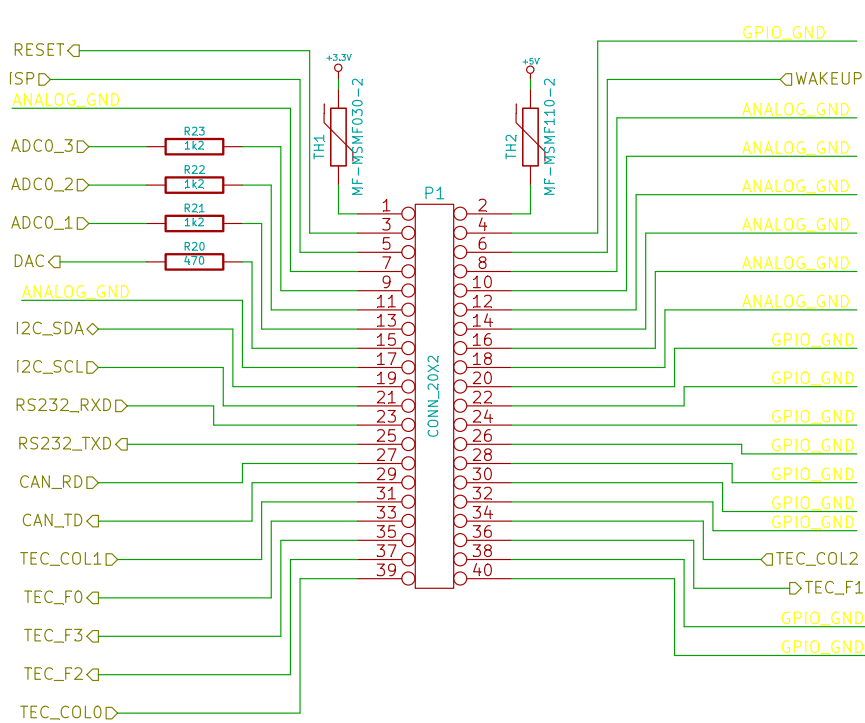


RS485



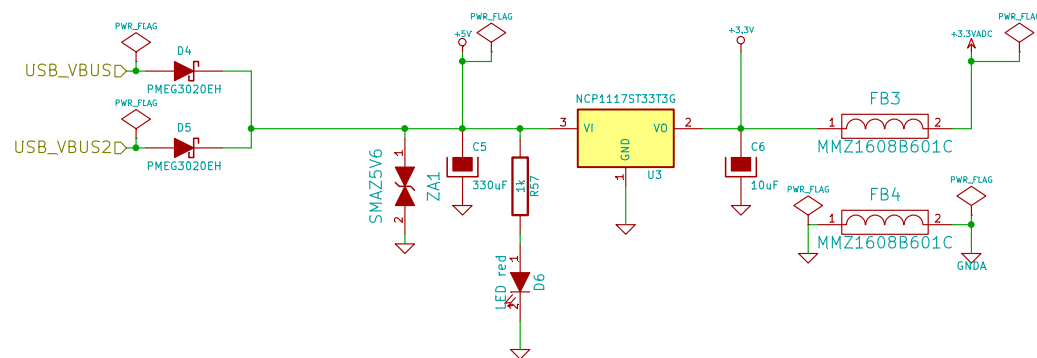


Conectores de expansión LVTTL.  
En formato de pines, 2.54mm de pitch.

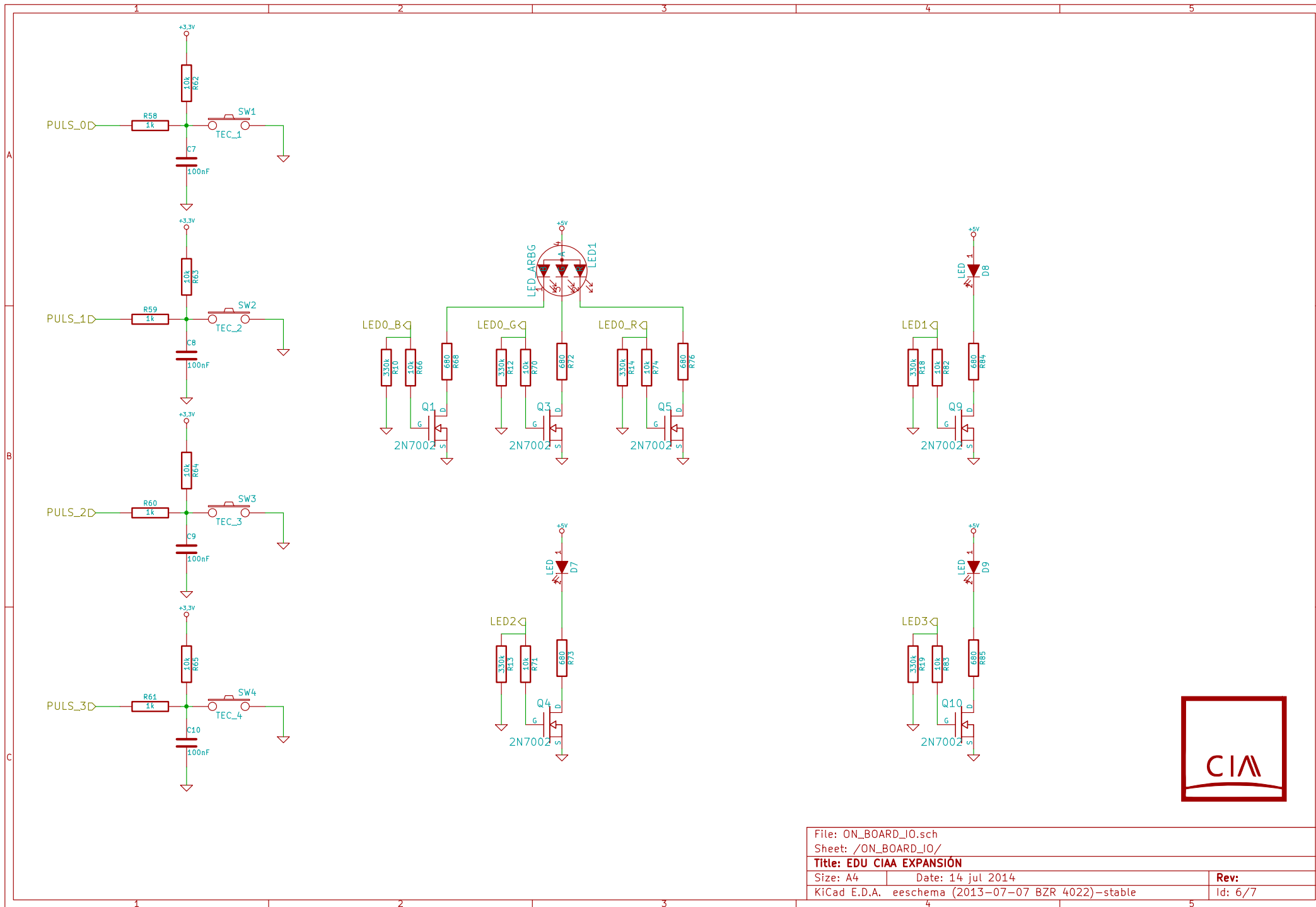


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Title: EDU CIAA GPIO		
Size: A4	Date: 14 jul 2014	Rev: 1.0
KiCad E.D.A. eschema (2013-07-07 BZR 4022)-stable		Id: 4/7

Fuente de alimentación a través del puerto USB  
Tensiones de salida: 5V y 3.3V



File: fuente.sch		
Sheet: /Fuente/		
<b>Title: EDU CIAA Fuente de alimentación</b>		
Size: A4	Date: 14 jul 2014	Rev: 1.0
KiCad E.D.A. eschema (2013-07-07 BZR 4022)-stable		Id: 5/7



File: ON_BOARD_IO.sch		
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<b>Title: EDU CIAA EXPANSIÓN</b>		
Size: A4	Date: 14 jul 2014	Rev:
KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable		Id: 6/7

