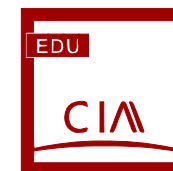


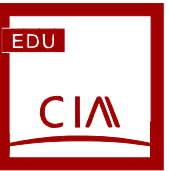
De acuerdo a Fig 27 de la AN: slla070d de TI.

JP1, JP2, JP4: cortocircuitar en caso que sea el último nodo de la red.

RS485

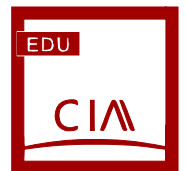
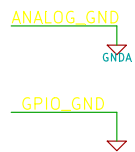
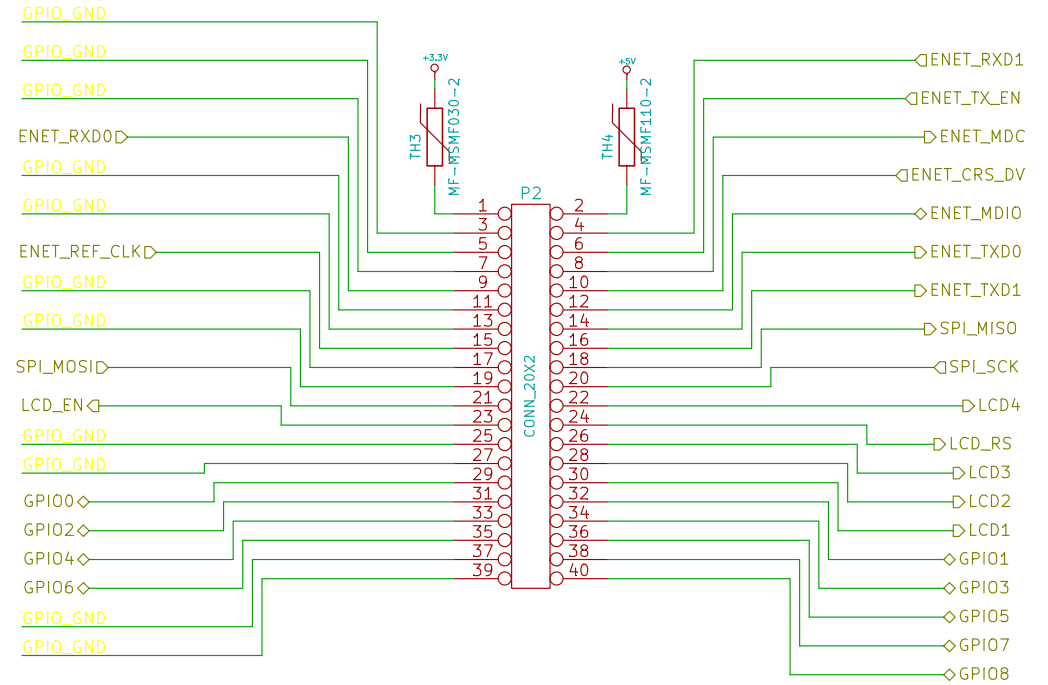
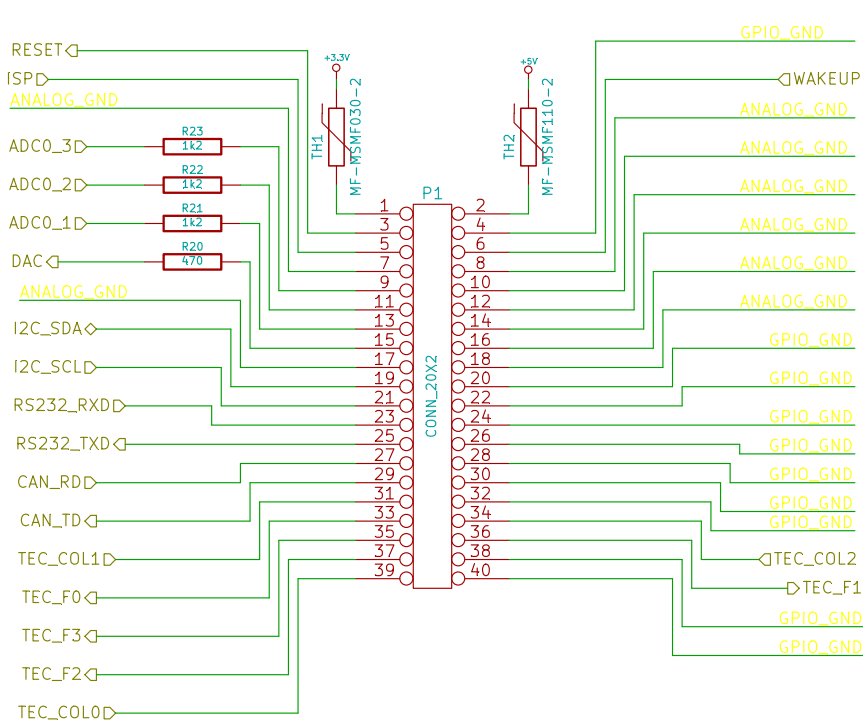


File: rsS485_rs232_can.sch		
Sheet: /RS485/		
<b>Title: EDU CIAA RS485</b>		
Size: A4	Date: 17 jul 2014	Rev: 1.0
KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable		Id: 2/7



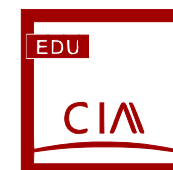
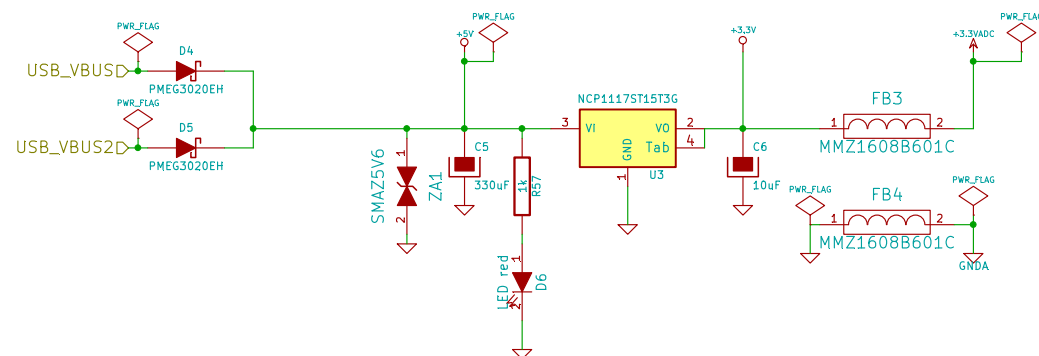
Id: 3/7

Conectores de expansión LVTTL.  
En formato de pines, 2.54mm de pitch.

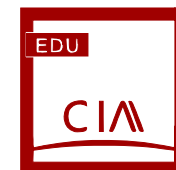
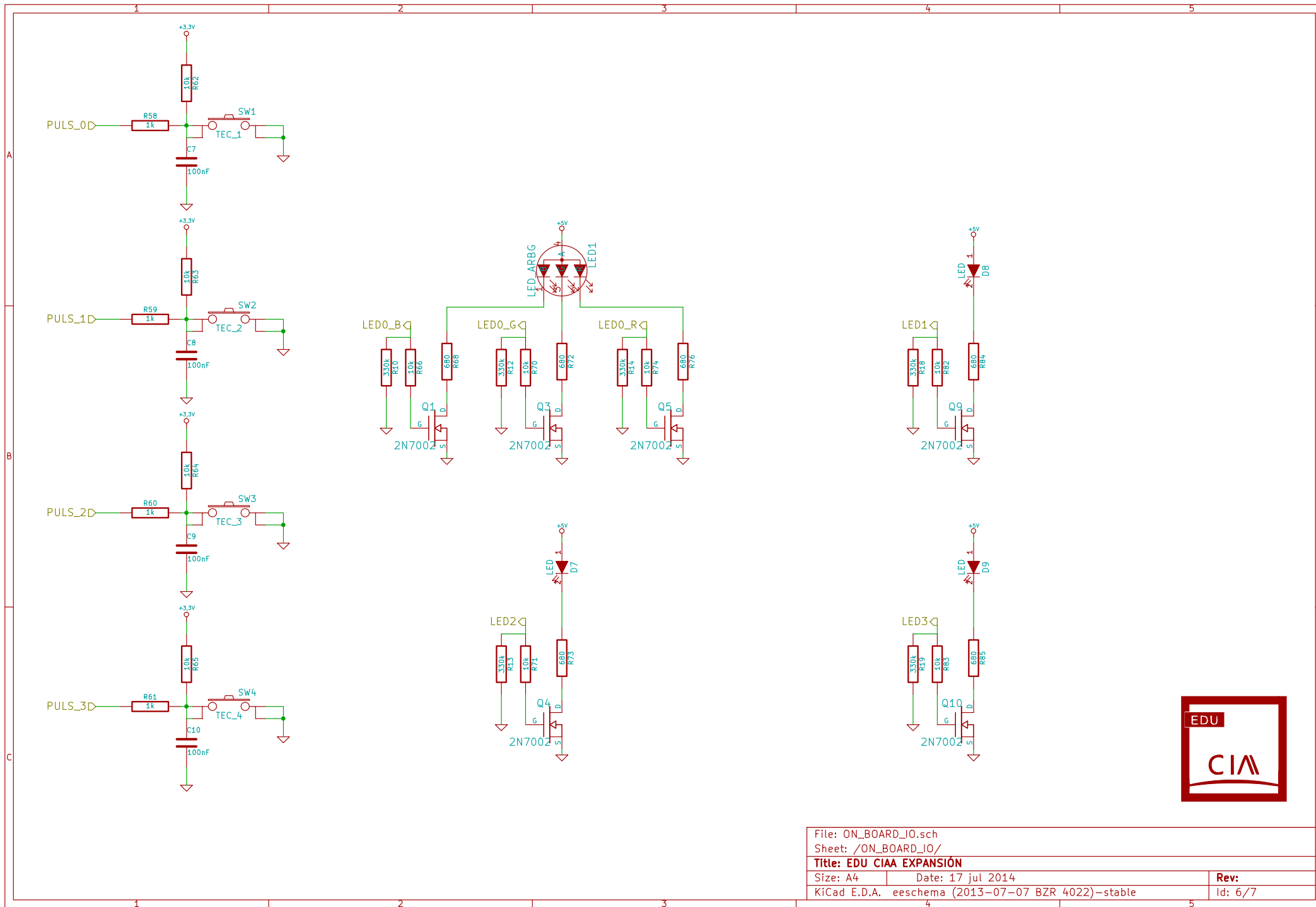


File: gpio.sch		
Sheet: /GPIO/		
Title: EDU CIAA GPIO		
Size: A4	Date: 17 jul 2014	Rev: 1.0
KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable		Id: 4/7

Fuente de alimentación a través del puerto USB  
Tensiones de salida: 5V y 3.3V



File: fuente.sch		
Sheet: /Fuente/		
<b>Title: EDU CIAA Fuente de alimentación</b>		
Size: A4	Date: 17 jul 2014	Rev: 1.0
KiCad E.D.A. eschema (2013-07-07 BZR 4022)-stable		Id: 5/7



File: ON_BOARD_IO.sch		
Sheet: /ON_BOARD_IO/		
Title: EDU CIAA EXPANSIÓN		
Size: A4	Date: 17 jul 2014	Rev:
KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable		Id: 6/7

