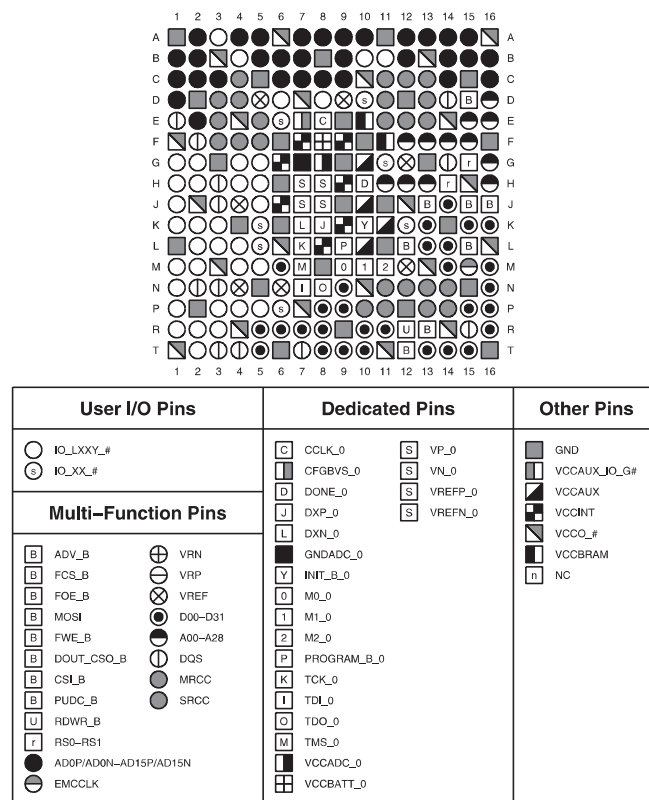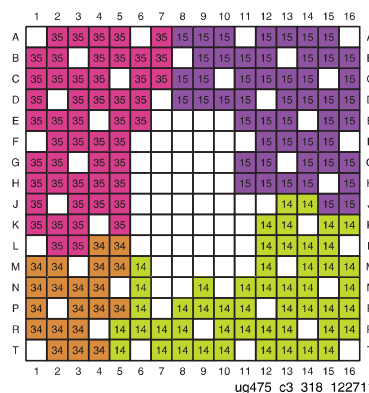## FT/FTG256 Package—XC7A15T, XC7A35T, XC7A50T, XC7A75T, and XC7A100T

ug475_c3_317_122711

Figure 3-13: **FT/FTG256 Package—XC7A15T, XC7A35T, XC7A50T, XC7A75T, and XC7A100T Pinout Diagram**

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Figure 3-14: **FT/FTG256 Package—XC7A15T, XC7A35T, XC7A50T, XC7A75T, and XC7A100T I/O Banks**
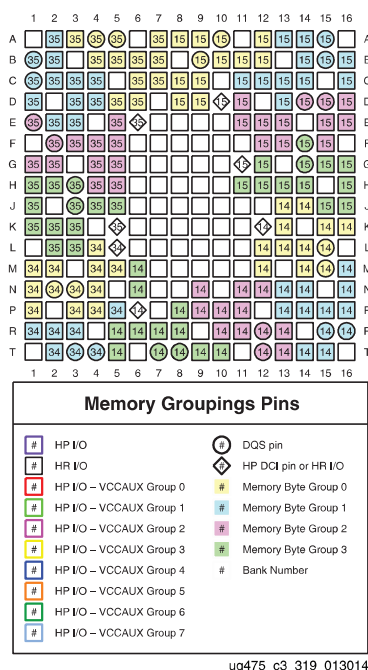
ug475_c3_319_013014

Figure 3-15: **FT/FTG256 Package—XC7A15T, XC7A35T, XC7A50T, XC7A75T, and XC7A100T Memory Groupings**
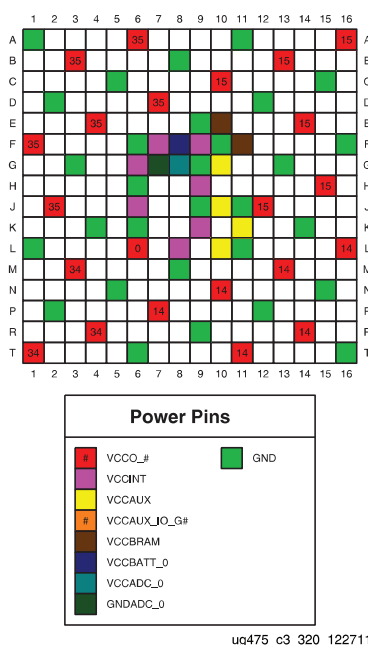

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Figure 3-16: **FT/FTG256 Package—XC7A15T, XC7A35T, XC7A50T, XC7A75T, and XC7A100T Power and GND Placement**