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6.1 Memory Management

As seen in the previous lecture there might be systems providing NUMA differently from Uniform Memory Access (UMA). Linux handles both cases similarly since UMA can be seen as a degenerative NUMA system having just one node.

Each node is described by struct pglist_data (typedefined to pg_data_t) even if the architecture is UMA. All the nodes structs are linked together forming a linked list called pgdat_list. In the UMA case only one pg_data_t structure called contig_page_data is used. From the kernel version 2.6.16 the pgdat_list has been replaced by a global array called node_data[] and the iteration over it is done through macros defined in include/linux/mm/zone.h.

Each node is divided into a number of blocks called *zones* which represent ranges in physical memory. A zone is described by struct zone_struct typedefined to zone_t namely ZONE_DMA, ZONE_NORMAL, ZONE_HIGHMEM.

ZONE_DMA (0:16MB) on x86 32 bit is associated with the first physical 16MB and is used for Direct Memory Access. This is done to remain compatible with constrained devices that are not capable to address more than 16MB. In Linux it is also for disk access (?).

ZONE_NORMAL (16MB:896MB) is the range of memory that is always mapped to the kernel's virtual memory.

ZONE_HIGHMEM (896MB:End) is present if there is more physical RAM than can be mapped into the kernel address space. Thus it is not directly mapped to the kernel's virtual address space, instead it is remapped whenever it is needed. In x86 64 bit this zone is not present since the kernel virtual address space is not confined to 1GB therefore all physical memory can be directly addressed by the kernel virtual space.

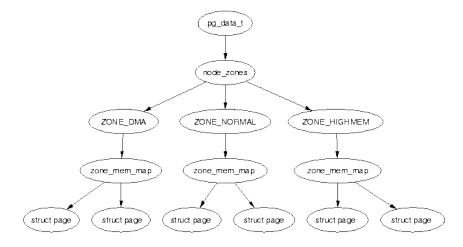


Figure 6.1: Relationships between structs. Note that there are multiple pg_data_t linked together.

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pgdat_list is created
 incrementally at each
init_bootmem_core call by
prepending each pg_data_t

To each page frame in the system there is associated a struct page element that holds all the information needed by the kernel to manage that frame. All the structs are kept together in a global array called mem_map.

We are going to describe all the structs represented in Figure 1 starting from the top of the image.

```
> include/linux/mmzone.h
typedef struct pglist_data {
   zone_t node_zones[MAX_NR_ZONES];
   zonelist_t node_zonelists[GFP_ZONEMASK+1];
   int nr_zones;
   struct page *node_mem_map;
   unsigned long *valid_addr_bitmap;
   struct bootmem_data *bdata;
   unsigned long node_start_paddr;
   unsigned long node_start_mapnr;
   unsigned long node_start_mapnr;
   unsigned long node_size;
   int node_id;
   struct pglist_data *node_next;
} pg_data_t;
```

node_zones[] holds the zone_t structs for each zone.

node_mem_map is the pointer to the first struct page within mem_map that belongs to this node (all the other struct pages of the node follow contiguously in mem_map).

node_size is the total number of page frames belonging to the node.

node_next is the pointer to the next node in the list pgdat_list.

In the i386 architecture (in which just UMA is supported) the only node contig_page_data is initialized in free_area_init() (mm/page_alloc.c) and zone_t fields are filled thanks to the parameters discovered beforehand through the E820 facility passed to this function. In the NUMA case (64 bit) node initialization is done in setup_arch() which indirectly will call free_area_init_node() (mm/numa.c). Both functions (free_area_init_x) will eventually call free_area_init_core() (mm/page_alloc.c) that performs the setup of the data structures described below.

During the POST phase the BIOS discovers how much physical memory is available and setups a table called E820, which contains information about how much physical memory is available and which are the usable regions (for example in case of shadow-ram initialization the BIOS must inform the kernel that some portion of memory is not available since it stores BIOS routines).

```
> include/linux/mmzone.h
typedef struct zone_struct {
    spinlock_t lock;
    unsigned long free_pages;
    zone_watermarks_t watermarks[MAX_NR_ZONES];
    unsigned long need_balance;
    unsigned long nr_active_pages,nr_inactive_pages;
    unsigned long nr_cache_pages;
    free_area_t free_area[MAX_ORDER];
    wait_queue_head_t *wait_table;
    unsigned long wait_table_size;
    unsigned long wait_table_shift;
    struct pglist_data *zone_pgdat;
    struct page *zone_mem_map;
   unsigned long zone_start_paddr;
    unsigned long zone_start_mapnr;
    char *name;
    unsigned long size;
    unsigned long realsize;
} zone_t;
```

lock is used to protect the zone from concurrent accesses and is used by the buddy system to protect the data structures from concurrent access.

free_area[] is an array of structs used for memory allocation by the Buddy System.

zone_mem_map similarly to node_mem_map points to the first struct page of mem_map that belongs to this zone.

```
> include/linux/mm.h
typedef struct page {
    struct list_head list;
    struct address_space *mapping;
    unsigned long index;
    struct page *next_hash;
    atomic_t count;
    unsigned long flags;
    struct list_head lru;
    struct page **prev_hash;

#if defined(CONFIG_HIGHMEM) || defined(WANT_PAGE_VIRTUAL)
        void *virtual;
    #endif
} mem_map_t
```

list is a field used to link this struct page to other struct pages forming a list of pages satisfying a certain property (e.g. free pages, dirty pages, locked pages etc.).

count tells the number of processes that are using that page frame (). If it is 0 then it can be put into the list of free pages (usable).

flags describe the status of the frame.

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virtual is used for pages in the highmen area. virtual then accepts the virtual address of the page if it is mapped to the kernel address space.

When pages are initialized in free_area_init() the count field is set to 0 and PG_reserved bit within the flags field is set to 1 so that no memory allocator except for bootmem (which doesn't rely on these data structures) can allocate that frame. This is done because the Main Memory subsystem of the kernel will not use the bootmem allocator anymore in its steady state but new kinds of allocators and therefore it must ensure that there aren't conflicts between the two.

Frame un-reserving is performed in mem_init() (arch/i386/mm/init.c) and it will allow the steady state allocator to start working.

6.2 Buddy System

The kernel subsystem that handles the memory allocation requests for groups of contiguous page frames is called the *zoned page frame allocator*. [undthelinkern pp. 302].

The component named "zoned allocator" receives the requests for allocation and deallocation of dynamic memory. In the case of allocation requests, the component search a memory zone that includes a group of contiguous page frames that can satisfy the request. Inside each zone, page frames are handled by a component named "buddy system". The buddy system of all zones and nodes are linked together via the allocation fallback list. When a request for memory cannot be satisfied in the preferred zone or node, first another zone in the same node, and then another node is picked to fulfill the request.

Free memory blocks in the system are always grouped as two buddies. The buddies can be allocated independently of each other; if, however, both remain unused at the same time, the kernel merges them into a larger pair that serves as a buddy on the next level.

The buddy system uses the free_area[] field of zone_t to perform memory allocation and deallocation. free_area[] is an array of structs defined as follows.

```
> include/linux/mmzone.h (v. < 2.6.10)
typedef struct free_area_struct {
    struct list_head free_list;
    unsigned long *map;
} free_area_t;</pre>
```

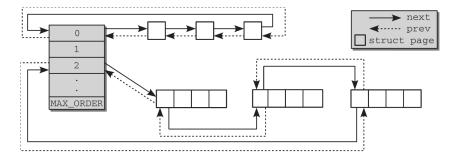


Figure 6.2: free_area[] array data structure representation within a zone

Each entry of the array is associated to an *order* and holds in a list pointed by free_list memory blocks of size 2^{order} composed of contiguous free struct pages. Therefore order 0 contains blocks of single pages, order 1 contains blocks of 2^1 pages, order 2 blocks of 2^2 pages, up to MAX_ORDER - 1 (MAX_ORDER usually set to 11). The list is formed by linking the

blocks of the same size together through the list field of the first struct page belonging to the block.

The map field of the struct points to a bitmap used for allocation and deallocation. Each bit represents a pair of buddies in the current order. It is set to 0 if both are allocated or both are free and it is set to 1 otherwise.

When a request for allocating an area of a given order ord is issued, the buddy system tries to find a free block inside of free_area[ord].free_list. If the request can be fulfilled in the requested order ord then the block is returned and the bit in the bitmap is toggled. Otherwise the buddy system recursively searches for free blocks in higher orders, say it is found in high_ord > ord, and splits the block into two blocks (called buddies), one put in the list of high_ord - 1 (also toggling the bits in the bitmap) and the other recursively split into smaller blocks (buddies) until ord is reached.

ALLOCATION

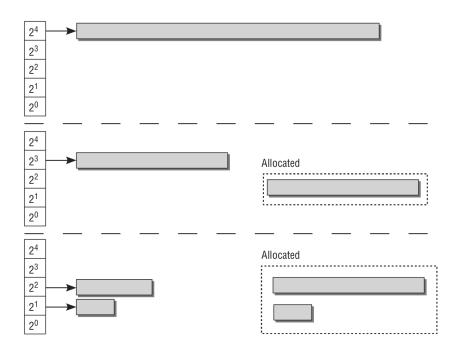


Figure 6.3: Evolution of free_area[] when first requesting a block of order 3 and then of order 1.

When the block is later freed, the buddy will be checked. If both are free, the kernel will try to coalesce the buddies together immediately. In the worst scenario the operation requires MAX_ORDER steps. To detect if the buddies can be merged or not, the bit corresponding to the affected pair of buddies in the bitmap is checked. As one buddy has just been freed by this function it is known that at least one buddy is free. If the bit in the map is 0 after toggling, we know that the other buddy must also be free and thus they can be merged. Otherwise if the bit is 1, the other buddy is in use therefore the buddies cannot be merged and the block goes into its order list.

All the memory operations performed by the buddy system require the acquirement of the lock found in zone_t therefore it can be a bottleneck for the responsiveness of the system since there can be contentions on the spinlock.

Even though the algorithm is very fast, the frequent allocation and release of page frames and blocks may lead to a situation in which several page frames are free in the system but they are scattered (not contiguous) throughout the physical address space. Single reserved pages

DEALLOCATION

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that sit in the middle of an otherwise large continuous free range can eliminate coalescing of this range very effectively.

6.3 High Memory

vmap: long duration mapping for multiple physical pages.

kmap: short duration, needs global sync, all the cores running in kernel mode will see that mapping

kmap atomic: short duration but only this cpu will see that mapping, used in interrupt handlers usually.

Deallocation: vector of counters for each high memory page. 0 is not mapped, not used by anyone else. 1 just been released. more than 1 it is mapped multiple times. The release is done with kunmap that decrements the counter. When the counter goes to 1 means that no kernel thread is referencing that page.

On mapping we create a PTE.

All the CR3 registers point to the same page table since it is not useful to have different views on memory. When counter 1 the PTE is removed and the tlb is flushed to be sure that all the cores remove that from tlb.

The struct pages hold the physical addresses of the pages that are then translated to virtual ones.

Memory allocation is finalised with mem init. Destroys the bootmem allocator and releases all the frames used by bootmem through the PG reserved bit. free page is invoked for each page that is freed to give them to buddy. The page count is set to one and free page is called to fake the buddy into thinking that this was actual freed by some kernel subsystem.

The memory allocator is general and doesn't know who is asking for the memory. Process context or Interrupt.

API offered by the buddy system for kernel internal allocation (kernel modules etc.). Get zeroed page, zeros the content and return the virtual address.

Get free pages increases the likelihood of fragmentation.

free page puts back a page into the free list. Failing to pass addr or order might spoil the kernel.

Allocations accept flags. GFP atomic won't put to sleep the asker, used for interrupt context. USER can put to sleep used for userspace. KERNEL is for kernel while BUFFER for buffers to pass data structures to userspace for example. Sleeping the kernel means the thread asking.

In case of NUMA we must also tell which numa node we want to allocate memory etc. or in case of Get free pages returns any numa node etc. Various NUMA policies are specified on how the node is chosen and can be set through set mempolicy. nodemask tell which nodes are involved and maxnode how many bits of the maks need to be checked. The modes can be default which asks for memory from the same node in which the request is issued. bind is from where I was spawned. Interleave different node on each allocation. Preferred you choose.

mbind sets a numa policy for a range of addresses.

move pages is used to move from one numa node to the other. A set of pages moved to a

set of nodes and the status tells whether the call succeeded. The cache controllers of the various cores read and communicate for moving pages. 1 line is 64 Bytes therefore there is great overhead to move pages.

Issues are fragmentations (internal and external). Latency. For each numa node there is a buddy system. Anytime asking pages and releasing a spinlock is used.

Fast allocators allow quick allocation through caching. SLAB a block is used. SLUB is the default and is an optimisation of SLAB.

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