# PlayStation®2 EE Library Reference Release 2.4

**Other Libraries** 

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Sony Computer Entertainment Inc. 1-1, Akasaka 7-chome, Minato-ku Tokyo 107-0052, Japan

Sony Computer Entertainment America 919 E. Hillsdale Blvd. Foster City, CA 94404, U.S.A.

Sony Computer Entertainment Europe 30 Golden Square London W1F 9LD, U.K.

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#### **About This Manual**

This is the Runtime Library Release 2.4 version of the *PlayStation®2 EE Library Reference - Other Libraries* manual.

The purpose of this manual is to define all available PlayStation®2 EE other library structures and functions. The companion *PlayStation®2 EE Library Overview - Other Libraries* describes the structure and purpose of the libraries.

#### **Changes Since Last Release**

#### **Chapter 2: DMA Basic Library**

 In the "Description" sections of sceDmaRecv() and sceDmaSend(), information about transfer to the SPR has been added.

#### **Related Documentation**

Library specifications for the IOP can be found in the *PlayStation®2 IOP Library Reference* manuals and the *PlayStation®2 IOP Library Overview* manuals.

**Note:** the Developer Support Web site posts current developments regarding the Libraries and also provides notice of future documentation releases and upgrades.

#### **Typographic Conventions**

Certain Typographic Conventions are used throughout this manual to clarify the meaning of the text:

Convention	Meaning
courier	Indicates literal program code.
italic	Indicates names of arguments and structure members (in structure/function definitions only).
medium bold	Indicates data types and structure/function names (in structure/function definitions only).
blue	Indicates a hyperlink.

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# **Structures**

#### sceDevGifCnd

GIF register set

Library	Introduced	Documentation last modified
libdev	1.1	December 23, 1999

#### **Structure**

#### typedef struct sceDevGifCnd {

```
u_long128 tag;
                                       Previously processed GIFtag
u_int stat;
                                       GIF_STAT register
                                       GIF_CNT register
u_int count;
u_int p3count;
                                       GIF_P3CNT register
u_int p3tag;
                                       GIF_P3TAG register
u_int pad;
                                       Padding
```

#### **Description**

}

This structure contains the registers of the GIF.

#### sceDevVif0Cnd

VIF0 register set

Library	Introduced	Documentation last modified
libdev	1.1	December 23, 1999

#### **Structure**

#### typedef struct sceDevVif0Cnd {

VIF0\_R[0-3] registers u\_int row[4]; u\_int col[4]; VIF0\_C[0-3] registers u\_int mask; VIF0\_MASK register u\_int code; VIF0\_CODE register u\_int stat; VIF0\_STAT register u\_short itop,itops; VIF0\_ITOP register, VIF0\_ITOPS register u\_short mark; VIF0\_MARK register VIF0\_NUM register u\_short num; u\_char error; VIF0\_ERR register VIF0\_CYCLE register u\_char cl,wl; u\_char cmod; VIF0\_MODE register Padding u\_char pad;

#### **Description**

}

This structure contains the registers of VIFO.

#### sceDevVif1Cnd

VIF1 register set

Library	Introduced	Documentation last modified
libdev	1.1	December 23, 1999

#### **Structure**

#### typedef struct sceDevVif1Cnd {

VIF1\_R[0-3] registers u\_int row[4]; u\_int col[4]; VIF1\_C[0-3] registers u\_int mask; VIF1\_MASK register u\_int code; VIF1\_CODE register u\_int stat; VIF1\_STAT register

u\_short itop,itops; VIF1\_ITOP register, VIF1\_ITOPS register u\_short base,offset; VIF1\_BASE register, VIF1\_OFFSET register VIF1\_TOP register, VIF1\_TOPS register u\_short top,tops;

u\_short mark; VIF1\_MARK register u\_short num; VIF1\_NUM register u\_char error; VIF1\_ERR register u\_char cl,wl; VIF1\_CYCLE register VIF1\_MODE register u\_char cmod;

**Padding** u\_char pad;

}

#### **Description**

This structure contains the registers of VIF1.

# sceDevVu0Cnd

VU0 register set

Library	Introduced	Documentation last modified
libdev	1.1	December 23, 1999

#### **Structure**

#### typedef struct sceDevVu0Cnd {

u\_long128 vf[32]; Floating-point registers VF00 to VF31 Status flag u\_int status; u\_int mac; MAC flag u\_int clipping; Clipping flag **u\_int** *r*, *i*, *q*; R register, I register, and Q register u\_short vi[16]; Integer registers VI00 to VI15

#### **Description**

}

This structure contains the registers of VU0.

### sceDevVu1Cnd

VU1 register set

Library	Introduced	Documentation last modified
libdev	1.1	December 23, 1999

#### **Structure**

#### typedef struct sceDevVu1Cnd {

u\_long128 vf[32]; Floating-point registers VF00 to VF31 u\_int status; Status flag u\_int mac; MAC flag u\_int clipping; Clipping flag **u\_int** *r,i,q,p;* R register, I register, Q register, and P register u\_short vi[16]; Integer registers VI00 to VI15

#### **Description**

}

This structure contains the registers of VU1.

# **VIF1 Control Functions**

#### sceDevVif1Continue

Cancel pause of VIF1

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devvif1.h>

int sceDevVif1Continue(void)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Restarts processing of VIF1, which had been stopped by the sceDevVif1Pause function.

#### **Return value**

0: Restart failed

1: Restart succeeded

#### sceDevVif1GetCnd

Get all registers

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devvif1.h> int sceDevVif1GetCnd( sceDevVif1Cnd \*cnd)

Structure in which the state that was obtained is stored

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Checks the state of VIF1.

The state can be obtained only when the VIF has stalled (STALL status) or is idle (IDLE status).

#### Return value

0: State could not be obtained

1: State was obtained

#### sceDevVif1GetErr

Get error mask

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devvif1.h> u\_int sceDevVif1GetErr(void)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Gets the VIF1 error mask.

#### **Return value**

- bit0
  - 0: UNMASK interrupt
  - 1: MASK interrupt
- bit1
  - 0: Stall due to DMAtag Mismatch error
  - 1: Ignore DMAtag Mismatch error
- bit2
  - 0: Stall due to VIFcode
  - 1: Ignore VIFcode error

#### sceDevVif1GetFifo

Read data from FIFO

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devvif1.h> int sceDevVif1GetFifo(

u\_long128 \*addr, Address for storing data that was read

int *n*) Number of data items to be read (qword units)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Reads data from VIF1-FIFO using an I/O operation.

#### Return value

Number of data items that were read

# sceDevVif1Pause

Pause VIF1

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devvif1.h> int sceDevVif1Pause(

int mode)

- 0: Pause immediately (Force Break)
- 1: Pause after completion of the VIFcode that is being processed

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Pauses VIF1.

#### **Return value**

- 0: Pause request failed
- 1: Pause request succeeded

#### sceDevVif1PutErr

Set error mask

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

# #include <devvif1.h> u\_int sceDevVif1PutErr(

int interrupt, 0: Enable i bit interrupt

1: Disable i bit interrupt

0: Stall due to DMAtag Mismatch error int miss1,

1: Ignore DMAtag Mismatch error

int miss2) 0: Stall due to VIFcode error

1: Ignore VIFcode error

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Sets the VIF1 error mask.

#### Return value

Bit pattern that was set in VIF1\_ERR register

#### sceDevVif1PutFifo

Write data to FIFO

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devvif1.h> int sceDevVif1PutFifo(

u\_long128 \*addr, Starting address of data to be written

int *n*) Number of data items to be written (qword units)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Writes data to VIF1-FIFO using an I/O operation.

#### **Return value**

Number of data items that were written

#### sceDevVif1Reset

Reset VIF1

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devvif1.h> void sceDevVif1Reset(void)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Resets the entire VIF1 including VIF1-FIFO.

The following bits are set in the error mask (VIF1\_ERR register):

- Enable i bit interrupt
- Ignore DMAtag Mismatch error
- Ignore VIFcode error

#### **Return value**

None

# **VIFO Control Functions**

#### sceDevVif0Continue

Cancel pause of VIFO

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devvif0.h>

int sceDevVif0Continue(void)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Restarts processing of VIFO, which had been stopped by the sceDevVifOPause function.

#### **Return value**

0: Restart failed

1: Restart succeeded

#### sceDevVif0GetCnd

Get all registers

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devvif0.h> int sceDevVif0GetCnd( sceDevVif0Cnd \*cnd)

Structure in which the state that was obtained is stored

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Checks the state of VIFO.

The state can be obtained only when the VIF has stalled (STALL status) or is idle (IDLE status).

#### **Return value**

0: State could not be obtained

1: State was obtained

# sceDevVif0GetErr

Get error mask

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devvif0.h> u\_int sceDevVif0GetErr(void)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Gets the VIF0 error mask.

#### **Return value**

Value of VIF0\_ERR register

# sceDevVif0Pause

Pause VIF0

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devvif0.h> int sceDevVif0Pause(

int mode)

- 0: Pause immediately (Force Break)
- 1: Pause after completion of the VIFcode that is being processed (Stop)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Pauses VIFO.

#### **Return value**

- 0: Pause request failed
- 1: Pause request succeeded

#### sceDevVif0PutErr

Set error mask

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

# #include <devvif0.h> u\_int sceDevVif0PutErr(

0: Enable i bit interrupt int interrupt,

1: Disable i bit interrupt

int miss1, 0: Stall due to DMAtag Mismatch error

1: Ignore DMAtag Mismatch error

int miss2) 0: Stall due to VIFcode error

1: Ignore VIFcode error

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Sets the VIF0 error mask.

#### Return value

Bit pattern that was set.

#### sceDevVif0PutFifo

Write data to FIFO

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devvif0.h> int sceDevVif0PutFifo(

u\_long128 \*addr, Starting address of data to be written

int *n*) Number of data items to be written (qword units)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Writes data to VIF0-FIFO using an I/O operation.

#### **Return value**

Number of data items that were written.

# sceDevVif0Reset

Reset VIF0

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devvif0.h> void sceDevVif0Reset(void)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Resets the entire VIF0 including VIF0-FIFO.

The following bits are set in the error mask (VIFO\_ERR register):

- Enable i bit interrupt
- Ignore DMAtag Mismatch error
- Ignore VIFcode error

#### **Return value**

None

# **GIF Control Functions**

#### sceDevGifContinue

Restart GIF processing

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devgif.h>

int sceDevGifContinue(void)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Restarts GIF processing, which had been stopped by the sceDevGifPause function.

#### **Return value**

0: Restart failed

1: Restart succeeded

#### sceDevGifGetCnd

Get all registers

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devgif.h> int sceDevGifGetCnd( sceDevGifCnd \*cnd)

Structure in which the state that was obtained is stored

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Checks the GIF state.

The state can be obtained only when the GIF has been paused (PAUSE status) or is idle (IDLE status).

#### **Return value**

0: State could not be obtained

1: State was obtained

#### sceDevGifGetImtMode

Get PATH3 transfer mode

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devgif.h>

u\_int sceDevGifGetImtMode(void)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Gets the PATH3 transfer mode.

#### **Return value**

- 0: Continuous transfer mode (PATH3 image data transfers are performed continuously)
- 1: Intermittent transfer mode (PATH3 image data transfers are stopped every 8 qwords. If there is another data transfer request, an interrupt occurs and that data is transferred)

# sceDevGifGetP3msk

Get PATH3 mask status

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devgif.h> int sceDevGifGetP3msk(void)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Gets the PATH3 mask status.

#### **Return value**

0: PATH3 unmasked

1: PATH3 masked

# sceDevGifPause

Pause GIF

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devgif.h> int sceDevGifPause(void)

# **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Pauses the GIF.

#### **Return value**

0: Pause failed

1: Pause succeeded

#### sceDevGifPutFifo

Write data to FIFO

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devgif.h> int sceDevGifPutFifo(

u\_long128 \*addr, Starting address of data to be written

int *n*) Number of data items to be written (qword units)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Writes data to GIF-FIFO using an I/O operation.

#### **Return value**

Number of data items that were written

#### sceDevGifPutImtMode

Set PATH3 transfer mode

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devgif.h> void sceDevGifPutImtMode(

int mode)

PATH3 transfer mode

- 0: Continuous transfer mode (PATH3 image data transfers are performed continuously)
- 1: Intermittent transfer mode (PATH3 image data transfers are stopped every 8 qwords. If there is another data transfer request, an interrupt occurs and that data is transferred)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Sets the PATH3 transfer mode.

#### Return value

None

# sceDevGifPutP3msk

Mask PATH3

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devgif.h> int sceDevGifPutP3msk(

int enable) PATH3 mask value

> 0: unmask 1: mask

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Masks PATH3.

The PATH3 mask can be set only when GIF/VIF1 are IDLE.

#### **Return value**

0: Setting failed

1: Setting succeeded

# sceDevGifReset

Reset GIF

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devgif.h> void sceDevGifReset(void)

# **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

# **Description**

Resets the entire GIF.

#### **Return value**

None

# **VU1 Control Functions**

## sceDevVu1Continue

Restart VU1 processing

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devvu1.h>

int sceDevVu1Continue(void)

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Restarts VU1 processing, which had been stopped by the sceDevVu1Pause function.

#### **Return value**

0: Restart failed

1: Restart succeeded

# sceDevVu1Exec

Re-execute microprogram

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

### **Syntax**

#include <devvu1.h> void sceDevVu1Exec(

u\_short addr) Re-execution address

# **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

## **Description**

Re-executes VU1 starting at addr.

#### **Return value**

# sceDevVu1GetCnd

Read all registers

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

### **Syntax**

#include <devvu1.h> int sceDevVu1GetCnd( sceDevVu1Cnd \*cnd)

Address of structure which will contain read data

# **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

### **Description**

Reads the contents of the registers of VU1.

#### **Return value**

0: Read failed

1: Read succeeded

# sceDevVu1GetDBit

Get state of debugging function

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

### **Syntax**

#include <devvu1.h> int sceDevVu1GetDBit(void)

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

## **Description**

Gets the state of the stop function according to the Dbit of VU1.

#### **Return value**

0: disabled

1: enabled

# sceDevVu1GetTBit

Get state of debugging function

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

### **Syntax**

#include <devvu1.h> int sceDevVu1GetTBit(void)

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

### **Description**

Gets the state of the stop function according to the Tbit of VU1.

#### **Return value**

0: disabled

1: enabled

# sceDevVu1GetTpc

Read TPC register

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

### **Syntax**

#include <devvu1.h> u\_short sceDevVu1GetTpc(void)

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

### **Description**

Reads the TPC register of VU1.

#### **Return value**

Data that was read

# sceDevVu1Pause

Force Break VU1

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

### **Syntax**

#include <devvu1.h> int sceDevVu1Pause(void)

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

### **Description**

Performs a Force Break operation on VU1.

#### **Return value**

0: Break failed (debugging was already stopped)

1: Break succeeded

# sceDevVu1PutCnd

Write to all registers

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

### **Syntax**

#include <devvu1.h> int sceDevVu1PutCnd( sceDevVu1Cnd \*cnd)

Address of structure which contains the data to be written

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

### **Description**

Writes data to the registers of VU1.

#### **Return value**

0: Write failed

1: Write succeeded

# sceDevVu1PutDBit

Set debugging function

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

### **Syntax**

#include <devvu1.h> void sceDevVu1PutDBit(

int dbit) 0: Disable Dbit

1: Enable Dbit

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

### **Description**

Enables or disables the stop function according to the Dbit of VU1.

#### **Return value**

# sceDevVu1PutTBit

Set debugging function

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

### **Syntax**

#include <devvu1.h> void sceDevVu1PutTBit(

int tbit) 0: Disable Tbit

1: Enable Tbit

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

### **Description**

Enables or disables the stop function according to the Tbit of VU1.

#### **Return value**

# sceDevVu1Reset

Reset VU1

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

### **Syntax**

#include <devvu1.h> void sceDevVu1Reset(void)

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

## **Description**

Resets VU1.

#### **Return value**

# **VUO Control Functions**

## sceDevVu0Continue

Restart VU0 processing

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

#### **Syntax**

#include <devvu0.h>

int sceDevVu0Continue(void)

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Restarts VU0 processing after VU0 was stopped due to debug or the sceDevVu0Pause function.

#### **Return value**

0: Restart failed

1: Restart succeeded

# sceDevVu0Exec

Re-execute microprogram

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

### **Syntax**

#include <devvu0.h>
void sceDevVu0Exec(

u\_short addr) Re-execution address

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

### **Description**

Re-executes VU0 starting at addr.

#### **Return value**

# sceDevVu0GetCnd

Read all registers

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

### **Syntax**

#include <devvu0.h> int sceDevVu0GetCnd( sceDevVu0Cnd \*cnd)

Address of structure which will contain read data

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

### **Description**

Reads the contents of the registers of VU0.

#### **Return value**

0: Read failed

1: Read succeeded

# sceDevVu0GetDBit

Get state of debugging function

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

### **Syntax**

#include <devvu0.h> int sceDevVu0GetDBit(void)

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

## **Description**

Gets the state of the stop function according to the Dbit of VUO.

#### **Return value**

0: disabled

1: enabled

# sceDevVu0GetTBit

Get state of debugging function

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

### **Syntax**

#include <devvu0.h> int sceDevVu0GetTBit(void)

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

## **Description**

Gets the state of the stop function according to the Tbit of VUO.

#### **Return value**

0: disabled

1: enabled

# sceDevVu0GetTpc

Read TPC register

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

### **Syntax**

#include <devvu0.h>

u\_short sceDevVu0GetTpc(void)

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

### **Description**

Reads the TPC register of VU0.

#### **Return value**

Data that was read

# sceDevVu0Pause

Force Break VU0

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

### **Syntax**

#include <devvu0.h> int sceDevVu0Pause(void)

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

### **Description**

Performs a Force Break operation on VU0.

#### **Return value**

0: Break failed (debugging was already stopped)

1: Break succeeded

# sceDevVu0PutCnd

Write to all registers

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

### **Syntax**

#include <devvu0.h> int sceDevVu0PutCnd( sceDevVu0Cnd \*cnd)

Address of structure which contains the data to be written

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

### **Description**

Writes data to the registers of VU0.

#### **Return value**

0: Write failed

1: Write succeeded

## sceDevVu0PutDBit

Set debugging function

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

### **Syntax**

#include <devvu0.h> void sceDevVu0PutDBit(

int dbit,) 0: Disable Dbit

1: Enable Dbit

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

### **Description**

Enables or disables the stop function according to the Dbit of VU0.

#### **Return value**

# sceDevVu0PutTBit

Set debugging function

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

### **Syntax**

#include <devvu0.h> void sceDevVu0PutTBit(

int tbit,) 0: Disable Tbit

1: Enable Tbit

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Enables or disables the stop function according to the Tbit of VUO.

#### **Return value**

# sceDevVu0Reset

Reset VU0

Library	Introduced	Documentation last modified
libdev	1.1	March 26, 2001

### **Syntax**

#include <devvu0.h> void sceDevVu0Reset(void)

# **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

# **Description**

Resets VU0.

#### **Return value**

# **Pseudo Console Control Functions**

## sceDevConsAttribute

Change attributes (color)

Library	Introduced	Documentation last modified
libdev	1.5	March 26, 2001

#### **Syntax**

#include <devfont.h>

void sceDevConsAttribute(

int cd, Console identifier Attribute (color code) u\_char col)

### **Calling conditions**

Can be called from a thread

Not multithread safe

### **Description**

Changes the default attributes of the characters output to the console.

### Return value

# sceDevConsClear

Clear console

Library	Introduced	Documentation last modified
libdev	1.4	March 26, 2001

### **Syntax**

#include <devfont.h> void sceDevConsClear(

int cd); Console identifier

## **Calling conditions**

Can be called from a thread

Not multithread safe

## **Description**

Clears the console.

#### **Return value**

# sceDevConsClearBox

Clear the box

Library	Introduced	Documentation last modified
libdev	1.5	March 26, 2001

### **Syntax**

#### #include <devfont.h>

### void sceDevConsClearBox(

int cd, Console identifier int X, Drawing X coordinate int y, Drawing Y coordinate

u\_int w, Width u\_int h) Height

### **Calling conditions**

Can be called from a thread

Not multithread safe

#### **Description**

Clears the box on the console, i.e. fills it with spaces.

#### **Return value**

# sceDevConsClose

Close console

Library	Introduced	Documentation last modified
libdev	1.4	March 26, 2001

### **Syntax**

#include <devfont.h> void sceDevConsClose(

int cd); Console identifier

## **Calling conditions**

Can be called from a thread

Not multithread safe

## **Description**

Closes the console.

#### **Return value**

## sceDevConsDraw

Draw console image

Library	Introduced	Documentation last modified
libdev	1.4	March 26, 2001

### **Syntax**

#include <devfont.h> void sceDevConsDraw(

int cd) Console identifier

### **Calling conditions**

Can be called from a thread

Not multithread safe

### **Description**

Uses functions such as sceDevConsPrintf() to draw an output console image.

While the packet is being built, because direct DMA kicks are performed internally, processing will not return until drawing ends.

#### **Return value**

## sceDevConsDrawS

Draw console image (using scratch pad)

Library	Introduced	Documentation last modified
libdev	1.4	March 26, 2001

### **Syntax**

#include <devfont.h> void sceDevConsDrawS(

int cd); Console identifier

# **Calling conditions**

Can be called from a thread

Not multithread safe

#### **Description**

Uses functions such as sceDevConsPrintf() to draw an output console image.

While the packet is being built, because direct DMA kicks are performed internally, processing will not return until drawing ends.

Scratch pad is used as the work area.

#### **Return value**

# sceDevConsFrame

Draw frame

Library	Introduced	Documentation last modified
libdev	1.5	March 26, 2001

### **Syntax**

# #include <devfont.h> void sceDevConsFrame(

int cd, Console identifier X coordinate int X, Y coordinate int y, Width u\_int w, u\_int h); Height

## **Calling conditions**

Can be called from a thread

Not multithread safe

# **Description**

Draws a specific size box to a specific location on the console.

#### **Return value**

# sceDevConsGet

Get 1 character from console location

Library	Introduced	Documentation last modified
libdev	1.5	March 26, 2001

### **Syntax**

#include <devfont.h> u\_short sceDevConsGet(

int cd) Console identifier

# **Calling conditions**

Can be called from a thread

Not multithread safe

## **Description**

Gets console location character code and attribute information.

#### **Return Value**

Character code to the lower 8 bits

Attributes to the upper 8 bits

# sceDevConsInit

Initialize console library

Library	Introduced	Documentation last modified
libdev	1.4	March 26, 2001

### **Syntax**

#include <devfont.h> void sceDevConsInit(void)

## **Calling conditions**

Can be called from a thread

Not multithread safe

## **Description**

Initializes the internal variables of the console library. Only call this function once.

### **Return value**

## sceDevConsLocate

Change cursor location

Library	Introduced	Documentation last modified
libdev	1.5	March 26, 2001

### **Syntax**

#include <devfont.h> void sceDevConsLocate(

int cd, Console identifier Console X coordinate u\_int /x, Console Y coordinate u\_int /y);

## **Calling conditions**

Can be called from a thread

Not multithread safe

#### **Description**

Changes the cursor location on the console.

#### **Return value**

# sceDevConsMessage

Displays message with frame

Library	Introduced	Documentation last modified
libdev	1.5	March 26, 2001

#### **Syntax**

#### #include <devfont.h>

#### void sceDevConsMessage(

Console identifier or -1 int cd,

int x, Character drawing X coordinate or frame GS coordinate int y, Character drawing Y coordinate or frame GS coordinate

char const\* str); Character string

### **Calling conditions**

Can be called from a thread

Not multithread safe

#### **Description**

When cd is specified, drawing is performed so that the upper left point of the character string is located at the specified position on the console.

When cd is -1, x and y become GS primitive coordinates as in sceDevConsOpen() and drawing is performed so that the upper left point of the frame enclosing the character string is positioned in that location.

#### Return value

### sceDevConsMove

Moves the box

Library	Introduced	Documentation last modified
libdev	1.5	March 26, 2001

#### **Syntax**

# #include <devfont.h> void sceDevConsMove(

int cd, Console identifier

int dx, Transfer destination X coordinate int dy, Transfer destination Y coordinate u\_int sx, Transfer source X coordinate Transfer source Y coordinate u\_int sy Transfer width

u\_int w, u\_int h); Transfer height

### **Calling conditions**

Can be called from a thread

Not multithread safe

### **Description**

Transfers the specified box on the console to the specified coordinates.

It is uncertain what happens when the transfer source and transfer destination overlap.

### **Return value**

# sceDevConsOpen

Open console

Library	Introduced	Documentation last modified
libdev	1.4	March 26, 2001

#### **Syntax**

#include <devfont.h> int sceDevConsOpen(

Console drawing X-coordinate (GS primitive coordinate) u\_int gs\_x, u\_int gs\_y, Console drawing Y-coordinate (GS primitive coordinate)

u\_int chr\_w, Console width u\_int chr\_h); Console height

### **Calling conditions**

Can be called from a thread

Not multithread safe

### **Description**

Opens the console. Only one console can be open at a time and no memory is allocated.

The maximum size which can be specified by *chr\_w* and *chr\_h* is 80 x 40 characters.

#### **Return value**

Console identifier

# sceDevConsPrintf

Output characters to console

Library	Introduced	Documentation last modified
libdev	1.4	March 26, 2001

### **Syntax**

#include <devfont.h> u\_int sceDevConsPrintf(

int cd, Console identifier

const char\* str, Formatted character string

...); Parameters

## **Calling conditions**

Can be called from a thread

Not multithread safe

### **Description**

Outputs character strings to the console.

#### Return value

Number of characters that were output

# sceDevConsPut

Output 1 character to console location

Library	Introduced	Documentation last modified
libdev	1.5	March 26, 2001

# **Syntax**

#include <devfont.h> void sceDevConsPut(

int cd, Console identifier u\_char c, Character code u\_char a); Attribute (color code)

# **Calling conditions**

Can be called from a thread

Not multithread safe

# **Description**

Outputs one character to the cursor location on the console.

#### **Return value**

# sceDevConsRef

Attach console image to packet

Library	Introduced	Documentation last modified
libdev	1.4	March 26, 2001

# **Syntax**

#include <devfont.h> void sceDevConsRef(

int cd Console identifier

Initialized GIF packet structure sceGifPacket\* pPacket);

# **Calling conditions**

Can be called from a thread

Not multithread safe

# **Description**

Uses functions such as sceDevConsPrintf() to build an output console image within a GIF packet. Approximately 112 qwords are required per character.

#### **Return value**

# ${\it sce Dev Cons Rollup}$

Roll up

Library	Introduced	Documentation last modified
libdev	1.5	March 26, 2001

# **Syntax**

#include <devfont.h> void sceDevConsRollup(

int cd, Console identifier Number of lines u\_int line);

# **Calling conditions**

Can be called from a thread

Not multithread safe

# **Description**

Rolls up only the specified number of lines on the console.

#### Return value

# sceDevConsSetColor

Change font color table

Library	Introduced	Documentation last modified
libdev	1.4	March 26, 2001

# **Syntax**

#### #include <devfont.h>

# void sceDevConsSetColor(

int cd, Console identifier u\_char c, Color code (0 to 7) u\_char r, Red (0 to 255)  $u_char g$ , Green (0 to 255) u\_char b); Blue (0 to 255)

# **Calling conditions**

Can be called from a thread

Not multithread safe

#### **Description**

Changes the color table of the characters to be output to the console.

The default color table is as follows:

- 0, 0x00, 0x00, 0x00
- 1, 0x00, 0x00, 0xff
- 2, 0xff, 0x00, 0x00
- 3, 0xff, 0x00, 0xff
- 4, 0x00, 0xff, 0x00
- 5, 0x00, 0xff, 0xff
- 6, 0xff, 0xff, 0x00
- 7, Oxff, Oxff, Oxff

## Return value

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# **Structures**

#### sceDmaChan

Individual channel state

Library	Introduced	Documentation last modified
libdma	1.1	December 23, 1999

#### **Structure**

#### typedef struct {

tD\_CHCR chcr; u\_int p0[3]; Channel control Dn\_CHCR

void \*madr; u\_int p1[3]; Transfer memory address Dn MADR

u\_int qwc; u\_int p2[3]; Transfer size Dn\_QWC

sceDmaTag \*tadr; u\_int p3[3]; Transfer tag address Dn\_TADR void \*as0; u\_int p4[3]; Address stack 0 Dn\_ASR0 void \*as1; u\_int p5[3], Address stack Dn\_ASR1

u\_int p6[4], pad u\_int p7[4]; pad

void \*sadr; u\_int p8[3]; SPR address Dn SADR

} sceDmaChan;

#### **Description**

This structure is used to get/set state for an individual channel. Each member corresponds to the register shown to the right of the description.

The sceDmaGetChan() function can be used to obtain the address of the structure corresponding to each channel. Since this address is a physical register address, values are reflected in the DMAC register as soon as they are set for each member of the structure.

#### sceDmaEnv

Common channel state

Library	Introduced	Documentation last modified
libdma	1.1	December 23, 1999

#### Structure

#### typedef struct {

u\_char sts; Stall control source channel D\_CTRL.STS u\_char std; Stall control drain channel D\_CTRL.STD u\_char mfd; MFIFO drain channel D\_CTRL.MFD Release cycle D\_CTRL.RCYC u\_char rcycle; u\_short express; Express channel setting (LSB=CH0) D\_PCR.CDE u\_short notify; Specification of channel for sending termination to COPO (LSB=CH0) D PCR.CPC u\_short sqwc; Skip word count in interleave mode D\_SQWC.SQWC Transfer word count in interleave mode D\_SQWC.TQWC u\_short tqwc; void \*rbadr; Starting address of MFIFO Ring buffer D\_RBOR.ADDR MFIFO Ring buffer size mask D\_RBSR.RMSK u\_int rbmsk;

} sceDmaEnv;

# **Description**

This structure is used to get/set state that is common to all channels.

Each member corresponds to the register shown to the right of the description.

The PutDEnv() and GetDenv() functions can be used to set and get member values.

# sceDmatag

DMAtag

Library	Introduced	Documentation last modified
libdma	1.1	December 23, 1999

#### **Structure**

typedef struct \_sceDmaTag {

u\_short qwc; Packet size u\_char mark; Mark value u\_char id; Tag ID and flag ADDR field struct \_sceDmaTag \*next; u\_int p[2]; Padding

} sceDmaTag \_\_attribute\_\_ ((aligned(16)));

# **Description**

This structure describes a DMAtag.

# **Functions**

# sceDmaAddCall

Add CALL tag

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void \*sceDmaAddCall(

Tag pointer address sceDmaTag \*\*tag,

int qwc, Size information to be written in new CALL tag void \*ctag) Address information to be written in new CALL tag

# **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Creates a new CALL tag having calling destination address ctag and size qwc in memory pointed to by

Since \*tag is updated so that it points to the tag (=ctag) following the new CALL tag, the next tag can be created in succession.

The address of the data that is transferred with the new CALL tag will be returned.

#### **Return value**

# sceDmaAddCont

Add CNT tag

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void \*sceDmaAddCont(

sceDmaTag \*\*tag, Tag pointer address

Size information to be written in new CNT tag int qwc)

# **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

# **Description**

Creates a new CNT tag (for Source Chain) having size *qwc* in memory pointed to by \*tag.

Since \*tag is updated so that it points to the tag address (=address following packet body) following the new CNT tag, the next tag can be created in succession.

The address of the data that is transferred with the new CNT tag will be returned.

#### Return value

# sceDmaAddDest

Add DEST tag

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void \*sceDmaAddDest(

Tag pointer address sceDmaTag \*\*tag,

Size information to be written in new DEST tag int qwc, void \*addr) Address information to be written in new DEST tag

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Creates a new Destination Chain DEST tag having address addr and size qwc in memory pointed to by \*tag.

The address of the data that is transferred with the new DEST tag will be returned.

Since \*tag is updated so that it points to the address following the packet body, the next tag can be created in succession.

#### **Return value**

# sceDmaAddDests

Add DESTS tag

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void \*sceDmaAddDests(

Tag pointer address sceDmaTag \*\*tag,

Size information to be written in new CNTS tag int qwc, void \*addr) Address information to be written in new CNTS tag

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Creates a Destination Chain DESTS tag having address addr and size qwc in memory pointed to by \*tag.

Since \*tag is updated so that it points to the address following the packet body, the next tag can be created in succession.

The address of the data that is transferred with the new DESTS tag will be returned.

# **Return value**

# sceDmaAddEnd

Add END tag

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void \*sceDmaAddEnd(

Tag pointer address sceDmaTag \*\*tag,

int qwc, Size information to be written in new END tag void \*addr) Transfer destination address (for Destination Chain)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Creates a new END tag having size qwc in memory pointed to by \*tag. To create a Destination Chain END tag, specify the memory address of the transfer destination in addr.

Since \*tag is updated so that it points to the address following the packet body, the next tag can be created following the packet body.

The address of the data that is transferred with the new END tag will be returned.

#### Return value

# sceDmaAddExpress

Specify express (priority) transfer

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

# **Syntax**

void sceDmaAddExpress(

sceDmatag \*tag) Relevant DMAtag

# **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

# **Description**

Adds the express transfer attribute to a DMAtag that is registered in the transfer list.

For details about express transfer, refer to the description of the D\_PCR register in the "EE User's Manual".

#### **Return value**

# sceDmaAddlCall

Add CALL tag

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void \*sceDmaAddlCall(

Tag pointer address sceDmaTag \*\*tag,

int qwc, Size information to be written in new CALL tag void \*ctag) Address information to be written in new CALL tag

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Creates a new CALL tag with interrupt having calling destination address ctag and size qwc in memory pointed to by \*tag.

Since \*tag is updated so that it points to the tag address (=ctag) following the new CALL tag, the next tag can be created in succession.

The address of the data that is transferred with the new CALL tag will be returned.

#### Return value

# sceDmaAddlCont

Add CNT tag

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void \*sceDmaAddlCont(

Tag pointer address sceDmaTag \*\*tag,

int qwc) Size information to be written in new CNT tag

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

# **Description**

Creates a new CNT tag with interrupt having size qwc in memory pointed to by \*tag.

Since \*tag is updated so that it points to the tag address (=address following packet body) following the new CNT tag, the next tag can be created in succession.

The address of the data that is transferred with the new CNT tag will be returned.

#### Return value

# sceDmaAddIDest

Add IDEST tag

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void \*sceDmaAddIDest(

Tag pointer address sceDmaTag \*\*tag,

int qwc, Size information to be written in new CNT tag void \*addr) Address information to be written in new CNT tag

# **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Creates a new Destination Chain CNT tag with interrupt having address addr and size qwc in memory pointed to by \*tag.

The address of the data that is transferred with the new CNT tag will be returned.

Since \*tag is updated so that it points to the address following the packet body, the next tag can be created following the packet body.

#### **Return value**

# sceDmaAddIDests

Add IDESTS tag

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void \*sceDmaAddIDests(

sceDmaTag \*\*tag, Tag pointer address

Size information to be written in new CNTS tag int qwc, void \*addr) Address information to be written in new CNTS tag

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Creates a new Destination Chain CNTS tag with interrupt having address addr and size qwc in memory pointed to by \*tag.

The address of the data that is transferred with the new CNTS tag will be returned.

Since \*tag is updated so that it points to the address following the packet body, the next tag can be created following the packet body.

# Return value

# sceDmaAddlEnd

Add END tag

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void \*sceDmaAddIEnd(

Tag pointer address sceDmaTag \*\*tag,

int qwc, Size information to be written in new END tag void \*addr) Transfer destination address (for Destination Chain)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Creates a new END tag with interrupt having size qwc in memory pointed to by \*tag. To create a Destination Chain END tag, specify the memory address of the transfer destination in addr.

The address of the data that is transferred with the new END tag will be returned.

Since \*tag is updated so that it points to the address following the packet body, the next tag can be created following the packet body.

#### **Return value**

# sceDmaAddINext

Add NEXT tag

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void \*sceDmaAddINext(

Tag pointer address sceDmaTag \*\*tag,

int qwc, Size information to be written in new NEXT tag void \*addr) Address information to be written in new NEXT tag

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Creates a new NEXT tag with interrupt having address addr and size qwc in memory pointed to by \*tag.

Since \*tag is updated so that it points to the tag address (=addr) following the new NEXT tag, the next tag can be created in succession.

The address of the data that is transferred with the new NEXT tag will be returned.

#### **Return value**

# sceDmaAddIRef

Add REF tag

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void \*sceDmaAddIRef(

Tag pointer address sceDmaTag \*\*tag,

Size information to be written in the new REF tag int qwc, void \*addr) Address information to be written in the new REF tag

# **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Creates a new REF tag with interrupt for referencing the data of size qwc at address addr in memory pointed to by \*tag.

Since \*tag is updated so that it points to the address following the new REF tag, the next tag can be created in succession.

The address of the data that is transferred with the new REF tag will be returned.

#### Return value

# sceDmaAddIRefe

Add REFE tag

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void \*sceDmaAddIRefe(

sceDmaTag \*\*tag, Tag pointer address

Size information to be written in new REFE tag int qwc, void \*addr) Address information to be written in new REFE tag

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Creates a new REFE tag with interrupt for referencing the data of size qwc at address addr in memory pointed to by \*tag.

Since \*tag is updated so that it points to the address following the new REFE tag, the next tag can be created in succession.

The address of the data that is transferred with the new REFE tag will be returned.

#### Return value

# sceDmaAddIRefs

Add REFS tag

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void \*sceDmaAddIRefs(

Tag pointer address sceDmaTag \*\*tag,

int qwc, Size information to be written in new REFS tag void \*addr) Address information to be written in new REFS tag

# **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Creates a new REFS tag with interrupt for referencing the data of size qwc at address addr in memory pointed to by \*tag.

Since \*tag is updated so that it points to the address following the new REFS tag, the next tag can be created in succession.

The address of the data that is transferred with the new REFS tag will be returned.

#### **Return value**

# sceDmaAddIRet

Add RET tag

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void \*sceDmaAddIRet(

sceDmaTag \*\*tag, Tag pointer address

Size information to be written in new RET tag int qwc)

# **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

# **Description**

Creates a new RET tag with interrupt having size *qwc* in memory pointed to by \*tag.

Since \*tag is updated so that it points to the tag address following the new RET tag, the next tag can be created in succession.

The address of the data that is transferred with the new RET tag will be returned.

#### Return value

# sceDmaAddNext

Add NEXT tag

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void \*sceDmaAddNext(

Tag pointer address sceDmaTag \*\*tag,

int qwc, Size information to be written in new NEXT tag void \*addr) Address information to be written in new NEXT tag

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Creates a new NEXT tag having tag address addr and size qwc in memory pointed to by \*tag.

Since \*tag is updated so that it points to the tag following the new NEXT tag, the next tag can be created in succession.

The address of the data that is transferred with the new NEXT tag will be returned.

#### **Return value**

# sceDmaAddRef

Add REF tag

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void \*sceDmaAddRef(

Tag pointer address sceDmaTag \*\*tag,

Size information to be written in new REF tag int qwc, void \*addr) Address information to be written in new REF tag

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Creates a new REF tag for referencing data of size qwc at address addr in memory pointed to by \*tag.

Since \*tag is updated so that it points to the address following the new REF tag, the next tag can be created in succession.

The address of the data that is transferred with the new REF tag will be returned.

# **Return value**

# sceDmaAddRefe

Add REFE tag

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void \*sceDmaAddRefe(

Tag pointer address sceDmaTag \*\*tag,

Size information to be written in new REFE tag int qwc, void \*addr) Address information to be written in new REFE tag

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Creates a new REFE tag for referencing data of size qwc at address addr in memory pointed to by \*tag.

Since \*tag is updated so that it points to the address following the new REFE tag, the next tag can be created in succession.

The address of the data that is transferred with the new REFE tag will be returned.

#### **Return value**

# sceDmaAddRefs

Add REFS tag

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void \*sceDmaAddRefs(

Tag pointer address sceDmaTag \*\*tag,

Size information to be written in new REFS tag int qwc, void \*addr) Address information to be written in new REFS tag

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Creates a new REFS tag for referencing data of size qwc at address addr in memory pointed to by \*tag.

Since \*tag is updated so that it points to the address following the new REFS tag, the next tag can be created in succession.

The address of the data that is transferred with the new REFS tag will be returned.

# **Return value**

# sceDmaAddRet

Add RET tag

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void \*sceDmaAddRet(

Tag pointer address sceDmaTag \*\*tag,

int qwc) Size information to be written in new RET tag

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

# **Description**

Creates a new RET tag having size *qwc* in memory pointed to by \*tag.

Since \*tag is updated so that it points to the tag following the new RET tag, the next tag can be created in succession.

The address of the data that is transferred with the new RET tag will be returned.

#### **Return value**

# sceDmaDebug

Set debugging mode

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

# **Syntax**

# int sceDmaDebug(

int mode) Debugging mode

> 0: Debugging OFF 1: Debugging ON

# **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

# **Description**

Sets the debugging mode. If 1 is specified, debugging is turned on and an argument consistency check will be performed by each libdma function.

#### **Return value**

Previous mode value

#### sceDmaGetChan

Get channel structure

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

#### sceDmaChan \*sceDmaGetChan(

Channel number int id)

#### Calling conditions

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Returns the address of the sceDmaChan structure corresponding to the channel number id. Since this address is the address of the DMA register that was mapped to memory, the DMAC register can be directly read or written by using the return value as a pointer.

Also, to specify the relevant DMA channel when starting or stopping DMA transfers, use the value returned by this function, not the channel number. The values of id correspond to DMA channels as follows:

Table 2-1

id	DMA channel
0	VIFO
1	VIF1
2	GIF
3	fromIPU
4	toIPU
5	SIF0
6	SIF1
7	SIF2
8	fromSPR
9	toSPR

#### **Notes**

Since data transfers involving the IPU and SIF must be performed carefully, fromIPU/toIPU control should be performed using libipu and SIF0/SIF1/SIF2 control should be performed using libsif.

Access to memory-mapped registers is uncached. Note that using this as a normal structure may cause processing speed to decrease.

#### Return value

The address of the sceDmaChan structure corresponding to the channel number id is returned.

If the id value is invalid, an error occurs, and 0 is returned.

# sceDmaGetEnv

Get common channel registers

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

# **Syntax**

sceDmaEnv \*sceDmaGetEnv(

sceDmaEnv \*denv) Address of structure into which register values are to be

read

**Calling conditions** 

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

# **Description**

Reads the values of the DMAC common channel registers and stores them in the structure specified by denv.

# **Return value**

denv is returned.

# sceDmaGetNextTag

Get next transfer list tag

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

# **Syntax**

# sceDmaTag \*sceDmaGetNextTag(

sceDmaTag \*tag)

Tag address in transfer list

# **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

# **Description**

Returns the address of the DMAtag that is transferred following tag.

#### **Return value**

The next tag address is returned. If tag specifies an invalid tag, 0 is returned.

# sceDmaPause

Pause DMA transfer

Library	Introduced	Documentation last modified
libdma	1.1	July 2, 2001

# **Syntax**

u\_int sceDmaPause(

sceDmaChan \*d)

DMA channel to be paused

# **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

# **Description**

Pauses DMA transfer for the specified channel.

DMA transfer can be restarted by the scdDmaRestart() function.

#### **Return value**

Returns the value of the Dn\_CHCR register when it was stopped. This value is passed to the sceDmaRestart function argument.

#### sceDmaPutEnv

Set common channel registers

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

int sceDmaPutEnv(

sceDmaEnv \*env) Address of structure containg values to be written to

registers

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Sets the DMAC registers with the contents of the structure pointed to by env.

In order to ensure system stability, the settings for common registers are first placed in memory in the sceDmaEnv structure, then this function is used to set the contents of those registers.

Since DMAC registers are shared between threads, if several threads set these registers separately, there is a danger that resource collisions will occur.

#### **Return value**

On normal termination, 0 is returned. If the contents of the structure pointed to by env are invalid, a negative number is returned.

### sceDmaPutStallAddr

Set stall address

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

#### void \*sceDmaPutStallAddr(

void \*addr)

Address at which stall is to occur

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Sets the value of addr in the D\_STADR register, which indicates the DMA stall address.

A positive value is normally specified for addr. However, if -1 is specified, no stall address is set, and the value that is currently set is returned.

#### Return value

The previous stall address is returned.

#### sceDmaRecv

Start DMA transfer (Destination Chain Mode)

Library	Introduced	Documentation last modified
libdma	1.1	October 11, 2001

#### **Syntax**

void sceDmaRecv( sceDmaChan \*d)

DMA channel for performing transfer

#### Calling conditions

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Starts DMA transfer from a device to memory using Destination Chain Mode.

The destination memory address is specified with tag information from the device.

The processing performed internally by this function only initiates the DMA transfer. The actual transfer is performed in the background. Also, queuing is not performed so the function is executed immediately.

When transfering to the SPR, the DMA tag address MSB must be set to 1.

#### **Return value**

# sceDmaRecvI

Start DMA transfer (Interleave Mode, SPR->memory)

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void sceDmaRecvI(

sceDmaChan \*d, DMA channel for performing transfer (fromSPR)

void \*addr, Memory address of transfer destination int size) Size of data to be transferred (qword)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Starts DMA transfer from SPR to memory using Interleave Mode. from SPR (id=8) must be specified for d.

The SPR address of the transfer source is specified by *d->sadr*, and the memory address of the transfer destination is specified by addr. Also, parameters indicating a small rectangular area must be set in advance for the DMAC via sqwc and tqwc of the sceDmaEnv structure.

The processing performed internally by this function only initiates the DMA transfer. The actual transfer is performed in the background. Also, queuing is not performed so the function is executed immediately.

#### Return value

#### sceDmaRecvN

Start DMA transfer (Normal Mode, device->memory)

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void sceDmaRecvN(

sceDmaChan \*d, DMA channel for performing transfer void \*addr, Memory address of transfer destination int size) Size of data to be transferred (gword)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Starts DMA transfer from a device to memory using Normal Mode. The memory address that is the transfer destination is specified by addr, and the size of the data to be transferred is specified by size.

The processing performed internally by this function only initiates the DMA transfer. The actual transfer is performed in the background. Also, queuing is not performed so the function is executed immediately.

#### **Return value**

### sceDmaReset

Reset DMAC

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

### int sceDmaReset(

int mode) Reset mode

> 0: disable 1: enable

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Resets the DMAC.

After waiting for termination of all DMA transfers, this function clears all end-of-transfer handlers, initializes the DMAC, and enables or disables the DMAC according to the mode.

#### **Return value**

Previous mode value

#### sceDmaRestart

Restart paused DMA transfer

Library	Introduced	Documentation last modified
libdma	1.1	July 2, 2001

#### **Syntax**

int sceDmaRestart(

sceDmaChan \*d, DMA channel for restarting transfer u\_int chdr) Return value of sceDmaPause function

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Restarts transfer processing for the specified DMA channel.

#### **Return value**

If the transfer was stopped, 0 is returned. If it was already operating, 1 is returned.

#### sceDmaSend

Start DMA transfer (Source Chain Mode)

Library	Introduced	Documentation last modified
libdma	1.1	October 11, 2001

#### **Syntax**

void sceDmaSend(

sceDmaChan \*d, DMA channel for performing transfer void \*tag) Starting address of transfer list

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Starts DMA transfer from memory to a device using Source Chain Mode. DMAtag, which is the beginning of the data to be transferred, is specified by tag, and the destination device (DMA channel) is specified by

The processing performed internally by this function only initiates the DMA transfer. The actual transfer is performed in the background. Also, queuing is not performed so the function is executed immediately.

In order to transfer from the SPR, the address MSB included in the DMA tag must be set to 1.

#### **Return value**

#### sceDmaSendI

Start DMA transfer (Interleave Mode, memory->SPR)

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### Syntax 1 4 1

void sceDmaSendI(

sceDmaChan \*d, DMA channel for performing transfer (to SPR only)

void \*addr, Address of transfer data int size) Size of transfer data (qword)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Starts DMA transfer from memory to SPR using Interleave Mode. The channel structure address of the SPR channel (id=9) must be specified for d.

Also, parameters indicating a small rectangular area of the transfer source must be set in advance for the DMAC via sqwc and tqwc of the sceDmaEnv structure. The SPR address, which is the transfer destination, is specified by *d->sadr*.

The processing performed internally by this function only initiates the DMA transfer. The actual transfer is performed in the background. Also, queuing is not performed so the function is executed immediately.

#### Return value

#### sceDmaSendN

Start DMA transfer (Normal Mode, memory->device)

Library	Introduced	Documentation last modified
libdma	1.1	March 26, 2001

#### **Syntax**

void sceDmaSendN(

sceDmaChan \*d, DMA channel for performing transfer

void \*addr, Transfer starting address

int size) Size of data to be transferred (qword)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Starts DMA transfer from memory to a device using Normal Mode. The starting address of the transfer data is specified by addr, and the data size is specified by size.

The processing performed internally by this function only initiates the DMA transfer. The actual transfer is performed in the background. Also, queuing is not performed so the function is executed immediately.

#### **Return value**

# sceDmaSync

Wait for end of DMA transfer and check status

Library	Introduced	Documentation last modified
libdma	1.1	October 11, 2001

#### **Syntax**

int sceDmaSync(

Relevant DMA channel sceDmaChan \*d,

int mode, Block mode 0: Block

1: Non-block

int timeout) Maximum wait interval (Hsync units)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Detects the end of DMA transfer for the channel specified by *d*.

When 0 is specified for mode, block mode is used, and control does not return from this function until the DMA transfer ends. When 1 is specified for mode, non-block mode is used, and control returns as soon as the status has been checked.

When block mode is used, a timeout occurs if the wait interval exceeds the horizontal synchronization count specified by timeout. (This function is not implemented in the current version.)

#### Return value

If the DMA transfer is in process, 1 is returned. If the DMA transfer has terminated, 0 is returned.

#### sceDmaWatch

Wait for transfer of specific address and check status

Library	Introduced	Documentation last modified
libdma	1.1	October 11, 2001

#### **Syntax**

int sceDmaWatch(

Relevant DMA channel sceDmaChan \*d,

void \*addr, Memory address to be checked

int mode. Block mode 0: Block

1: Non-block

int timeout) Maximum wait interval (Hsync units)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Checks whether or not the data at address addr has been transferred using the channel specified by d.

When 0 is specified for *mode*, block mode is used, and control does not return from this function until the specified address has been transferred. When 1 is specified for mode, non-block mode is used, and control returns as soon as the status has been checked.

When block mode is used, a timeout occurs if the wait interval exceeds the horizontal synchronization count specified by timeout. (This function is not implemented in the current version.)

#### Return value

If the address has been transferred, 1 is returned. Otherwise, 0 is returned.

# **Chapter 3: Performance Counter Library Table of Contents**

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# **Functions**

# scePcGetCounter0

Get Counter 0 value

Library	Introduced	Documentation last modified
libpc	1.1	March 26, 2001

#### **Syntax**

int scePcGetCounter0(void)

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

# **Description**

Gets the value of Counter 0.

### **Return value**

Counter 0 value

# scePcGetCounter1

Get Counter 1 value

Library	Introduced	Documentation last modified
libpc	1.1	March 26, 2001

#### **Syntax**

int scePcGetCounter1(void)

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

### **Description**

Gets the value of Counter 1.

#### **Return value**

Counter 1 value

#### scePcStart

Start Performance Counter

Library	Introduced	Documentation last modified
libpc	1.1	March 26, 2001

#### Syntax 1 4 1

#### void scePcStart(

int control. Setting of events to be counted

int counter0, Counter 0 initial value int counter1) Counter 1 initial value

# **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

#### **Description**

Starts the Performance Counter.

The control argument specifies the event to be counted and the logical OR of the processor modes for which that event is to be counted, for each counter. See the list of event codes for the corresponding event values. The processor mode values and counter enable are represented as follows:

```
SCE_PC_EXL0 (1 << 1) // exception mode for counter0
SCE PC K0 (1 << 2)
                        // kernel mode for counter0
SCE_PC_S0
              (1 << 3)
                        // supervisor mode for counter0
SCE_PC_U0
              (1 << 4)
                        // user mode for counter0
SCE_PC_EXL1 (1 << 11) // exception mode for counter1
SCE_PC_K1 (1 << 12) // kernel mode for counter1
SCE_PC_S1
              (1 << 13) // supervisor mode for counter1
              (1 << 14) // user mode for counter1
SCE_PC_U1
SCE_PC_CTE (1 << 31) // counter enable
```

If counter enable is not specified, counting will not start.

For example, to count processor cycles in all modes using Counter 0 and to count D\$ misses only in user mode using Counter 1, the value of control would be set as follows.

```
control = SCE_PC0_CPU_CYCLE | (SCE_PC_U0|SCE_PC_S0|SCE_PC_K0|SCE_PC_EXL0);
control |= SCE_PC1_DCACHE_MISS | (SCE_PC_U1);
control |= SCE_PC_CTE;
```

If only Counter 0 or only Counter 1 is to be used, be sure to specify NO EVENT for the counter event of the unused counter.

#### Return value

# scePcStop

Stop Performance Counter

Library	Introduced	Documentation last modified
libpc	1.1	March 26, 2001

#### **Syntax**

void scePcStop(void)

# **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

### **Description**

Stops the Performance Counter.

### Return value

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# **Structures**

### sceDmaPacket

Packet management information

Library	Introduced	Documentation last modified
libpkt	1.1	December 23, 1999

#### **Structure**

typedef struct \_DMAPACKET{

u\_int \*pCurrent; Address of last packet u\_long128 \*pBase; Base address of packet area u\_long128 \*pDmaTag; Address of active DMAtag

u\_int pad03; } sceDmaPacket;

# **Description**

# sceGifPacket

Packet management information

Library	Introduced	Documentation last modified
libpkt	1.1	December 23, 1999

#### **Structure**

### typedef struct {

u\_int \*pCurrent; Address of last packet

u\_long128 \*pBase; Base address of packet area u\_long128 \*pDmaTag; Address of active DMAtag

u\_long \*pGifTag; Opened GIFtag

} sceGifPacket;

### **Description**

#### sceVif0Packet

Packet management information

Library	Introduced	Documentation last modified
libpkt	1.1	December 23, 1999

#### **Structure**

#### typedef struct{

u\_int \*pCurrent; Address of last packet

u\_long128 \*pBase; Base address of packet area Address of active DMAtag u\_long128 \*pDmaTag; u\_int \*pVifCode; Address of opened VIFcode

u\_int numlen; u\_int pad11; u\_int pad12; u\_int pad13;

}sceVif0Packet;

# **Description**

#### sceVif1Packet

Packet management information

Library	Introduced	Documentation last modified
libpkt	1.1	December 23, 1999

#### **Structure**

#### typedef struct{

u\_int \*pCurrent; Address of last packet Base address of packet area u\_long128 \*pBase; u\_long128 \*pDmaTag; Address of active DMAtag u\_int \*pVifCode; Address of opened VIFcode u\_int numlen; u\_long \*pGifTag; Address of opened GIFtag u\_int pad12; u\_int pad13; }sceVif1Packet;

#### **Description**

# **Functions**

# sceDmaPkAddData

Add data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceDmaPkAddData(

sceDmaPacket \*pPacket, Pointer to packet management information structure

u\_long128 data); Data to be added to packet

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Adds 1 qword of data to the end of the packet.

#### **Return value**

#### sceDmaPkAddDataN

Add multiple items of data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceDmaPkAddDataN(

sceDmaPacket \*pPacket, Pointer to packet management information structure

Pointer to data array to be added to packet u\_long128\* pData,

u\_int count); Number of data items to be added to packet (in qwords)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Adds *count* qwords of data to the end of the packet.

#### **Return value**

#### sceDmaPkCall

Add CALL tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceDmaPkCall(

sceDmaPacket \*pPacket, Pointer to packet management information structure

u\_long128 \*pCall, Address of next DMAtag

u\_int opt1, Data to be placed in high-order bits of DMAtag (bits 95-64) Data to be placed in high-order bits of DMAtag (bits 127-96) u\_int opt2,

u\_int flag); Bit pattern specifying IRQ, PCE

#### Calling conditions

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Adds a CALL tag to the end of the packet.

Subsequently, the size of data added to the packet is counted and reflected in the size information (QWC field).

opt1, opt2 are data to be placed in the 64 high-order bits of DMAtag.

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### Return value

#### sceDmaPkCnt

Add CNT tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceDmaPkCnt(

sceDmaPacket \*pPacket, Pointer to packet management information structure

u\_int opt1, Data to be placed in high-order bits of DMAtag (bits 95-64) u\_int opt2, Data to be placed in high-order bits of DMAtag (bits 127-96)

u\_int flag); Bit pattern specifying IRQ, PCE

#### Calling conditions

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Adds a CNT tag to the end of the packet.

Subsequently, the size of data added to the packet is counted and reflected in the size information (QWC field).

opt1, opt2 are data to be placed in the 64 high-order bits of DMAtag.

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### Return value

# sceDmaPkDump

Dump contents of packet

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceDmaPkDump(

sceDmaPacket \*pPacket); Pointer to packet management information structure

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

# **Description**

Sends a hex dump of the specified packet to standard output.

This function is provided for debugging.

#### **Return value**

#### sceDmaPkEnd

Add END tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceDmaPkEnd(

sceDmaPacket \*pPacket, Pointer to packet management information structure

u\_int opt1, Data to be placed in high-order bits of DMAtag (bits 95-64) u\_int opt2, Data to be placed in high-order bits of DMAtag (bits 127-96)

u\_int flag); Bit pattern specifying IRQ, PCE

#### Calling conditions

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Adds an END tag to the end of the packet.

Subsequently, the size of data added to the packet is counted and reflected in the size information (QWC field).

opt1, opt2 are data to be placed in the 64 high-order bits of DMAtag.

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### Return value

#### sceDmaPkInit

Initialize packet

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceDmaPkInit(

sceDmaPacket \*pPacket, Pointer to packet management information structure

Base address of packet area u\_long128 \*pBase);

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Initializes packets.

pBase, pDmaTag, and pCurrent are initialized in the packet management information structure.

Enough memory must be reserved in advance for the packet area.

#### **Return value**

#### sceDmaPkNext

Add NEXT tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceDmaPkNext(

sceDmaPacket \*pPacket, Pointer to packet management information structure

u\_long128 \*pNext, Address of next tag

u\_int opt1, Data to be placed in high-order bits of DMAtag (bits 95-64) Data to be placed in high-order bits of DMAtag (bits 127-96) u\_int opt2,

u\_int flag); Bit pattern specifying IRQ, PCE

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Adds a NEXT tag to the end of the packet.

Subsequently, the size of data added to the packet is counted and reflected in the size information (QWC field).

opt1, opt2 are data to be placed in the 64 high-order bits of DMAtag.

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### Return value

#### sceDmaPkRef

Add REF tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceDmaPkRef(

sceDmaPacket \*pPacket, Pointer to packet management information structure

u\_long128 \*pRef, Address of transfer data

u\_int size, Size of transfer data (in gwords)

u\_int opt1, Data to be placed in high-order bits of DMAtag (bits 95-64) u\_int opt2, Data to be placed in high-order bits of DMAtag (bits 127-96)

u\_int flag); Bit pattern specifying IRQ, PCE

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

# **Description**

Adds a REF tag to the end of the packet.

opt1, opt2 are data to be placed in the 64 high-order bits of DMAtag.

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### **Return value**

## sceDmaPkRefe

Add REFE tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceDmaPkRefe(

sceDmaPacket \*pPacket, Pointer to packet management information structure

u\_long128 \*pRef, Address of transfer data

u\_int size, Size of transfer data (in gwords)

u\_int opt1, Data to be placed in high-order bits of DMAtag (bits 95-64) u\_int opt2, Data to be placed in high-order bits of DMAtag (bits 127-96)

u\_int flag); Bit pattern specifying IRQ, PCE

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Adds a REFE tag to the end of the packet.

opt1, opt2 are data to be placed in the 64 high-order bits of DMAtag.

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### Return value

## sceDmaPkRefs

Add REFS tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceDmaPkRefs(

sceDmaPacket \*pPacket, Pointer to packet management information structure

u\_long128 \*pRef, Address of transfer data

u\_int size, Size of transfer data (in qwords)

u\_int opt1, Data to be placed in high-order bits of DMAtag (bits 95-64) u\_int opt2, Data to be placed in high-order bits of DMAtag (bits 127-96)

u\_int flag); Bit pattern specifying IRQ, PCE

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Adds a REFS tag to the end of the packet.

opt1, opt2 are data to be placed in the 64 high-order bits of DMAtag.

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### Return value

## sceDmaPkReserve

Add data area

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

u\_int \*sceDmaPkReserve( sceDmaPacket \*pPacket,

Pointer to packet management information structure

u\_int count);

Amount of space to reserve (in words)

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Reserves an area with size count (in words) at the end of the packet area.

If there is an active DMAtag, the size of the reserved area is added to the size field in the DMAtag.

#### **Return value**

Base address of reserved area

## sceDmaPkReset

Reset packet management information

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

void sceDmaPkReset(

sceDmaPacket \*pPacket); Pointer to packet management information structure to be

reset

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Resets packet management information.

The value of *pBase* will be copied to *pCurrent*, which indicates the last packet address.

#### **Return value**

## sceDmaPkRet

Add RET tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceDmaPkRet(

sceDmaPacket \*pPacket, Pointer to packet management information structure

u\_int opt1, Data to be placed in high-order bits of DMAtag (bits 95-64) u\_int opt2, Data to be placed in high-order bits of DMAtag (bits 127-96)

u\_int flag); Bit pattern specifying IRQ, PCE

#### Calling conditions

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds a RET tag to the end of the packet.

Subsequently, the size of data added to the packet is counted and reflected in the size information (QWC field).

opt1, opt2 are data to be placed in the 64 high-order bits of DMAtag.

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### **Return value**

# sceDmaPkSize

Get packet size

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

u\_int sceDmaPkSize(

sceDmaPacket \*pPacket); Pointer to packet management information structure

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Returns the size of the packet area (in qwords).

The size can be expressed as *pCurrent - pBase*.

### **Return value**

# sceDmaPkTerminate

Terminate packet

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

## u\_long128 \*sceDmaPkTerminate(

sceDmaPacket \*pPacket);

Pointer to packet management information structure

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Terminates the packet.

In other words, size management for the active DMAtag is terminated.

### **Return value**

Next address after terminated packet.

## sceGifPkAddGsAD

Add GS packed mode A+D command

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

void sceGifPkAddGsAD(

sceGifPacket \*pPacket, Pointer to packet management information structure

u\_int address, GS command to add (register address) Parameter for GS command to be added u\_long data);

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds a GS packed mode A+D command to the packet. The GIFtag must be opened in A+D packed mode.

#### Return value

# sceGifPkAddGsData

Add GS data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

void sceGifPkAddGsData(

sceGifPacket \*pPacket, Pointer to packet management information structure

Data to be added u\_long data);

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Adds GS data to the packet. The GIFtag must be open.

#### **Return value**

## sceGifPkAddGsDataN

Add GS data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

void sceGifPkAddGsDataN(

sceGifPacket \*pPacket, Pointer to packet management information structure

u\_long\* pData, Base address of data to be added

Number of data units to be added (in 64-bit units) u\_int count);

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Adds GS data to the packet.

The GIFtag must be open.

### Return value

## sceGifPkAddGsPacked

Add GS packed mode data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceGifPkAddGsPacked(

sceGifPacket \*pPacket, Pointer to packet management information structure

Parameter to GS command to be added u\_long128 data);

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Adds data to be transferred in the GS packed mode (non A+D command) to the packet.

The GIFtag must be opened in packed mode.

#### **Return value**

## sceGifPkAddGsPackedN

Add GS packed mode data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceGifPkAddGsPackedN(

sceGifPacket \*pPacket, Pointer to packet management information structure Address of parameter for GS command to be added u\_long128 \*pData, u\_int count); Number of GS command parameters to be added

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds multiple sets of data to the packet (commands other than A+D) that will be transferred using the packed mode of the GS.

The GIFtag must be opened in packed mode.

#### Return value

## sceGifPkCall

Add CALL tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### Syntax 1 4 1

void sceGifPkCall(

sceGifPacket \*pPacket, Pointer to packet management information structure

u\_long128 \*pCall, Address of next DMAtag

u\_int opt1, Data to be placed in high-order bits of DMAtag (bits 95-64) Data to be placed in high-order bits of DMAtag (bits 127-96) u\_int opt2,

u\_int flag); Bit pattern specifying IRQ, PCE

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds a CALL tag to the end of the packet.

Subsequently, the size of data added to the packet is counted and reflected in the size information (QWC field).

opt1, opt2 are data to be placed in the 64 high-order bits of DMAtag.

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### **Return value**

# sceGifPkCloseGifTag

Close GIFtag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

void sceGifPkCloseGifTag(

sceGifPacket \*pPacket);

Pointer to packet management information structure

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Closes the GIFtag.

### **Notes**

GIFtags must be closed before terminating a packet.

### Return value

## sceGifPkCnt

Add CNT tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceGifPkCnt(

sceGifPacket \*pPacket, Pointer to packet management information structure

u\_int opt1, Data to be placed in high-order bits of DMAtag (bits 95-64) u\_int opt2, Data to be placed in high-order bits of DMAtag (bits 127-96)

u\_int flag); Bit pattern specifying IRQ, PCE

#### Calling conditions

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds a CNT tag to the end of the packet.

Subsequently, the size of data added to the packet is counted and reflected in the size information (QWC field).

opt1, opt2 are data to be placed in the 64 high-order bits of DMAtag.

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### **Return value**

# sceGifPkDump

Dump contents of packet

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

void sceGifPkDump(

sceGifPacket \*pPacket); Pointer to packet management information structure

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

## **Description**

Sends a hex dump of the specified packet to standard output.

This function is provided for debugging.

### **Return value**

## sceGifPkEnd

Add END tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### Syntax 1 4 1

void sceGifPkEnd(

sceGifPacket \*pPacket, Pointer to packet management information structure u\_int opt1, Data to be placed in high-order bits of DMAtag (bits 95-64)

u\_int opt2, Data to be placed in high-order bits of DMAtag (bits 127-96)

u\_int flag); Bit pattern specifying IRQ, PCE

#### Calling conditions

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds an END tag to the end of the packet.

Subsequently, the size of data added to the packet is counted and reflected in the size information (QWC field).

opt1, opt2 are data to be placed in the 64 high-order bits of DMAtag.

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### **Return value**

# sceGifPkInit

Initialize packet

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

void sceGifPkInit(

sceGifPacket \*pPacket, Pointer to packet structure u\_long128 \*pBase); Base address of packet area

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Initializes the packet.

### Return value

## sceGifPkNext

Add NEXT tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### Syntax 1 4 1

void sceGifPkNext(

sceGifPacket \*pPacket, Pointer to packet management information structure

u\_long128 \*pNext, Address of next DMAtag

u\_int opt1, Data to be placed in high-order bits of DMAtag (bits 95-64) Data to be placed in high-order bits of DMAtag (bits 127-96) u\_int opt2,

u\_int flag); Bit pattern specifying IRQ, PCE

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds a Next tag to the end of the packet.

opt1, opt2 are data to be placed in the 64 high-order bits of DMAtag.

Subsequently, the size of data added to the packet is counted and reflected in the size information (QWC field).

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### **Return value**

# sceGifPkOpenGifTag

Open and add GIFtag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceGifPkOpenGifTag(

sceGifPacket \*pPacket, Pointer to packet management information structure GIFtag to be added (the NLOOP field must be set to 0) u\_long128 gifTag);

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Opens the GIFtag and adds it to the end of the packet.

Size management will be performed on the GIFtag until the GIFtagis closed.

#### **Notes**

An appropriate DMAtag must be opened before opening the GIFtag.

#### **Return value**

## sceGifPkRef

Add REF tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceGifPkRef(

sceGifPacket \*pPacket, Pointer to packet management information structure

u\_long128 \*pRef, Address of transfer data

u\_int size, Size of transfer data (in qwords)

Data to be placed in high-order bits of DMAtag (bits 95-64) u\_int opt1, u\_int opt2, Data to be placed in high-order bits of DMAtag (bits 127-96)

u\_int flag); Bit pattern specifying IRQ, PCE

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds a REF tag to the end of the packet.

opt1, opt2 are data to be placed in the 64 high-order bits of DMAtag.

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### Return value

## sceGifPkRefe

Add REFE tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceGifPkRefe(

sceGifPacket \*pPacket, Pointer to packet management information structure

u\_long128 \*pRef, Address of transfer data

u\_int size, Size of transfer data (in qwords)

u\_int opt1, Data to be placed in high-order bits of DMAtag (bits 95-64) u\_int opt2, Data to be placed in high-order bits of DMAtag (bits 127-96)

u\_int flag); Bit pattern specifying IRQ, PCE

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Adds a REFE tag to the end of the packet.

opt1, opt2 are data to be placed in the 64 high-order bits of DMAtag.

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### Return value

## sceGifPkRefLoadImage

Add REF tag and image data to be transferred to the GS

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### Syntax 1 4 1

void

sceGifPkRefLoadImage(

sceGifPacket \*pPacket, Pointer to packet management information structure

Base address of destination buffer (actual address will be bp x 64) u\_short bp,

u\_char psm, Data format

> Constant Value Format SCE\_GS\_PSMCT32 0 PSMCT32 SCE\_GS\_PSMCT24 1 PSMCT24 SCE\_GS\_PSMCT16 2 PSMCT16 SCE\_GS\_PSMCT16S 10 PSMCT16S SCE\_GS\_PSMT8 PSMT8 19 PSMT4 SCE\_GS\_PSMT4 20 SCE\_GS\_PSMT8H 27 PSMT8H SCE\_GS\_PSMT4HL 36 PSMT4HL SCE\_GS\_PSMT4HH 44 PSMT4HH SCE\_GS\_PSMZ32 48 PSMZ32 SCE\_GS\_PSMZ24 49 PSMZ24 SCE\_GS\_PSMZ16 50 PSMZ16 SCE\_GS\_PSMZ16S 58 PSMZ16S

u\_short bw, Destination buffer width (actual width will be bw x 64)

u\_long128 \*image, Base address of data to be transferred Size of data to be transferred (in gwords) u int size, Upper-left coordinate of destination area  $\mathbf{u}$  int x,  $\mathbf{u}$  int y,

**u\_int** *w*, **u\_int** *h*); Width, height of transfer area

#### Calling conditions

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Adds image data to be transferred to GS local memory to the packet. If the data size is large, the data will be automatically partitioned.

#### **Notes**

When transferring texture data to the GS, the texture page buffer must be disabled.

### **Return value**

## sceGifPkRefs

Add REFS tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceGifPkRefs(

sceGifPacket \*pPacket, Pointer to packet management information structure

u\_long128 \*pRef, Address of transfer data

u\_int size, Size of transfer data (in qwords)

u\_int opt1, Data to be placed in high-order bits of DMAtag (bits 95-64) u\_int opt2, Data to be placed in high-order bits of DMAtag (bits 127-96)

u\_int flag); Bit pattern specifying IRQ, PCE

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Adds a REFS tag to the end of the packet.

opt1, opt2 are data to be placed in the 64 high-order bits of DMAtag.

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### Return value

## sceGifPkReserve

Add data area

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

u\_int \*sceGifPkReserve(

sceGifPacket \*pPacket, Pointer to packet management information structure

Amount of space to reserve (in words) u\_int count);

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Reserves an area of size count (in words) at the end of the packet.

If there is an active DMAtag or an opened GIFtag, the value isadded to the respective size field (qwc, NLOOP).

### Return value

Base address of reserved area

# sceGifPkReset

Reset packet management information

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

void sceGifPkReset(

sceGifPacket \*pPacket); Pointer to packet management information structure

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Resets the packet.

The contents of the packet will be discarded.

## **Return value**

## sceGifPkRet

Add RET tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceGifPkRet(

sceGifPacket \*pPacket, Pointer to packet management information structure

u\_int opt1, Data to be placed in high-order bits of DMAtag (bits 95-64) u\_int opt2, Data to be placed in high-order bits of DMAtag (bits 127-96)

u\_int flag); Bit pattern specifying IRQ, PCE

#### Calling conditions

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds a RET tag to the end of the packet.

Subsequently, the size of data added to the packet is counted and reflected in the size information (QWC field).

opt1, opt2 are data to be placed in the 64 high-order bits of DMAtag.

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### **Return value**

# sceGifPkSize

Get packet size

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

u\_int sceGifPkSize(

sceGifPacket \*pPacket);

Pointer to packet management information structure

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Returns the packet size (in qwords).

The size can be expressed as *pCurrent - pBase*.

### **Return value**

## sceGifPkTerminate

Terminate packet

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

## u\_long128 \*sceGifPkTerminate(

sceGifPacket \*pPacket);

Pointer to packet management information structure

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Terminates the packet.

In other words, size management for the active DMAtag is terminated.

### **Notes**

Any open GIFtags must be closed before terminating the packet.

#### **Return value**

Address after terminated packet

## sceVif0PkAddCode

Add VIF code

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

void sceVif0PkAddCode(

sceVif0Packet \*pPacket, Pointer to packet management information structure

u\_int code); VIFcode to be added

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Adds a VIF code to the end of the packet.

### **Return value**

## sceVif0PkAddData

Add VIF data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

void sceVif0PkAddData(

sceVif0Packet \*pPacket, Pointer to packet management information structure

u\_int data); Data to be added

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Adds VIF data to the end of the packet.

### **Return value**

## sceVif0PkAddDataN

Add VIF data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

void sceVif0PkAddDataN(

sceVif0Packet \*pPacket, Pointer to packet management information structure

u\_int\* pData, Base address of data to be added u\_int count); Number of sets of data to be added

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Adds VIF data to the end of the packet.

#### Return value

# sceVif0PkAddUpkData128

Add UNPACK data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

void sceVif0PkAddUpkData128(

sceVif0Packet \*pPacket, Pointer to packet management information structure

u\_long128 data); Data

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Adds 128-bit data to the open VIF UNPACK command.

#### **Return value**

# sceVif0PkAddUpkData128N

Add UNPACK data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif0PkAddUpkData128N(

sceVif0Packet \*pPacket, Pointer to packet management information structure

Base address of data to be added u\_long128 \*pData, u\_int count); Number of sets of data to be added

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds multiple sets of 128-bit data to the open VIF UNPACK command.

#### Return value

# sceVif0PkAddUpkData32

Add UNPACK data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

void sceVif0PkAddUpkData32(

sceVif0Packet \*pPacket, Pointer to packet management information structure

u\_int data); Data

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Adds 32-bit data to the open VIF UNPACK command.

#### **Return value**

# sceVif0PkAddUpkData32N

Add UNPACK data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif0PkAddUpkData32N(

sceVif0Packet \*pPacket, Pointer to packet management information structure

u\_int \*pData, Base address of data to be added u\_int count); Number of sets of data to be added

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds multiple sets of 32-bit data to the open VIF UNPACK command.

#### Return value

# sceVif0PkAddUpkData64

Add UNPACK data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif0PkAddUpkData64(

sceVif0Packet \*pPacket, Pointer to packet management information structure

u\_long data); Data

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds 64-bit data to the open VIF UNPACK command.

#### **Return value**

# sceVif0PkAddUpkData64N

Add UNPACK data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif0PkAddUpkData64N(

sceVif0Packet \*pPacket, Pointer to packet management information structure

Base address of data to be added u\_long \*pData, u\_int count); Number of sets of data to be added

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds multiple sets of 64-bit data to the open VIF UNPACK command.

#### Return value

# sceVif0PkAlign

Adjust VIF code alignment

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif0PkAlign(

sceVif0Packet \*pPacket, Pointer to packet management information structure

u\_int bit, Alignment unit

> 0:32bit 1:64bit 2:128bit

Alignment position **u\_int** *pos*);

0 - 3 (in 32-bit units)

#### Calling conditions

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds the necessary number of VIF NOPs to the end of the packet in order to adjust the alignment.

This function must be used immediately before adding the VIF MPG command to the end of the packet. The bit and pos arguments indicate the conditions for adjusting the alignment. They are specified as follows.

Table 4-1

Next VIFcode	bit	pos
MPG	1	1
Other	Alignment adjustment not needed	

### Return value

### sceVif0PkCall

Add CALL tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif0PkCall(

sceVif0Packet \*pPacket, Pointer to packet management information structure

Address of next DMAtag u\_long128 \*pCall,

u\_int flag); Bit pattern specifying IRQ, PCE

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds a CALL tag to the end of the packet.

Subsequently, the size of data added to the packet is counted and reflected in the size information (QWC field).

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

### Return value

# sceVif0PkCloseUpkCode

Close VIF UNPACK packet

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif0PkCloseUpkCode(

sceVif0Packet \*pPacket);

Pointer to packet management information structure

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Closes the VIF UNPACK command

### **Return value**

### sceVif0PkCnt

Add CNT tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceVif0PkCnt(

sceVif0Packet \*pPacket, Pointer to packet management information structure

Bit pattern specifying IRQ, PCE u\_int flag);

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds a CNT tag to the end of the packet.

Subsequently, the size of data added to the packet is counted and reflected in the size information (QWC field).

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

### Return value

# sceVif0PkDump

Dump contents of packet

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif0PkDump(

sceVif0Packet \*pPacket);

Pointer to packet management information structure

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

### **Description**

Sends a hex dump of the specified packet to standard output.

This function is provided for debugging.

#### Return value

### sceVif0PkEnd

Add END tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceVif0PkEnd(

sceVif0Packet \*pPacket, Pointer to packet management information structure

Bit pattern specifying IRQ, PCE u\_int flag);

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds an END tag to the end of the packet.

Subsequently, the size of data added to the packet is counted and reflected in the size information (QWC field).

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

### Return value

# sceVif0PkInit

Initialize packet

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif0PkInit(

sceVif0Packet \*pPacket, Pointer to packet management information structure

u\_long128 \*pBase); Base address of packet area

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Initializes the packet.

### **Return value**

### sceVif0PkNext

Add NEXT tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceVif0PkNext(

sceVif0Packet \*pPacket, Pointer to packet management information structure

Address of next DMAtag u\_long128 \*pNext,

u\_int flag); Bit pattern specifying IRQ, PCE

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds a NEXT tag to the end of the packet.

Subsequently, the size of data added to the packet is counted and reflected in the size information (QWC field).

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

### Return value

## sceVif0PkOpenUpkCode

Open VIF UNPACK packet

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

### void sceVif0PkOpenUpkCode(

sceVif0Packet \*pPacket, Pointer to packet management information structure u\_short vuaddr, Address of VU Mem0 for data transfer destination

u\_int upkcmd, UNPACK command code (8 bit)

u\_int c/, Cycle register (cl) count u\_int w/); Cycle register (wl) count

### Calling conditions

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Opens the VIF UNPACK command and adds it to the end of the packet.

Subsequently, the NUM field in the VIFcode will be managed until the command is closed using sceVif0PkCloseUpkCode(). When data is added to the packet with a function such as sceVif0PkAddUpkData32(), the field will be set according to the data size.

#### **Notes**

An appropriate DMAtag must be added to the packet beforehand.

#### **Return value**

### sceVif0PkRef

Add REF tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif0PkRef(

sceVif0Packet \*pPacket, Pointer to packet management information structure

u\_long128 \*pRef, Address of transfer data

u\_int size, Size of transfer data (in qwords)

u\_int opt1, Data to be placed in high-order bits of DMAtag (bits 95-64) u\_int opt2, Data to be placed in high-order bits of DMAtag (bits 127-96)

u\_int flag); Bit pattern specifying IRQ, PCE

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds a REF tag to the end of the packet.

opt1, opt2 are data to be placed in the 64 high-order bits of DMAtag.

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### Return value

### sceVif0PkRefe

Add REFE tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

### void sceVif0PkRefe(

sceVif0Packet \*pPacket, Pointer to packet management information structure

u\_long128 \*pRef, Address of transfer data

u\_int size, Size of transfer data (in gwords)

Data to be placed in high-order bits of DMAtag (bits 95-64) u\_int opt1, u\_int opt2, Data to be placed in high-order bits of DMAtag (bits 127-96)

u\_int flag); Bit pattern specifying IRQ, PCE

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds a REFE tag to the end of the packet.

opt1, opt2 are data to be placed in the 64 high-order bits of DMAtag.

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### Return value

## sceVif0PkRefMpg

Add REF tag and microprogram

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceVif0PkRefMpg(

sceVif0Packet \*pPacket, Pointer to packet management information structure MicroMem0 address of destination (in 64-bit units) u\_short vuaddr,

u\_long128 \*pMicro, Destination address

u\_int size, Size of microprogram (in qword units)

**u\_int** *opt1*); VIFcode to be placed in empty areas. Usually VIF NOP.

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Loads the indicated microprogram to MicroMem0.

A REF tag is generated and added to the end of the packet.

### **Return value**

### sceVif0PkRefs

Add REFS tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceVif0PkRefs(

sceVif0Packet \*pPacket, Pointer to packet management information structure

u\_long128 \*pRef, Address of transfer data

u\_int size, Size of transfer data (in qwords)

Data to be placed in high-order bits of DMAtag (bits 95-64) u\_int opt1, u\_int opt2, Data to be placed in high-order bits of DMAtag (bits 127-96)

u\_int flag); Bit pattern specifying IRQ, PCE

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds a REFS tag to the end of the packet.

opt1, opt2 are data to be placed in the 64 high-order bits of DMAtag.

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### Return value

### sceVif0PkReserve

Add data area

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

u\_int \*sceVif0PkReserve(

sceVif0Packet \*pPacket, Pointer to packet management information structure

Amount of space to reserve (in words) u\_int count);

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Reserves an area of size count (in words) at the end of the packet.

If an active DMAtag or an open VIF UNPACK command is available, the size of the reserved area will be reflected in their respective size fields (qwc, NUM).

### Return value

Base address of reserved area

## sceVif0PkReset

Reset packet management information

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif0PkReset(

sceVif0Packet \*pPacket);

Pointer to packet management information structure

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Resets packet management information.

The value of pBase is copied to pCurrent, which holds the end of the packet.

#### **Return value**

### sceVif0PkRet

Add RET tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif0PkRet(

sceVif0Packet \*pPacket, Pointer to packet management information structure

Bit pattern specifying IRQ, PCE u\_int flag);

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds a RET tag to the end of the packet.

Subsequently, the size of data added to the packet is counted and reflected in the size information (QWC field).

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

### Return value

# sceVif0PkSize

Get packet size

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

u\_int sceVif0PkSize(

sceVif0Packet \*pPacket)

Pointer to packet management information structure

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Returns the size of the data (in qwords) registered in the packet.

The size can be expressed as pCurrent - pBase.

### **Return value**

### sceVif0PkTerminate

Terminate packet

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

### u\_long128 \*sceVif0PkTerminate(

sceVif0Packet \*pPacket);

Pointer to packet management information structure

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

This function terminates the packet.

In other words, size management for the active DMAtag is terminated.

#### **Notes**

If a VIF UNPACK code is open, it must be closed before the packet is terminated.

#### **Return value**

Address after terminated packet

## sceVif1PkAddCode

Add VIF code

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif1PkAddCode(

sceVif1Packet \*pPacket, Pointer to packet management information structure

u\_int code); VIFcode to be added

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Adds the VIF code to the end of the packet.

### **Return value**

## sceVif1PkAddData

Add VIF data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif1PkAddData(

sceVif1Packet \*pPacket, Pointer to packet management information structure

u\_int data); Data to be added

**Calling conditions** 

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

**Description** 

Adds VIF data to the end of the packet.

**Return value** 

## sceVif1PkAddDataN

Add VIF data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif1PkAddDataN(

sceVif1Packet \*pPacket, Pointer to packet management information structure

u\_int\* pData, Base address of data to be added u\_int count); Number of sets of data to be added

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds multiple sets of VIF data to the end of the packet.

#### **Return value**

## sceVif1PkAddDirectData

Add DIRECT data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif1PkAddDirectData(

sceVif1Packet \*pPacket, Pointer to packet management information structure

u\_long128 data); Data to be added

**Calling conditions** 

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

**Description** 

Adds data to the open VIF DIRECT command.

**Return value** 

## sceVif1PkAddDirectDataN

Add DIRECT data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif1PkAddDirectDataN(

sceVif1Packet \*pPacket, Pointer to packet management information structure

Base address of data to be added u\_long128\* pData, u\_int count); Number of sets of data to be added

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds multiple sets of data to the open VIF DIRECT command.

#### Return value

### sceVif1PkAddGsAD

Add GS packed mode A+D command

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

## void sceVif1PkAddGsAD(

sceVif1Packet \*pPacket, Pointer to packet management information structure

u\_int address, GS command number u\_long data); GS command parameter

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds the GS packed mode A+D command to the packet.

The GIFtag must be open in packed mode A+D.

#### **Return value**

## sceVif1PkAddGsData

Add GS data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif1PkAddGsData(

sceVif1Packet \*pPacket, Pointer to packet management information structure

Data to be added u\_long data);

**Calling conditions** 

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

**Description** 

Adds GS data to the packet.

The GIFtag must be open.

#### **Return value**

### sceVif1PkAddGsDataN

Add GS data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif1PkAddGsDataN(

sceVif1Packet \*pPacket, Pointer to packet management information structure

Base address of data to be added u\_long\* pData,

Number of sets of data to be added (in 64-bit units) u\_int count);

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds multiple sets of GS data to the packet.

The GIFtag must be open.

#### **Return value**

### sceVif1PkAddGsPacked

Add GS packed mode data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif1PkAddGsPacked(

sceVif1Packet \*pPacket, Pointer to packet management information structure

GS command parameter to be added u\_long128 data);

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds data to be transferred in GS packed mode to the packet (commands other than A+D).

The GIFtag must be open in packed mode.

#### **Return value**

### sceVif1PkAddGsPackedN

Add GS packed mode data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceVif1PkAddGsPackedN(

sceVif1Packet \*pPacket, Pointer to packet management information structure Address of GS command parameter to be added u\_long128 \*pData, u\_int count); Number of GS command parameters to be added

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds multiple sets of data to be transferred in GS packed mode to the packet (commands other than A+D).

The GIFtag must be open in packed mode.

#### Return value

# sceVif1PkAddUpkData128

Add UNPACK data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif1PkAddUpkData128(

sceVif1Packet \*pPacket, Pointer to packet management information structure

u\_long128 data); Data

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds 128-bit data to the open VIF UNPACK command.

#### **Return value**

# sceVif1PkAddUpkData128N

Add UNPACK data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif1PkAddUpkData128N(

sceVif1Packet \*pPacket, Pointer to packet management information structure

Base address of data to be added u\_long128 \*pData, u\_int count); Number of sets of data to be added

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds multiple sets of 128-bit data to the open VIF UNPACK command.

#### Return value

# sceVif1PkAddUpkData32

Add UNPACK data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif1PkAddUpkData32(

sceVif1Packet \*pPacket, Pointer to packet management information structure

u\_int data); Data

**Calling conditions** 

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

**Description** 

Adds 32-bit data to the open VIF UNPACK command.

**Return value** 

# sceVif1PkAddUpkData32N

Add UNPACK data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

## void sceVif1PkAddUpkData32N(

sceVif1Packet \*pPacket, Pointer to packet management information structure

u\_int \*pData, Base address of data to be added u\_int count); Number of sets of data to be added

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds multiple sets of 32-bit data to the open VIF UNPACK command.

#### Return value

# sceVif1PkAddUpkData64

Add UNPACK data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

void sceVif1PkAddUpkData64(

sceVif1Packet \*pPacket, Pointer to packet management information structure

u\_long data); Data

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds 64-bit data to the open VIF UNPACK command.

#### **Return value**

# sceVif1PkAddUpkData64N

Add UNPACK data

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

### **Syntax**

## void sceVif1PkAddUpkData64N(

sceVif1Packet \*pPacket, Pointer to packet management information structure

Base address of data to be added u\_long \*pData, u\_int count); Number of sets of data to be added

### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

### **Description**

Adds multiple sets of 64-bit data to the open VIF UNPACK command.

#### Return value

# sceVif1PkAlign

Adjust alignment of VIFcode

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceVif1PkAlign(

sceVif1Packet \*pPacket, Pointer to packet management information structure

u\_int bit, Alignment unit

> 0:32bit 1:64bit 2:128bit

Alignment position **u\_int** *pos*);

0-3 (in 32-bit units)

#### Calling conditions

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Adds the necessary number of VIF NOPs to the end of the packet in order to adjust the alignment.

This function must be used immediately before adding the VIF MPG/VIF DIRECT/VIF DIRECTHL commands to the end of the packet.

The bit and pos arguments indicate the conditions for adjusting the alignment. They are specified as follows.

Table 4-2

Next VIFcode	bit	pos
MPG	1	1
DIRECT	2	3
DIRECTHL	2	3
Other	Alignment adjustment not needed	

#### **Return value**

## sceVif1PkCall

Add CALL tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceVif1PkCall(

sceVif1Packet \*pPacket, Pointer to packet management information structure

Address of next DMAtag u\_long128 \*pCall,

u\_int flag); Bit pattern specifying IRQ, PCE

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Adds a CALL tag.

Subsequently, the size of data added to the packet is counted and reflected in the size information (QWC field).

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### Return value

# sceVif1PkCloseDirectCode

Close VIF DIRECT packet

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

void sceVif1PkCloseDirectCode(

sceVif1Packet \*pPacket);

Pointer to packet management information structure

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Closes the VIF DIRECT command.

#### **Notes**

If there are any open GIFtags, they must be closed before this function is used to close the VIF DIRECT command.

#### Return value

## sceVif1PkCloseDirectHLCode

Close VIF DIRECTHL packet

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

void sceVif1PkCloseDirectHLCode(

sceVif1Packet \*pPacket); Pointer to packet management information structure

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

This function closes the VIF DIRECTHL command.

#### **Notes**

If there are any open GIFtags, they must be closed before this function is used to close the VIF DIRECTHL command.

#### Return value

# sceVif1PkCloseGifTag

Close GIFtag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

void sceVif1PkCloseGifTag(

sceVif1Packet \*pPacket);

Pointer to packet management information structure

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Closes the GIFtag.

#### **Return value**

# sceVif1PkCloseUpkCode

Close VIF UNPACK packet

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

void sceVif1PkCloseUpkCode(

sceVif1Packet \*pPacket);

Pointer to packet management information structure

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Closes the VIF UNPACK command.

#### **Return value**

## sceVif1PkCnt

Add CNT tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceVif1PkCnt(

sceVif1Packet \*pPacket, Pointer to packet management information structure

Bit pattern specifying IRQ, PCE u\_int flag);

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Adds a CNT tag to the end of the packet.

Subsequently, the size of data added to the packet is counted and reflected in the size information (QWC field).

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### Return value

# sceVif1PkDump

Dump contents of packet

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

void sceVif1PkDump(

sceVif1Packet \*pPacket); Pointer to packet management information structure

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Not multithread safe

## **Description**

Sends a hex dump of the specified packet to standard output.

This function is provided for debugging.

#### **Return value**

## sceVif1PkEnd

Add END tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceVif1PkEnd(

sceVif1Packet \*pPacket, Pointer to packet management information structure

Bit pattern specifying IRQ, PCE u\_int flag);

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Adds an END tag to the end of the packet.

Subsequently, the size of data added to the packet is counted and reflected in the size information (QWC field).

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### Return value

# sceVif1PkInit

Initialize packet

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

void sceVif1PkInit(

sceVif1Packet \*pPacket, Pointer to packet management information structure

u\_long128 \*pBase); Base address of packet

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Initializes the packet.

#### Return value

## sceVif1PkNext

Add NEXT tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceVif1PkNext(

sceVif1Packet \*pPacket, Pointer to packet management information structure

address for next DMAtag u\_long128 \*pNext,

u\_int flag); Bit pattern specifying IRQ, PCE

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Adds a NEXT tag to the end of the packet.

Subsequently, the size of data added to the packet is counted and reflected in the size information (QWC field).

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### Return value

# sceVif1PkOpenDirectCode

Open VIF DIRECT packet

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceVif1PkOpenDirectCode( sceVif1Packet \*pPacket,

int stall);

Pointer to packet management information structure

Interrupt flag (i-bit) setting 0: No interrupts (i=0)

1: Generate interrupt (i=1)

#### Calling conditions

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Opens the VIF DIRECT command after adjusting the alignment and adds it to the end of the packet.

Subsequently, the NUM field in the VIFcode will be managed until the command is closed using sceVif1PkCloseDirectCode(). When data is added to the packet with a function such as sceVif1PkAddDirectData(), the field will be set according to the data size.

#### **Return value**

## sceVif1PkOpenDirectHLCode

Open VIF DIRECTHL packet

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceVif1PkOpenDirectHLCode( sceVif1Packet \*pPacket,

int stall);

Pointer to packet management information structure

Interrupt flag (i-bit) setting 0: No interrupts (i=0)

1: Generate interrupt (i=1)

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Opens the VIF DIRECTHL command after adjusting the alignment and adds it to the end of the packet.

Subsequently, the NUM field in the VIFcode will be managed until the command is closed using sceVif1PkCloseDirectHLCode(). When data is added to the packet with a function such as sceVif1PkAddDirectData(), the field will be set according to the data size.

#### **Return value**

# sceVif1PkOpenGifTag

Open GIFtag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceVif1PkOpenGifTag(

sceVif1Packet \*pPacket, Pointer to packet management information structure

GIFtag to be added u\_long128 gifTag);

**Calling conditions** 

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

This function opens the GIFtag and adds it to an already open VIF DIRECT command.

Subsequently, the qwc field in the GIFtag will be managed until the command is closed using sceVif1PkCloseGifTag(). When data is added to the packet with a function such as sceVif1PkAddGsData(), the field will be set according to the data size.

#### **Return value**

## sceVif1PkOpenUpkCode

Open VIF UNPACK packet

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

## void sceVif1PkOpenUpkCode(

sceVif1Packet \*pPacket, Pointer to packet management information structure u\_short vuaddr, Address of VU Mem1 for data transfer destination

u\_int upkcmd, UNPACK command code (8 bit)

u\_int c/, Cycle register (cl) count u\_int w/); Cycle register (wl) count

#### Calling conditions

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Opens the VIF UNPACK command and adds it to the end of the packet.

Subsequently, the NUM field in the VIFcode will be managed until the command is closed using sceVif1PkCloseUpkCode(). When data is added to the packet with a function such as sceVif1PkAddUpkData32(), the field will be set according to the data size.

#### **Notes**

An appropriate DMAtag must be added to the packet beforehand.

#### **Return value**

## sceVif1PkRef

Add REF tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceVif1PkRef(

sceVif1Packet \*pPacket, Pointer to packet management information structure

u\_long128 \*pRef, Address of transfer data

u\_int size, Size of transfer data (in qwords)

u\_int opt1, Data to be placed in high-order bits of DMAtag (bits 95-64) u\_int opt2, Data to be placed in high-order bits of DMAtag (bits 127-96)

u\_int flag); Bit pattern specifying IRQ, PCE

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Adds a REF tag to the end of the packet.

opt1, opt2 are data to be placed in the 64 high-order bits of DMAtag.

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### Return value

## sceVif1PkRefe

Add REFE tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceVif1PkRefe(

sceVif1Packet \*pPacket, Pointer to packet management information structure

u\_long128 \*pRef, Address of transfer data

u\_int size, Size of transfer data (in qwords)

u\_int opt1, Data to be placed in high-order bits of DMAtag (bits 95-64) u\_int opt2, Data to be placed in high-order bits of DMAtag (bits 127-96)

u\_int flag); Bit pattern specifying IRQ, PCE

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Adds a REFE tag to the end of the packet.

opt1, opt2 are data to be placed in the 64 high-order bits of DMAtag.

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### Return value

## sceVif1PkRefLoadImage

Add REF tag and image data to be transferred to the GS

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### Syntax 1 4 1

void sceVif1PkRefLoadImage(

sceVif1Packet \*pPacket, Pointer to packet management information

u\_short bp, Base address of destination buffer

(actual address will be bp x 64)

Data format u\_char psm,

> Constant Value Format SCE\_GS\_PSMCT32 0 PSMCT32 SCE GS PSMCT24 1 PSMCT24 SCE GS PSMCT16 2 PSMCT16 SCE GS PSMCT16S 10 PSMCT16S SCE\_GS\_PSMT8 19 PSMT8 SCE\_GS\_PSMT4 20 PSMT4 SCE GS PSMT8H 27 PSMT8H PSMT4HL SCE\_GS\_PSMT4HL 36 SCE\_GS\_PSMT4HH 44 PSMT4HH SCE GS PSMZ32 48 PSMZ32 PSMZ24 SCE GS PSMZ24 49 PSMZ16 SCE\_GS\_PSMZ16 50 SCE GS PSMZ16S 58 PSMZ16S

u short bw, Destination buffer width (actual width will be bw x 64)

u\_long128 \*image, Base address of data to be transferred u\_int size, Size of data to be transferred (in gwords) Upper-left coordinate of destination area u\_int x, u\_int y,

**u\_int** *w*, **u\_int** *h*); Width, height of transfer area

#### Calling conditions

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Adds image data to be transferred to GS local memory along with a REF tag to the packet. If the data size is large, the data will be automatically partitioned.

#### **Notes**

When transferring texture data to the GS, the texture page buffer must be disabled.

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## Return value

## sceVif1PkRefMpg

Add REF tag and microprogram

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceVif1PkRefMpg(

sceVif1Packet \*pPacket, Pointer to packet management information structure

MicroMem1 address of transfer destination (in 64-bit units) u\_short vuaddr,

u\_long128 \*pMicro, Destination address

u\_int size, Size of microprogram (in qword units)

**u\_int** *opt1*); VIFcode to be placed in empty areas. usually VIF NOP.

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

#### **Description**

Loads the specified microprogram to MicroMem1.

A REF tag is generated and added to the end of the packet.

#### **Return value**

## sceVif1PkRefs

Add REFS tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceVif1PkRefs(

sceVif1Packet \*pPacket, Pointer to packet management information structure

u\_long128 \*pRef, Address of transfer data

u\_int size, Size of transfer data (in qwords)

u\_int opt1, Data to be placed in high-order bits of DMAtag (bits 95-64) u\_int opt2, Data to be placed in high-order bits of DMAtag (bits 127-96)

u\_int flag); Bit pattern specifying IRQ, PCE

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Adds a REFS tag to the end of the packet.

opt1, opt2 are data to be placed in the 64 high-order bits of DMAtag.

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### Return value

## sceVif1PkReserve

Add data area

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

u\_int \*sceVif1PkReserve(

sceVif1Packet \*pPacket, Pointer to packet management information structure

Amount of space to reserve (in words) u\_int count);

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Reserves an area of size count (in words) at the end of the packet.

If an active DMAtag or an open VIFcode/GIFtag is available, the size of the reserved area will be reflected in their respective size fields.

#### Return value

Base address of reserved area

## sceVif1PkReset

Reset packet management information

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

## void sceVif1PkReset(

sceVif1Packet \*pPacket);

Pointer to packet management information structure

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Resets packet management information.

The value of pBase is copied to pCurrent, which holds the end of the packet.

#### **Return value**

## sceVif1PkRet

Add RET tag

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

void sceVif1PkRet(

sceVif1Packet \*pPacket, Pointer to packet management information structure

Bit pattern specifying IRQ, PCE u\_int flag);

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Adds a RET tag to the end of the packet.

Subsequently, the size of data added to the packet is counted and reflected in the size information (QWC field).

Bit 31 of flag indicates the IRQ and bits 26-27 indicate the PCE.

The 32 low-order bits of DMAtag are OR'ed so all other bits must be set to 0.

#### Return value

# sceVif1PkSize

Get packet size

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

## **Syntax**

u\_int sceVif1PkSize(

sceVif1Packet \*pPacket);

Pointer to packet management information structure

## **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Returns the size of the packet (in qwords).

The size can be expressed as pCurrent - pBase.

#### **Return value**

## sceVif1PkTerminate

Terminate packet

Library	Introduced	Documentation last modified
libpkt	1.1	March 26, 2001

#### **Syntax**

## u\_long128 \*sceVif1PkTerminate(

sceVif1Packet \*pPacket);

Pointer to packet management information structure

#### **Calling conditions**

Can be called from an interrupt handler

Can be called from a thread

Multithread safe

## **Description**

Terminates the packet.

In other words, size management for the active DMAtag is terminated.

#### **Notes**

If there are any open VIFcodes or GIFtags, they must be closed before the packet is terminated.

#### **Return value**

Address after the terminated packet

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## **Structures**

## sceScfT10kConfig

Structure for setting up DTL-T10000 system configuration information

Library	Introduced	Documentation last modified
libscf	2.0	January 2, 2001

#### **Structure**

#### #include <libscf.h>

#### typedef struct {

**short** *TimeZone*; Difference between local standard time and GMT (in minutes)

u\_char Aspect; Aspect ratio setting for television screen

Date format **u\_char** *DateNotation* u\_char Language; Language setting SPDIF setting u\_char Spdif;

Daylight savings setting u\_char SummerTime;

u\_char TimeNotation; Time format

} sceScfT10kConfig;

#### **Description**

This structure is used to set up system configuration information for the DTL-T10000.

The allowable values for each of the members are given below.

#### <TimeZone>

The difference, in minutes, between the local standard time (local time if daylight savings is not in effect) and GMT.

Example: Tokyo 540, London 0

## <Aspect>

SCE\_ASPECT\_43 4:3

SCE\_ASPECT\_FULL Full-screen

SCE\_ASPECT\_169 16:9

#### <DateNotation>

SCE\_DATE\_YYYYMMDD Year/Month/Day (SPCH-10000, SPCH-15000)

SCE\_DATE\_MMDDYYYY Month/Day/Year SCE\_DATE\_DDMMYYYY Day/Month/Year

#### < Language>

SCE\_JAPANESE\_LANGUAGE Japanese SCE\_ENGLISH\_LANGUAGE English SCE\_FRENCH\_LANGUAGE French SCE\_SPANISH\_LANGUAGE Spanish SCE\_GERMAN\_LANGUAGE German SCE\_ITALIAN\_LANGUAGE Italian SCE\_DUTCH\_LANGUAGE Dutch

SCE\_PORTUGUESE\_LANGUAGE Portuguese

#### <Spdif>

SCE\_SPDIF\_ON SPDIF ON SCE\_SPDIF\_OFF SPDIF OFF

#### <SummerTime>

SCE\_SUMMERTIME\_OFF No daylight savings (SPCH-10000, SPCH-15000)

SCE\_SUMMERTIME\_ON Daylight savings

## <TimeNotation>

SCE\_TIME\_24HOUR 24-hour format (SPCH-10000, SPCH-15000)

SCE\_TIME\_12HOUR 12-hour (AM/PM) format

#### See also

sceScfSetT10kConfig

# **Functions**

## sceScfGetAspect

Get the aspect ratio for the television screen

Library	Introduced	Documentation last modified
libscf	2.0	March 26, 2001

#### **Syntax**

#include <libscf.h>

int sceScfGetAspect (void)

## **Calling conditions**

Can be called from a thread

Not multithread safe (must be called in an interrupt-enabled state)

#### **Description**

This function gets the video screen aspect ratio from the detailed settings in the system configuration information.

#### **Return value**

Return value	Meaning
SCE_ASPECT_43	4:3
SCE_ASPECT_FULL	Full-screen
SCE_ASPECT_169	16:9

## sceScfGetDateNotation

Get date format

Library	Introduced	Documentation last modified
libscf	2.0	March 26, 2001

## **Syntax**

#include <libscf.h>

int sceScfGetDateNotation (void)

#### **Calling conditions**

Can be called from a thread

Not multithread safe (must be called in an interrupt-enabled state)

## **Description**

This function returns the date display format from the system configuration information.

The current Japanese models of the PlayStation 2 (SPCH-10000, SPCH-15000) will return SCE\_DATE\_YYYMMDD.

#### **Return value**

Return value	Description
SCE_DATE_YYYYMMDD	Year/Month/Day (SPCH-10000, SPCH-15000)
SCE_DATE_MMDDYYYY	Month/Day/Year
SCE_DATE_DDMMYYYY	Day/Month/Year

## sceScfGetGMTfromRTC

Convert from JST to GMT

Library	Introduced	Documentation last modified
libscf	2.0	March 26, 2001

#### **Syntax**

#include <libscf.h>

void sceScfGetGMTfromRTC (

sceCdCLOCK \*rtc)

Address of structure holding date and time

#### **Calling conditions**

Can be called from a thread

Not multithread safe (must be called in an interrupt-enabled state)

#### **Description**

The structure pointed to by the rtc parameter will be treated as JST (Japan Standard time) and will be converted to GMT (Greenwich Mean Time). The converted values will be written back to the same structure.

The sceCdCLOCK structure can only store dates from January 1, 2000 through December 31, 2099.

For example, if Japan Standard Time is 6:00 January 1, 2000, GMT would be 21:00 December 31, 1999. This function would write 99/12/31 21:00.

#### **Return value**

# sceScfGetLanguage

Get the language setting

Library	Introduced	Documentation last modified
libscf	2.0	March 26, 2001

## **Syntax**

#include <libscf.h>

int sceScfGetLanguage (void)

## **Calling conditions**

Can be called from a thread

Not multithread safe (must be called in an interrupt-enabled state)

## **Description**

This function gets the language setting from the system configuration information.

#### **Return value**

Return value	Description
SCE_JAPANESE_LANGUAGE	Japanese
SCE_ENGLISH_LANGUAGE	English
SCE_FRENCH_LANGUAGE	French
SCE_SPANISH_LANGUAGE	Spanish
SCE_GERMAN_LANGUAGE	German
SCE_ITALIAN_LANGUAGE	Italian
SCE_DUTCH_LANGUAGE	Dutch
SCE_PORTUGUESE_LANGUAGE	Portuguese

## sceScfGetLocalTimefromRTC

Convert from JST to local time

Library	Introduced	Documentation last modified
libscf	2.0	March 26, 2001

#### **Syntax**

#include <libscf.h> void sceScfGetLocalTimefromRTC ( sceCdCLOCK \*rtc)

Address of structure holding date and time

## **Calling conditions**

Can be called from a thread

Not multithread safe (must be called in an interrupt-enabled state)

#### **Description**

The structure pointed to by the rtc parameter will be treated as JST (Japan Standard time) and will be converted to local time, taking the time zone and daylight savings time into consideration. The converted values will be written back to the same structure.

The sceCdCLOCK structure can only store dates from January 1, 2000 through December 31, 2099.

For example, if Japan Standard Time is 6:00 January 1, 2000, then U.S. Eastern standard time (GMT-5 hours) would be 16:00 December 31, 1999. This function would write 99/12/31 16:00.

#### Return value

# sceScfGetSpdif

Return whether SPDIF is enabled

Library	Introduced	Documentation last modified
libscf	2.0	March 26, 2001

## **Syntax**

#include <libscf.h> int sceScfGetSpdif (void)

## **Calling conditions**

Can be called from a thread

Not multithread safe (must be called in an interrupt-enabled state)

## **Description**

This function gets information about whether the SPDIF is enabled in the system configuration information.

#### **Return value**

Return value	Meaning
SCE_SPDIF_ON	SPDIF is ON
SCE_SPDIF_OFF	SPDIF is OFF

## sceScfGetSummerTime

Return whether daylight savings is in effect

Library	Introduced	Documentation last modified
libscf	2.0	March 26, 2001

## **Syntax**

#include <libscf.h>

int sceScfGetSummerTime (void)

## **Calling conditions**

Can be called from a thread

Not multithread safe (must be called in an interrupt-enabled state)

## **Description**

This function returns whether or not daylight savings is set in the system configuration information.

The current Japanese models of the PlayStation 2 (SPCH-10000, SPCH-15000) will return SCE\_SUMMERTIME\_OFF.

#### **Return value**

Return value	Meaning
SCE_SUMMERTIME_OFF	No daylight savings (SPCH-10000, SPCH-15000)
SCE_SUMMERTIME_ON	Daylight savings in effect

## sceScfGetTimeNotation

Return time format

Library	Introduced	Documentation last modified
libscf	2.0	March 26, 2001

#### **Syntax**

#include <libscf.h>

int sceScfGetTimeNotation (void)

#### **Calling conditions**

Can be called from a thread

Not multithread safe (must be called in an interrupt-enabled state)

## **Description**

This function returns the time format in the system configuration information.

If SCE\_TIME\_24HOUR is returned, the time will be displayed in 24-hour format (e.g., 15:23). If SCE\_TIME\_12HOUR is returned, time will be displayed in 12-hour format (e.g., 3:23PM).

The current Japanese models of the PlayStation 2 (SPCH-10000, SPCH-15000) will return SCE\_TIME\_24HOUR.

#### Return value

Return value	Meaning
SCE_TIME_24HOUR	24-hour format (SPCH-10000,SPCH-15000)
SCE_TIME_12HOUR	12-hour (AM/PM) format

## sceScfGetTimeZone

Return time zone information

Library	Introduced	Documentation last modified
libscf	2.0	March 26, 2001

## **Syntax**

#include <libscf.h>

int sceScfGetTimeZone (void)

## **Calling conditions**

Can be called from a thread

Not multithread safe (must be called in an interrupt-enabled state)

## **Description**

The current Japanese models of the PlayStation 2 (SPCH-10000, SPCH-15000) will return 540.

## **Return value**

The difference, in minutes, between local time and GMT will be returned.

# sceScfSetT10kConfig

Set up DTL-T10000 system configuration information

Library	Introduced	Documentation last modified
libscf	2.0	March 26, 2001

## **Syntax**

#include <libscf.h>

void sceScfSetT10kConfig(

sceScfT10kConfig \*config)

Address of system configuration information structure

## **Calling conditions**

Can be called from a thread

Not multithread safe (must be called in an interrupt-enabled state)

## **Description**

This function sets up system configuration information for the DTL-T10000.

It will have no effect on units other than the DTL-T10000.

#### **Return value**