

ROCmSMI

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Contents

1	ROCm System Management Interface (ROCm SMI) Library	1
2	Data Structure Index	3
2.1	Data Structures	3
3	File Index	5
3.1	File List	5
4	Data Structure Documentation	7
4.1	rsmi_freq_volt_region Struct Reference	7
4.1.1	Detailed Description	7
4.2	rsmi_frequencies Struct Reference	7
4.2.1	Detailed Description	8
4.2.2	Field Documentation	8
4.2.2.1	num_supported	8
4.2.2.2	current	8
4.2.2.3	frequency	8
4.3	rsmi_od_vddc_point Struct Reference	8
4.3.1	Detailed Description	9
4.4	rsmi_od_volt_freq_data Struct Reference	9
4.4.1	Detailed Description	9
4.4.2	Field Documentation	9
4.4.2.1	curr_mclk_range	9
4.5	rsmi_pcie_bandwidth Struct Reference	10
4.5.1	Detailed Description	10

4.5.2	Field Documentation	10
4.5.2.1	transfer_rate	10
4.5.2.2	lanes	10
4.6	rsmi_power_profile_status Struct Reference	10
4.6.1	Detailed Description	11
4.6.2	Field Documentation	11
4.6.2.1	available_profiles	11
4.6.2.2	current	11
4.6.2.3	num_profiles	11
4.7	rsmi_range Struct Reference	11
4.7.1	Detailed Description	11
4.8	rsmi_version Struct Reference	12
4.8.1	Detailed Description	12
5	File Documentation	13
5.1	rocm_smi.h File Reference	13
5.1.1	Detailed Description	16
5.1.2	Macro Definition Documentation	16
5.1.2.1	RSMI_MAX_FAN_SPEED	16
5.1.3	Enumeration Type Documentation	16
5.1.3.1	rsmi_status_t	16
5.1.3.2	rsmi_dev_perf_level	17
5.1.3.3	rsmi_clk_type	17
5.1.3.4	rsmi_temperature_metric	18
5.1.3.5	rsmi_power_profile_preset_masks	18
5.1.3.6	rsmi_freq_ind	18
5.1.4	Function Documentation	19
5.1.4.1	rsmi_init(uint64_t init_flags)	19
5.1.4.2	rsmi_shut_down(void)	19
5.1.4.3	rsmi_num_monitor_devices(uint32_t *num_devices)	19
5.1.4.4	rsmi_dev_pci_bandwidth_get(uint32_t dv_ind, rsmi_pcie_bandwidth *bandwidth)	19

5.1.4.5	<code>rsmi_dev_busy_percent_get(uint32_t dv_ind, uint32_t *busy_percent)</code>	20
5.1.4.6	<code>rsmi_dev_pci_bandwidth_set(uint32_t dv_ind, uint64_t bw_bitmask)</code>	20
5.1.4.7	<code>rsmi_dev_pci_id_get(uint32_t dv_ind, uint64_t *bdfid)</code>	21
5.1.4.8	<code>rsmi_dev_id_get(uint32_t dv_ind, uint64_t *id)</code>	21
5.1.4.9	<code>rsmi_dev_perf_level_get(uint32_t dv_ind, rsmi_dev_perf_level *perf)</code>	21
5.1.4.10	<code>rsmi_dev_perf_level_set(int32_t dv_ind, rsmi_dev_perf_level perf_lv)</code>	22
5.1.4.11	<code>rsmi_dev_overdrive_level_get(uint32_t dv_ind, uint32_t *od)</code>	22
5.1.4.12	<code>rsmi_dev_overdrive_level_set(int32_t dv_ind, uint32_t od)</code>	22
5.1.4.13	<code>rsmi_dev_gpu_clk_freq_get(uint32_t dv_ind, rsmi_clk_type clk_type, rsmi_clk_frequencies *f)</code>	23
5.1.4.14	<code>rsmi_dev_gpu_clk_freq_set(uint32_t dv_ind, rsmi_clk_type clk_type, uint64_t freq_bitmask)</code>	23
5.1.4.15	<code>rsmi_dev_name_get(uint32_t dv_ind, char *name, size_t len)</code>	24
5.1.4.16	<code>rsmi_dev_temp_metric_get(uint32_t dv_ind, uint32_t sensor_ind, rsmi_temp_metric temperature_metric, int64_t *temperature)</code>	24
5.1.4.17	<code>rsmi_dev_fan_reset(uint32_t dv_ind, uint32_t sensor_ind)</code>	25
5.1.4.18	<code>rsmi_dev_fan_rpms_get(uint32_t dv_ind, uint32_t sensor_ind, int64_t *speed)</code>	25
5.1.4.19	<code>rsmi_dev_fan_speed_get(uint32_t dv_ind, uint32_t sensor_ind, int64_t *speed)</code>	25
5.1.4.20	<code>rsmi_dev_fan_speed_max_get(uint32_t dv_ind, uint32_t sensor_ind, uint64_t *max_speed)</code>	26
5.1.4.21	<code>rsmi_dev_fan_speed_set(uint32_t dv_ind, uint32_t sensor_ind, uint64_t speed)</code>	26
5.1.4.22	<code>rsmi_dev_od_volt_info_get(uint32_t dv_ind, rsmi_od_volt_freq_data *odv)</code>	27
5.1.4.23	<code>rsmi_dev_od_volt_curve_regions_get(uint32_t dv_ind, uint32_t *num_regions, rsmi_freq_volt_region *buffer)</code>	27
5.1.4.24	<code>rsmi_dev_power_ave_get(uint32_t dv_ind, uint32_t sensor_ind, uint64_t *power)</code>	28
5.1.4.25	<code>rsmi_dev_power_cap_get(uint32_t dv_ind, uint32_t sensor_ind, uint64_t *cap)</code>	28
5.1.4.26	<code>rsmi_dev_power_cap_range_get(uint32_t dv_ind, uint32_t sensor_ind, uint64_t *max, uint64_t *min)</code>	28
5.1.4.27	<code>rsmi_dev_power_cap_set(uint32_t dv_ind, uint32_t sensor_ind, uint64_t cap)</code>	29
5.1.4.28	<code>rsmi_dev_power_profile_presets_get(uint32_t dv_ind, uint32_t sensor_ind, rsmi_power_profile_status *status)</code>	29
5.1.4.29	<code>rsmi_dev_power_profile_set(uint32_t dv_ind, uint32_t sensor_ind, rsmi_power_profile_preset_masks profile)</code>	30
5.1.4.30	<code>rsmi_status_string(rsmi_status_t status, const char **status_string)</code>	30
5.1.4.31	<code>rsmi_version_get(rsmi_version *version)</code>	30

Chapter 1

ROCm System Management Interface (ROCm SMI) Library

The ROCm System Management Interface Library, or ROCm SMI library, is part of the Radeon Open Compute [ROCm](#) software stack . It is a C library for Linux that provides a user space interface for applications to monitor and control GPU applications.

Building ROCm SMI

Additional Required software for building

In order to build the ROCm SMI library, the following components are required. Note that the software versions listed are what was used in development. Earlier versions are not guaranteed to work:

- CMake (v3.5.0)
- g++ (5.4.0)

In order to build the latest documentation, the following are required:

- DOxygen (1.8.11)
- latex (pdfTeX 3.14159265-2.6-1.40.16)

The source code for ROCm SMI is available on [Github](#).

After the the ROCm SMI library git repository has been cloned to a local Linux machine, building the library is achieved by following the typical CMake build sequence. Specifically,

```
$ mk -p build
```

```
$ cd build
```

```
$ cmake <location of root of ROCm SMI library CMakeLists.txt>
```

```
$ make
```

The built library will appear in the `build` folder.

Building the Documentation

The documentation PDF file can be built with the following steps (continued from the steps above):

```
$ make doc

$ cd latex

$ make
```

The reference manual, `refman.pdf` will be in the `latex` directory upon a successful build.

Building the Tests

In order to verify the build and capability of ROCm SMI on your system and to see an example of how ROCm SMI can be used, you may build and run the tests that are available in the repo. To build the tests, follow these steps:

```
# Set environment variables used in CMakeLists.txt file

$ ROCM_DIR=<location of ROCm SMI library>

$ mkdir <location for test build>

$ cd <location for test build>

$ cmake -DROCM_DIR=<location of ROCM SMI library .so> <ROCm SMI source root>/tests/rocm_
_smi_test
```

To run the test, execute the program `rsmitst` that is built from the steps above. Make sure ROCm SMI library is in your library search path when executing the test program.

Hello ROCm SMI

The only required ROCm-SMI call for any program that wants to use ROCm-SMI is the `rsmi_init()` call. This call initializes some internal data structures that will be used by subsequent ROCm-SMI calls.

When ROCm-SMI is no longer being used, `rsmi_shut_down()` should be called. This provides a way to do any releasing of resources that ROCm-SMI may have held. In many cases, this may have no effect, but may be necessary in future versions of the library.

A simple "Hello World" type program that displays the device ID of detected devices would look like this:

```
1 #include <stdint.h>
2 #include "rocm_smi/rocm_smi.h"
3 int main() {
4     rsmi_status_t ret;
5     uint32_t num_devices;
6     uint64_t dev_id;
7
8     // We will skip return code checks for this example, but it
9     // is recommended to always check this as some calls may not
10    // apply for some devices or ROCm releases
11
12    ret = rsmi_init(0);
13    ret = rsmi_num_monitor_devices(&num_devices);
14
15    for (int i=0; i < num_devices; ++i) {
16        ret = rsmi_dev_id_get(i, &dev_id);
17        // dev_id holds the device ID of device i, upon a
18        // successful call
19    }
20    ret = rsmi_shut_down();
21    return 0;
22 }
```


Chapter 2

Data Structure Index

2.1 Data Structures

Here are the data structures with brief descriptions:

rsmi_freq_volt_region	This structure holds 2 rsmi_od_vddc_point 's, representing the diagonal corners of a rectangular region in freq-voltage space	7
rsmi_frequencies	This structure holds information about clock frequencies	7
rsmi_od_vddc_point	This structure represents a point on the frequency-voltage plane	8
rsmi_od_volt_freq_data	This structure holds the frequency-voltage values for a device	9
rsmi_pcie_bandwidth	This structure holds information about the possible PCIe bandwidths. Specifically, the possible transfer rates and their associated numbers of lanes are stored here	10
rsmi_power_profile_status	This structure contains information about which power profiles are supported by the system for a given device, and which power profile is currently active	10
rsmi_range	This structure represents a range (e.g., frequencies or voltages)	11
rsmi_version	This structure holds version information	12

Chapter 3

File Index

3.1 File List

Here is a list of all documented files with brief descriptions:

rocm_smi.h	13
--------------------------------------	----

Chapter 4

Data Structure Documentation

4.1 `rsmi_freq_volt_region` Struct Reference

This structure holds 2 `rsmi_od_vddc_point`'s, representing the diagonal corners of a rectangular region in freq-voltage space.

```
#include <rocm_smi.h>
```

Data Fields

- `rsmi_od_vddc_point min_corner`
The "lower-left" corner of rectangle.
- `rsmi_od_vddc_point max_corner`
The "upper-right" corner of rectangle.

4.1.1 Detailed Description

This structure holds 2 `rsmi_od_vddc_point`'s, representing the diagonal corners of a rectangular region in freq-voltage space.

The documentation for this struct was generated from the following file:

- `rocm_smi.h`

4.2 `rsmi_frequencies` Struct Reference

This structure holds information about clock frequencies.

```
#include <rocm_smi.h>
```

Data Fields

- uint32_t [num_supported](#)
- uint32_t [current](#)
- uint64_t [frequency](#) [[RSMI_MAX_NUM_FREQUENCIES](#)]

4.2.1 Detailed Description

This structure holds information about clock frequencies.

4.2.2 Field Documentation

4.2.2.1 uint32_t rsmi_frequencies::num_supported

The number of supported frequencies

4.2.2.2 uint32_t rsmi_frequencies::current

The current frequency index

4.2.2.3 uint64_t rsmi_frequencies::frequency[RSMI_MAX_NUM_FREQUENCIES]

List of frequencies. Only the first num_supported frequencies are valid.

The documentation for this struct was generated from the following file:

- [rocm_smi.h](#)

4.3 rsmi_od_vddc_point Struct Reference

This structure represents a point on the frequency-voltage plane.

```
#include <rocm_smi.h>
```

Data Fields

- uint64_t [frequency](#)
Frequency coordinate (in Hz)
- uint64_t [voltage](#)
Voltage coordinate (in mV)

4.3.1 Detailed Description

This structure represents a point on the frequency-voltage plane.

The documentation for this struct was generated from the following file:

- [rocm_smi.h](#)

4.4 rsmi_od_volt_freq_data Struct Reference

This structure holds the frequency-voltage values for a device.

```
#include <rocm_smi.h>
```

Data Fields

- [rsmi_range curr_sclk_range](#)
The current SCLK frequency range.
- [rsmi_range curr_mclk_range](#)
- [rsmi_range sclk_freq_limits](#)
The range possible of SCLK values.
- [rsmi_range mclk_freq_limits](#)
The range possible of MCLK values.
- [rsmi_od_vddc_point curve](#) [RSMI_NUM_VOLTAGE_CURVE_POINTS]
The current voltage curve.
- [uint32_t num_regions](#)
The number of voltage curve regions.

4.4.1 Detailed Description

This structure holds the frequency-voltage values for a device.

4.4.2 Field Documentation

4.4.2.1 rsmi_range rsmi_od_volt_freq_data::curr_mclk_range

The current MCLK frequency range; (upper bound only)

The documentation for this struct was generated from the following file:

- [rocm_smi.h](#)

4.5 rsmi_pcie_bandwidth Struct Reference

This structure holds information about the possible PCIe bandwidths. Specifically, the possible transfer rates and their associated numbers of lanes are stored here.

```
#include <rocm_smi.h>
```

Data Fields

- [rsmi_frequencies](#) [transfer_rate](#)
- [uint32_t](#) [lanes](#) [[RSMI_MAX_NUM_FREQUENCIES](#)]

4.5.1 Detailed Description

This structure holds information about the possible PCIe bandwidths. Specifically, the possible transfer rates and their associated numbers of lanes are stored here.

4.5.2 Field Documentation

4.5.2.1 [rsmi_frequencies](#) [rsmi_pcie_bandwidth::transfer_rate](#)

Transfer rates (T/s) that are possible

4.5.2.2 [uint32_t](#) [rsmi_pcie_bandwidth::lanes](#)[[RSMI_MAX_NUM_FREQUENCIES](#)]

List of lanes for corresponding transfer rate. Only the first num_supported bandwidths are valid.

The documentation for this struct was generated from the following file:

- [rocm_smi.h](#)

4.6 rsmi_power_profile_status Struct Reference

This structure contains information about which power profiles are supported by the system for a given device, and which power profile is currently active.

```
#include <rocm_smi.h>
```

Data Fields

- [rsmi_bit_field](#) [available_profiles](#)
- [rsmi_power_profile_preset_masks](#) [current](#)
- [uint32_t](#) [num_profiles](#)

4.6.1 Detailed Description

This structure contains information about which power profiles are supported by the system for a given device, and which power profile is currently active.

4.6.2 Field Documentation

4.6.2.1 `rsmi_bit_field` `rsmi_power_profile_status::available_profiles`

Which profiles are supported by this system

4.6.2.2 `rsmi_power_profile_preset_masks` `rsmi_power_profile_status::current`

Which power profile is currently active

4.6.2.3 `uint32_t` `rsmi_power_profile_status::num_profiles`

How many power profiles are available

The documentation for this struct was generated from the following file:

- [rocm_smi.h](#)

4.7 rsmi_range Struct Reference

This structure represents a range (e.g., frequencies or voltages).

```
#include <rocm_smi.h>
```

Data Fields

- `uint64_t` [lower_bound](#)
Lower bound of range.
- `uint64_t` [upper_bound](#)
Upper bound of range.

4.7.1 Detailed Description

This structure represents a range (e.g., frequencies or voltages).

The documentation for this struct was generated from the following file:

- [rocm_smi.h](#)

4.8 rsmi_version Struct Reference

This structure holds version information.

```
#include <rocm_smi.h>
```

Data Fields

- uint32_t [major](#)
Major version.
- uint32_t [minor](#)
Minor version.
- uint32_t [patch](#)
Patch, build or stepping version.
- const char * [build](#)
Build string.

4.8.1 Detailed Description

This structure holds version information.

The documentation for this struct was generated from the following file:

- [rocm_smi.h](#)

Chapter 5

File Documentation

5.1 rocm_smi.h File Reference

```
#include <stdint.h>
#include <stddef.h>
```

Data Structures

- struct [rsmi_power_profile_status](#)
This structure contains information about which power profiles are supported by the system for a given device, and which power profile is currently active.
- struct [rsmi_frequencies](#)
This structure holds information about clock frequencies.
- struct [rsmi_pcie_bandwidth](#)
This structure holds information about the possible PCIe bandwidths. Specifically, the possible transfer rates and their associated numbers of lanes are stored here.
- struct [rsmi_version](#)
This structure holds version information.
- struct [rsmi_range](#)
This structure represents a range (e.g., frequencies or voltages).
- struct [rsmi_od_vddc_point](#)
This structure represents a point on the frequency-voltage plane.
- struct [rsmi_freq_volt_region](#)
This structure holds 2 [rsmi_od_vddc_point](#)'s, representing the diagonal corners of a rectangular region in freq-voltage space.
- struct [rsmi_od_volt_freq_data](#)
This structure holds the frequency-voltage values for a device.

Macros

- #define [RSMI_MAX_NUM_FREQUENCIES](#) 32
Guaranteed maximum possible number of supported frequencies.
- #define [RSMI_MAX_FAN_SPEED](#) 255
- #define [RSMI_NUM_VOLTAGE_CURVE_POINTS](#) 3
The number of points that make up a voltage-frequency curve definition.
- #define [RSMI_MAX_NUM_POWER_PROFILES](#) (sizeof(rsmi_bit_field) * 8)
Number of possible power profiles that a system could support.

Typedefs

- typedef uint64_t **rsmi_bit_field**

Bitfield used in various RSMI calls.

Enumerations

- enum **rsmi_status_t** {
RSMI_STATUS_SUCCESS = 0x0, **RSMI_STATUS_INVALID_ARGS**, **RSMI_STATUS_NOT_SUPPORTED**,
RSMI_STATUS_FILE_ERROR,
RSMI_STATUS_PERMISSION, **RSMI_STATUS_OUT_OF_RESOURCES**, **RSMI_STATUS_INTERNAL_**↵
EXCEPTION, **RSMI_STATUS_INPUT_OUT_OF_BOUNDS**,
RSMI_STATUS_INIT_ERROR, **RSMI_INITIALIZATION_ERROR** = **RSMI_STATUS_INIT_ERROR**, **RSMI_**↵
_STATUS_NOT_YET_IMPLEMENTED, **RSMI_STATUS_UNKNOWN_ERROR** = 0xFFFFFFFF }

Error codes returned by rocm_smi_lib functions.

- enum **rsmi_dev_perf_level** {
RSMI_DEV_PERF_LEVEL_AUTO = 0, **RSMI_DEV_PERF_LEVEL_FIRST** = **RSMI_DEV_PERF_LEVEL_**↵
AUTO, **RSMI_DEV_PERF_LEVEL_LOW**, **RSMI_DEV_PERF_LEVEL_HIGH**,
RSMI_DEV_PERF_LEVEL_MANUAL, **RSMI_DEV_PERF_LEVEL_STABLE_STD**, **RSMI_DEV_PERF_LE**↵
VEL_STABLE_PEAK, **RSMI_DEV_PERF_LEVEL_STABLE_MIN_MCLK**,
RSMI_DEV_PERF_LEVEL_STABLE_MIN_SCLK, **RSMI_DEV_PERF_LEVEL_LAST** = **RSMI_DEV_PER**↵
F_LEVEL_STABLE_MIN_SCLK, **RSMI_DEV_PERF_LEVEL_UNKNOWN** = 0x100 }

PowerPlay performance levels.

- enum **rsmi_clk_type** { **RSMI_CLK_TYPE_SYS** = 0x0, **RSMI_CLK_TYPE_FIRST** = **RSMI_CLK_TYPE_SYS**,
RSMI_CLK_TYPE_MEM, **RSMI_CLK_TYPE_LAST** = **RSMI_CLK_TYPE_MEM** }

Available clock types.

- enum **rsmi_temperature_metric** {
RSMI_TEMP_CURRENT = 0x0, **RSMI_TEMP_FIRST** = **RSMI_TEMP_CURRENT**, **RSMI_TEMP_MAX**, **R**↵
SMI_TEMP_MIN,
RSMI_TEMP_MAX_HYST, **RSMI_TEMP_MIN_HYST**, **RSMI_TEMP_CRITICAL**, **RSMI_TEMP_CRITICAL_**↵
_HYST,
RSMI_TEMP_EMERGENCY, **RSMI_TEMP_EMERGENCY_HYST**, **RSMI_TEMP_CRIT_MIN**, **RSMI_TEM**↵
P_CRIT_MIN_HYST,
RSMI_TEMP_OFFSET, **RSMI_TEMP_LOWEST**, **RSMI_TEMP_HIGHEST**, **RSMI_TEMP_LAST** = **RSMI_**↵
TEMP_HIGHEST }

Temperature Metrics. This enum is used to identify various temperature metrics. Corresponding values will be in millidegrees Celcius.

- enum **rsmi_power_profile_preset_masks** {
RSMI_PWR_PROF_PRST_CUSTOM_MASK = 0x1, **RSMI_PWR_PROF_PRST_VIDEO_MASK** = 0x2, **R**↵
SMI_PWR_PROF_PRST_POWER_SAVING_MASK = 0x4, **RSMI_PWR_PROF_PRST_COMPUTE_MASK**
= 0x8,
RSMI_PWR_PROF_PRST_VR_MASK = 0x10, **RSMI_PWR_PROF_PRST_3D_FULL_SCR_MASK** = 0x20,
RSMI_PWR_PROF_PRST_BOOTUP_DEFAULT = 0x40, **RSMI_PWR_PROF_PRST_LAST** = **RSMI_PW**↵
R_PROF_PRST_BOOTUP_DEFAULT,
RSMI_PWR_PROF_PRST_INVALID = 0xFFFFFFFFFFFFFFFF }

Pre-set Profile Selections. These bitmasks can be AND'd with the `rsmi_power_profile_status::available_profiles` returned from `rsmi_dev_power_profile_presets_get()` to determine which power profiles are supported by the system.

- enum **rsmi_freq_ind** { **RSMI_FREQ_IND_MIN** = 0, **RSMI_FREQ_IND_MAX** = 1, **RSMI_FREQ_IND_INVALID**
= 0xFFFFFFFF }

This values of this enum are used as frequency identifiers.

Functions

- [rocm_smi_status_t rocm_smi_init](#) (uint64_t init_flags)
Initialize Rocrm SMI.
- [rocm_smi_status_t rocm_smi_shut_down](#) (void)
Shutdown Rocrm SMI.
- [rocm_smi_status_t rocm_smi_num_monitor_devices](#) (uint32_t *num_devices)
Get the number of devices that have monitor information.
- [rocm_smi_status_t rocm_smi_dev_pci_bandwidth_get](#) (uint32_t dv_ind, [rocm_smi_pcie_bandwidth_t](#) *bandwidth)
Get the list of possible pci bandwidths that are available.
- [rocm_smi_status_t rocm_smi_dev_busy_percent_get](#) (uint32_t dv_ind, uint32_t *busy_percent)
Get percentage of time device is busy doing any processing.
- [rocm_smi_status_t rocm_smi_dev_pci_bandwidth_set](#) (uint32_t dv_ind, uint64_t bw_bitmask)
Control the set of allowed PCIe bandwidths that can be used.
- [rocm_smi_status_t rocm_smi_dev_pci_id_get](#) (uint32_t dv_ind, uint64_t *bdfid)
Get the unique PCI device identifier associated for a device.
- [rocm_smi_status_t rocm_smi_dev_id_get](#) (uint32_t dv_ind, uint64_t *id)
Get the device id associated with the device with provided device index.
- [rocm_smi_status_t rocm_smi_dev_perf_level_get](#) (uint32_t dv_ind, [rocm_smi_dev_perf_level_t](#) *perf)
Get the performance level of the device with provided device index.
- [rocm_smi_status_t rocm_smi_dev_perf_level_set](#) (uint32_t dv_ind, [rocm_smi_dev_perf_level_t](#) perf_lvl)
Set the PowerPlay performance level associated with the device with provided device index with the provided value.
- [rocm_smi_status_t rocm_smi_dev_overdrive_level_get](#) (uint32_t dv_ind, uint32_t *od)
Get the overdrive percent associated with the device with provided device index.
- [rocm_smi_status_t rocm_smi_dev_overdrive_level_set](#) (uint32_t dv_ind, uint32_t od)
Set the overdrive percent associated with the device with provided device index with the provided value. See details for WARNING.
- [rocm_smi_status_t rocm_smi_dev_gpu_clk_freq_get](#) (uint32_t dv_ind, [rocm_smi_clk_type_t](#) clk_type, [rocm_smi_frequencies_t](#) *f)
Get the list of possible system clock speeds of device for a specified clock type.
- [rocm_smi_status_t rocm_smi_dev_gpu_clk_freq_set](#) (uint32_t dv_ind, [rocm_smi_clk_type_t](#) clk_type, uint64_t freq_bitmask)
Control the set of allowed frequencies that can be used for the specified clock.
- [rocm_smi_status_t rocm_smi_dev_name_get](#) (uint32_t dv_ind, char *name, size_t len)
Get the name of a gpu device.
- [rocm_smi_status_t rocm_smi_dev_temp_metric_get](#) (uint32_t dv_ind, uint32_t sensor_ind, [rocm_smi_temperature_metric_t](#) metric, int64_t *temperature)
Get the temperature metric value for the specified metric, from the specified temperature sensor on the specified device.
- [rocm_smi_status_t rocm_smi_dev_fan_reset](#) (uint32_t dv_ind, uint32_t sensor_ind)
Reset the fan to automatic driver control.
- [rocm_smi_status_t rocm_smi_dev_fan_rpms_get](#) (uint32_t dv_ind, uint32_t sensor_ind, int64_t *speed)
Get the fan speed in RPMs of the device with the specified device index and 0-based sensor index.
- [rocm_smi_status_t rocm_smi_dev_fan_speed_get](#) (uint32_t dv_ind, uint32_t sensor_ind, int64_t *speed)
Get the fan speed for the specified device in RPMs.
- [rocm_smi_status_t rocm_smi_dev_fan_speed_max_get](#) (uint32_t dv_ind, uint32_t sensor_ind, uint64_t *max_speed)
Get the max. fan speed of the device with provided device index.
- [rocm_smi_status_t rocm_smi_dev_fan_speed_set](#) (uint32_t dv_ind, uint32_t sensor_ind, uint64_t speed)
Set the fan speed for the specified device with the provided speed, in RPMs.
- [rocm_smi_status_t rocm_smi_dev_od_volt_info_get](#) (uint32_t dv_ind, [rocm_smi_od_volt_freq_data_t](#) *odv)
This function retrieves the voltage/frequency curve information.
- [rocm_smi_status_t rocm_smi_dev_od_volt_curve_regions_get](#) (uint32_t dv_ind, uint32_t *num_regions, [rocm_smi_freq_volt_region_t](#) *buffer)
This function will retrieve the current valid regions in the frequency/voltage space.

- [rsmi_status_t rsmi_dev_power_ave_get](#) (uint32_t dv_ind, uint32_t sensor_ind, uint64_t *power)
Get the average power consumption of the device with provided device index.
- [rsmi_status_t rsmi_dev_power_cap_get](#) (uint32_t dv_ind, uint32_t sensor_ind, uint64_t *cap)
Get the cap on power which, when reached, causes the system to take action to reduce power.
- [rsmi_status_t rsmi_dev_power_cap_range_get](#) (uint32_t dv_ind, uint32_t sensor_ind, uint64_t *max, uint64_t *min)
Get the range of valid values for the power cap.
- [rsmi_status_t rsmi_dev_power_cap_set](#) (uint32_t dv_ind, uint32_t sensor_ind, uint64_t cap)
Set the power cap value.
- [rsmi_status_t rsmi_dev_power_profile_presets_get](#) (uint32_t dv_ind, uint32_t sensor_ind, [rsmi_power_profile_status](#) *status)
Get the list of available preset power profiles and an indication of which profile is currently active.
- [rsmi_status_t rsmi_dev_power_profile_set](#) (uint32_t dv_ind, uint32_t sensor_ind, [rsmi_power_profile_preset_masks](#) profile)
Set the power profile.
- [rsmi_status_t rsmi_status_string](#) ([rsmi_status_t](#) status, const char **status_string)
Get a description of a provided RSMI error status.
- [rsmi_status_t rsmi_version_get](#) ([rsmi_version](#) *version)
Get the build version information for the currently running build of RSMI.

5.1.1 Detailed Description

Main header file for the ROCm SMI library. All required function, structure, enum, etc. definitions should be defined in this file.

The rocm_smi library api is new, and therefore subject to change either at the ABI or API level. Once committed, every effort will be made to not break backward compatibility, but it may not be achievable in some cases. Instead of marking every function prototype as "unstable", we are instead saying the API is unstable (i.e., changes are possible) while the major version remains 0. This means that if the API/ABI changes, we will not increment the major version to 1. Once the ABI stabilizes, we will increment the major version to 1, and thereafter increment it on all ABI breaks.

5.1.2 Macro Definition Documentation

5.1.2.1 #define RSMI_MAX_FAN_SPEED 255

Maximum possible value for fan speed. Should be used as the denominator when determining fan speed percentage.

5.1.3 Enumeration Type Documentation

5.1.3.1 enum rsmi_status_t

Error codes returned by rocm_smi_lib functions.

Enumerator

RSMI_STATUS_SUCCESS Operation was successful.

RSMI_STATUS_INVALID_ARGS Passed in arguments are not valid.

RSMI_STATUS_NOT_SUPPORTED The requested information or action is not available for the given input

RSMI_STATUS_FILE_ERROR Problem accessing a file. This may because the operation is not supported by the Linux kernel version running on the executing machine

RSMI_STATUS_PERMISSION Permission denied/EACCESS file error

RSMI_STATUS_OUT_OF_RESOURCES Unable to acquire memory or other resource

RSMI_STATUS_INTERNAL_EXCEPTION An internal exception was caught.

RSMI_STATUS_INPUT_OUT_OF_BOUNDS The provided input is out of allowable or safe range

RSMI_STATUS_INIT_ERROR An error occurred when rsmi initializing internal data structures

RSMI_STATUS_NOT_YET_IMPLEMENTED The requested function has not yet been implemented in the current system for the current devices

RSMI_STATUS_UNKNOWN_ERROR An unknown error occurred.

5.1.3.2 enum rsmi_dev_perf_level

PowerPlay performance levels.

Enumerator

RSMI_DEV_PERF_LEVEL_AUTO Performance level is "auto".

RSMI_DEV_PERF_LEVEL_LOW Keep PowerPlay levels "low", regardless of workload

RSMI_DEV_PERF_LEVEL_HIGH Keep PowerPlay levels "high", regardless of workload

RSMI_DEV_PERF_LEVEL_MANUAL Only use values defined by manually setting the RSMI_CLK_TYP↵
E_SYS speed

RSMI_DEV_PERF_LEVEL_STABLE_STD Stable power state with profiling clocks

RSMI_DEV_PERF_LEVEL_STABLE_PEAK Stable power state with peak clocks.

RSMI_DEV_PERF_LEVEL_STABLE_MIN_MCLK Stable power state with minimum memory clock

RSMI_DEV_PERF_LEVEL_STABLE_MIN_SCLK Stable power state with minimum system clock

RSMI_DEV_PERF_LEVEL_UNKNOWN Unknown performance level.

5.1.3.3 enum rsmi_clk_type

Available clock types.

Enumerator

RSMI_CLK_TYPE_SYS System clock.

RSMI_CLK_TYPE_MEM Memory clock.

5.1.3.4 enum rsmi_temperature_metric

Temperature Metrics. This enum is used to identify various temperature metrics. Corresponding values will be in millidegress Celcius.

Enumerator

- RSMI_TEMP_CURRENT*** Temperature current value.
- RSMI_TEMP_MAX*** Temperature max value.
- RSMI_TEMP_MIN*** Temperature min value.
- RSMI_TEMP_MAX_HYST*** Temperature hysteresis value for max limit.
- RSMI_TEMP_MIN_HYST*** Temperature hysteresis value for min limit.
- RSMI_TEMP_CRITICAL*** Temperature critical max value, typically greater than corresponding temp_max values.
- RSMI_TEMP_CRITICAL_HYST*** Temperature hysteresis value for critical limit.
- RSMI_TEMP_EMERGENCY*** Temperature emergency max value, for chips supporting more than two upper temperature limits. Must be equal or greater than corresponding temp_crit values.
- RSMI_TEMP_EMERGENCY_HYST*** Temperature hysteresis value for emergency limit.
- RSMI_TEMP_CRIT_MIN*** Temperature critical min value, typically lower than corresponding temperature minimum values.
- RSMI_TEMP_CRIT_MIN_HYST*** Temperature hysteresis value for critical minimum limit.
- RSMI_TEMP_OFFSET*** Temperature offset which is added to the temperature reading by the chip.
- RSMI_TEMP_LOWEST*** Historical minimum temperature.
- RSMI_TEMP_HIGHEST*** Historical maximum temperature.

5.1.3.5 enum rsmi_power_profile_preset_masks

Pre-set Profile Selections. These bitmasks can be AND'd with the [rsmi_power_profile_status::available_profiles](#) returned from [rsmi_dev_power_profile_presets_get\(\)](#) to determine which power profiles are supported by the system.

Enumerator

- RSMI_PWR_PROF_PRST_CUSTOM_MASK*** Custom Power Profile.
- RSMI_PWR_PROF_PRST_VIDEO_MASK*** Video Power Profile.
- RSMI_PWR_PROF_PRST_POWER_SAVING_MASK*** Power Saving Profile.
- RSMI_PWR_PROF_PRST_COMPUTE_MASK*** Compute Saving Profile.
- RSMI_PWR_PROF_PRST_VR_MASK*** VR Power Profile. 3D Full Screen Power Profile
- RSMI_PWR_PROF_PRST_BOOTUP_DEFAULT*** Default Boot Up Profile.
- RSMI_PWR_PROF_PRST_LAST*** Invalid power profile.

5.1.3.6 enum rsmi_freq_ind

This values of this enum are used as frequency identifiers.

Enumerator

- RSMI_FREQ_IND_MIN*** Index used for the minimum frequency value.
- RSMI_FREQ_IND_MAX*** Index used for the maximum frequency value.
- RSMI_FREQ_IND_INVALID*** An invalid frequency index.

5.1.4 Function Documentation

5.1.4.1 `rsmi_status_t rsmi_init (uint64_t init_flags)`

Initialize Rocm SMI.

When called, this initializes internal data structures, including those corresponding to sources of information that SMI provides.

Parameters

<code>in</code>	<code><i>init_flags</i></code>	Bit flags that tell SMI how to initialize. Not currently used.
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Return values

<code>RSMI_STATUS_SUCCESS</code>	is returned upon successful call.
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5.1.4.2 `rsmi_status_t rsmi_shut_down (void)`

Shutdown Rocm SMI.

Do any necessary clean up.

5.1.4.3 `rsmi_status_t rsmi_num_monitor_devices (uint32_t * num_devices)`

Get the number of devices that have monitor information.

The number of devices which have monitors is returned. Monitors are referenced by the index which can be between 0 and `num_devices - 1`.

Parameters

<code>in, out</code>	<code><i>num_devices</i></code>	Caller provided pointer to <code>uint32_t</code> . Upon successful call, the value <code>num_devices</code> will contain the number of monitor devices.
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Return values

<code>RSMI_STATUS_SUCCESS</code>	is returned upon successful call.
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5.1.4.4 `rsmi_status_t rsmi_dev_pci_bandwidth_get (uint32_t dv_ind, rsmi_pcie_bandwidth * bandwidth)`

Get the list of possible pci bandwidths that are available.

Given a device index `dv_ind` and a pointer to a to an `rsmi_pcie_bandwidth` structure `bandwidth`, this function will fill in `bandwidth` with the possible T/s values and associated number of lanes, and indication of the current selection.

Parameters

in	<i>dv_ind</i>	a device index
in, out	<i>bandwidth</i>	a pointer to a caller provided rsmi_pcie_bandwidth structure to which the frequency information will be written

Return values

RSMI_STATUS_SUCCESS	is returned upon successful call.
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5.1.4.5 `rsmi_status_t rsmi_dev_busy_percent_get (uint32_t dv_ind, uint32_t * busy_percent)`

Get percentage of time device is busy doing any processing.

Given a device index *dv_ind*, this function returns the percentage of time that the specified device is busy. The device is considered busy if any one or more of its sub-blocks are working, and idle if none of the sub-blocks are working.

Parameters

in	<i>dv_ind</i>	a device index
in, out	<i>busy_percent</i>	a pointer to the <code>uint32_t</code> to which the busy percent will be written

Return values

RSMI_STATUS_SUCCESS	is returned upon successful call
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5.1.4.6 `rsmi_status_t rsmi_dev_pci_bandwidth_set (uint32_t dv_ind, uint64_t bw_bitmask)`

Control the set of allowed PCIe bandwidths that can be used.

Given a device index *dv_ind* and a 64 bit bitmask *bw_bitmask*, this function will limit the set of allowable bandwidths. If a bit in *bw_bitmask* has a value of 1, then the frequency (as ordered in an [rsmi_frequencies](#) returned by `rsmi_dev_get_gpu_clk_freq()`) corresponding to that bit index will be allowed.

This function will change the performance level to [RSMI_DEV_PERF_LEVEL_MANUAL](#) in order to modify the set of allowable band_widths. Caller will need to set to [RSMI_DEV_PERF_LEVEL_AUTO](#) in order to get back to default state.

All bits with indices greater than or equal to `rsmi_pcie_bandwidth.transfer_rate.num_supported` will be ignored.

Parameters

in	<i>dv_ind</i>	a device index
in	<i>bw_bitmask</i>	A bitmask indicating the indices of the bandwidths that are to be enabled (1) and disabled (0). Only the lowest <code>rsmi_pcie_bandwidth.transfer_rate.num_supported</code> bits of this mask are relevant.

5.1.4.7 `rsmi_status_t rsmi_dev_pci_id_get (uint32_t dv_ind, uint64_t * bdfid)`

Get the unique PCI device identifier associated for a device.

Give a device index `dv_ind` and a pointer to a `uint64_t bdfid`, this function will write the Bus/Device/Function PCI identifier (BDFID) associated with device `dv_ind` to the value pointed to by `bdfid`.

Parameters

in	<i>dv_ind</i>	a device index
in, out	<i>bdfid</i>	a pointer to <code>uint64_t</code> to which the device bdfid value will be written

Return values

<code>RSMI_STATUS_SUCCESS</code>	is returned upon successful call.
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5.1.4.8 `rsmi_status_t rsmi_dev_id_get (uint32_t dv_ind, uint64_t * id)`

Get the device id associated with the device with provided device index.

Given a device index `dv_ind` and a pointer to a `uint32_t id`, this function will write the device id value to the `uint64_t` pointed to by `id`. This ID is an identification of the type of device, so calling this function for different devices will give the same value if they are kind of device. Consequently, this function should not be used to distinguish one device from another. [`rsmi_dev_pci_id_get\(\)`](#) should be used to get a unique identifier.

Parameters

in	<i>dv_ind</i>	a device index
in, out	<i>id</i>	a pointer to <code>uint64_t</code> to which the device id will be written

Return values

<code>RSMI_STATUS_SUCCESS</code>	is returned upon successful call.
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5.1.4.9 `rsmi_status_t rsmi_dev_perf_level_get (uint32_t dv_ind, rsmi_dev_perf_level * perf)`

Get the performance level of the device with provided device index.

Given a device index `dv_ind` and a pointer to a `uint32_t perf`, this function will write the [`rsmi_dev_perf_level`](#) to the `uint32_t` pointed to by `perf`

Parameters

in	<i>dv_ind</i>	a device index
in, out	<i>perf</i>	a pointer to <code>rsmi_dev_perf_level</code> to which the performance level will be written

Return values

<i>RSMI_STATUS_SUCCESS</i>	is returned upon successful call.
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5.1.4.10 `rsmi_status_t rsmi_dev_perf_level_set (int32_t dv_ind, rsmi_dev_perf_level perf_lvl)`

Set the PowerPlay performance level associated with the device with provided device index with the provided value.

Given a device index *dv_ind* and an `rsmi_dev_perf_lvl` *perf_level*, this function will set the PowerPlay performance level for the device to the value *perf_lvl*.

Parameters

in	<i>dv_ind</i>	a device index
in	<i>perf_lvl</i>	the value to which the performance level should be set

Return values

<i>RSMI_STATUS_SUCCESS</i>	is returned upon successful call.
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5.1.4.11 `rsmi_status_t rsmi_dev_overdrive_level_get (uint32_t dv_ind, uint32_t * od)`

Get the overdrive percent associated with the device with provided device index.

Given a device index *dv_ind* and a pointer to a `uint32_t` *od*, this function will write the overdrive percentage to the `uint32_t` pointed to by *od*

Parameters

in	<i>dv_ind</i>	a device index
in, out	<i>od</i>	a pointer to <code>uint32_t</code> to which the overdrive percentage will be written

Return values

<i>RSMI_STATUS_SUCCESS</i>	is returned upon successful call.
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5.1.4.12 `rsmi_status_t rsmi_dev_overdrive_level_set (int32_t dv_ind, uint32_t od)`

Set the overdrive percent associated with the device with provided device index with the provided value. See details for WARNING.

Given a device index *dv_ind* and an overdrive level *od*, this function will set the overdrive level for the device to the value *od*. The overdrive level is an integer value between 0 and 20, inclusive, which represents the overdrive percentage; e.g., a value of 5 specifies an overclocking of 5%.

The overdrive level is specific to the gpu system clock.

The overdrive level is the percentage above the maximum Performance Level to which overclocking will be limited. The overclocking percentage does not apply to clock speeds other than the maximum. This percentage is limited to 20%.

*****WARNING***** Operating your AMD GPU outside of official AMD specifications or outside of factory settings, including but not limited to the conducting of overclocking (including use of this overclocking software, even if such software has been directly or indirectly provided by AMD or otherwise affiliated in any way with AMD), may cause damage to your AMD GPU, system components and/or result in system failure, as well as cause other problems. DAMAGES CAUSED BY USE OF YOUR AMD GPU OUTSIDE OF OFFICIAL AMD SPECIFICATIONS OR OUTSIDE OF FACTORY SETTINGS ARE NOT COVERED UNDER ANY AMD PRODUCT WARRANTY AND MAY NOT BE COVERED BY YOUR BOARD OR SYSTEM MANUFACTURER'S WARRANTY. Please use this utility with caution.

Parameters

in	<i>dv_ind</i>	a device index
in	<i>od</i>	the value to which the overdrive level should be set

Return values

<i>RSMI_STATUS_SUCCESS</i>	is returned upon successful call.
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5.1.4.13 `rsmi_status_t rsmi_dev_gpu_clk_freq_get (uint32_t dv_ind, rsmi_clk_type clk_type, rsmi_frequencies * f)`

Get the list of possible system clock speeds of device for a specified clock type.

Given a device index *dv_ind*, a clock type *clk_type*, and a pointer to a to an [`rsmi_frequencies`](#) structure *f*, this function will fill in *f* with the possible clock speeds, and indication of the current clock speed selection.

Parameters

in	<i>dv_ind</i>	a device index
in	<i>clk_type</i>	the type of clock for which the frequency is desired
in, out	<i>f</i>	a pointer to a caller provided <code>rsmi_frequencies</code> structure to which the frequency information will be written

Return values

<i>RSMI_STATUS_SUCCESS</i>	is returned upon successful call.
--	-----------------------------------

5.1.4.14 `rsmi_status_t rsmi_dev_gpu_clk_freq_set (uint32_t dv_ind, rsmi_clk_type clk_type, uint64_t freq_bitmask)`

Control the set of allowed frequencies that can be used for the specified clock.

Given a device index *dv_ind*, a clock type *clk_type*, and a 64 bit bitmask *freq_bitmask*, this function will limit the set of allowable frequencies. If a bit in *freq_bitmask* has a value of 1, then the frequency (as ordered in an [`rsmi_frequencies`](#) returned by `rsmi_dev_get_gpu_clk_freq()`) corresponding to that bit index will be allowed.

This function will change the performance level to [RSMI_DEV_PERF_LEVEL_MANUAL](#) in order to modify the set of allowable frequencies. Caller will need to set to [RSMI_DEV_PERF_LEVEL_AUTO](#) in order to get back to default state.

All bits with indices greater than or equal to [rsmi_frequencies::num_supported](#) will be ignored.

Parameters

in	<i>dv_ind</i>	a device index
in	<i>clk_type</i>	the type of clock for which the set of frequencies will be modified
in	<i>freq_bitmask</i>	A bitmask indicating the indices of the frequencies that are to be enabled (1) and disabled (0). Only the lowest rsmi_frequencies.num_supported bits of this mask are relevant.

5.1.4.15 [rsmi_status_t](#) [rsmi_dev_name_get](#) ([uint32_t](#) *dv_ind*, [char *](#) *name*, [size_t](#) *len*)

Get the name of a gpu device.

Given a device index *dv_ind*, a pointer to a caller provided char buffer *name*, and a length of this buffer *len*, this function will write the name of the device (up to *len* characters) buffer *name*.

Parameters

in	<i>dv_ind</i>	a device index
in, out	<i>name</i>	a pointer to a caller provided char buffer to which the name will be written
in	<i>len</i>	the length of the caller provided buffer <i>name</i> .

Return values

RSMI_STATUS_SUCCESS	is returned upon successful call.
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5.1.4.16 [rsmi_status_t](#) [rsmi_dev_temp_metric_get](#) ([uint32_t](#) *dv_ind*, [uint32_t](#) *sensor_ind*, [rsmi_temperature_metric](#) *metric*, [int64_t *](#) *temperature*)

Get the temperature metric value for the specified metric, from the specified temperature sensor on the specified device.

Given a device index *dv_ind*, a 0-based sensor index

Parameters

in	<i>dv_ind</i>	a device index
in	<i>sensor_ind</i>	a 0-based sensor index. Normally, this will be 0. If a device has more than one sensor, it could be greater than 0.
in	<i>metric</i>	enum indicated which temperature value should be retrieved
in, out	<i>temperature</i>	a pointer to int64_t to which the temperature will be written, in millidegrees Celcius.

Return values

<i>RSMI_STATUS_SUCCESS</i>	is returned upon successful call.
--	-----------------------------------

5.1.4.17 `rsmi_status_t rsmi_dev_fan_reset (uint32_t dv_ind, uint32_t sensor_ind)`

Reset the fan to automatic driver control.

This function returns control of the fan to the system

Parameters

in	<i>dv_ind</i>	a device index
in	<i>sensor_ind</i>	a 0-based sensor index. Normally, this will be 0. If a device has more than one sensor, it could be greater than 0.

Return values

<i>RSMI_STATUS_SUCCESS</i>	is returned upon successful call.
--	-----------------------------------

5.1.4.18 `rsmi_status_t rsmi_dev_fan_rpms_get (uint32_t dv_ind, uint32_t sensor_ind, int64_t * speed)`

Get the fan speed in RPMs of the device with the specified device index and 0-based sensor index.

Given a device index *dv_ind* and a pointer to a `uint32_t` *speed*, this function will write the current fan speed in RPMs to the `uint32_t` pointed to by *speed*

Parameters

in	<i>dv_ind</i>	a device index
in	<i>sensor_ind</i>	a 0-based sensor index. Normally, this will be 0. If a device has more than one sensor, it could be greater than 0.
in, out	<i>speed</i>	a pointer to <code>uint32_t</code> to which the speed will be written

Return values

<i>RSMI_STATUS_SUCCESS</i>	is returned upon successful call.
--	-----------------------------------

5.1.4.19 `rsmi_status_t rsmi_dev_fan_speed_get (uint32_t dv_ind, uint32_t sensor_ind, int64_t * speed)`

Get the fan speed for the specified device in RPMs.

Given a device index *dv_ind* this function will get the fan speed.

Parameters

in	<i>dv_ind</i>	a device index
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Given a device index *dv_ind* and a pointer to a `uint32_t` *speed*, this function will write the current fan speed (a value between 0 and 255) to the `uint32_t` pointed to by *speed*

Parameters

in	<i>dv_ind</i>	a device index
in	<i>sensor_ind</i>	a 0-based sensor index. Normally, this will be 0. If a device has more than one sensor, it could be greater than 0.
in, out	<i>speed</i>	a pointer to <code>uint32_t</code> to which the speed will be written

Return values

<i>RSMI_STATUS_SUCCESS</i>	is returned upon successful call.
--	-----------------------------------

5.1.4.20 `rsmi_status_t rsmi_dev_fan_speed_max_get (uint32_t dv_ind, uint32_t sensor_ind, uint64_t * max_speed)`

Get the max. fan speed of the device with provided device index.

Given a device index *dv_ind* and a pointer to a `uint32_t` *max_speed*, this function will write the maximum fan speed possible to the `uint32_t` pointed to by *max_speed*

Parameters

in	<i>dv_ind</i>	a device index
in	<i>sensor_ind</i>	a 0-based sensor index. Normally, this will be 0. If a device has more than one sensor, it could be greater than 0.
in, out	<i>max_speed</i>	a pointer to <code>uint32_t</code> to which the maximum speed will be written

Return values

<i>RSMI_STATUS_SUCCESS</i>	is returned upon successful call.
--	-----------------------------------

5.1.4.21 `rsmi_status_t rsmi_dev_fan_speed_set (uint32_t dv_ind, uint32_t sensor_ind, uint64_t speed)`

Set the fan speed for the specified device with the provided speed, in RPMs.

Given a device index *dv_ind* and a integer value indicating speed *speed*, this function will attempt to set the fan speed to *speed*. An error will be returned if the specified speed is outside the allowable range for the device. The maximum value is 255 and the minimum is 0.

Parameters

in	<i>dv_ind</i>	a device index
in	<i>sensor_ind</i>	a 0-based sensor index. Normally, this will be 0. If a device has more than one sensor, it could be greater than 0.
in	<i>speed</i>	the speed to which the function will attempt to set the fan

Return values

RSMI_STATUS_SUCCESS	is returned upon successful call.
-------------------------------------	-----------------------------------

5.1.4.22 `rsmi_status_t rsmi_dev_od_volt_info_get (uint32_t dv_ind, rsmi_od_volt_freq_data * odv)`

This function retrieves the voltage/frequency curve information.

Given a device index `dv_ind` and a pointer to a [rsmi_od_volt_freq_data](#) structure `odv`, this function will populate `odv`. See [rsmi_od_volt_freq_data](#) for more details.

Parameters

in	<code>dv_ind</code>	a device index
in	<code>odv</code>	a pointer to an rsmi_od_volt_freq_data structure

Return values

RSMI_STATUS_SUCCESS	is returned upon successful call.
-------------------------------------	-----------------------------------

5.1.4.23 `rsmi_status_t rsmi_dev_od_volt_curve_regions_get (uint32_t dv_ind, uint32_t * num_regions, rsmi_freq_volt_region * buffer)`

This function will retrieve the current valid regions in the frequency/voltage space.

Given a device index `dv_ind`, a pointer to an unsigned integer `num_regions` and a buffer of [rsmi_freq_volt_region](#) structures, `buffer`, this function will populate `buffer` with the current frequency-volt space regions. The caller should assign `buffer` to memory that can be written to by this function. The caller should also indicate the number of [rsmi_freq_volt_region](#) structures that can safely be written to `buffer` in `num_regions`.

The number of regions to expect this function provide (`num_regions`) can be obtained by calling [rsmi_dev_od_volt_info_get\(\)](#).

Parameters

in	<code>dv_ind</code>	a device index
in, out	<code>num_regions</code>	As input, this is the number of rsmi_freq_volt_region structures that can be written to <code>buffer</code> . As output, this is the number of rsmi_freq_volt_region structures that were actually written.
in, out	<code>buffer</code>	a caller provided buffer to which rsmi_freq_volt_region structures will be written

Return values

RSMI_STATUS_SUCCESS	is returned upon successful call.
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5.1.4.24 `rsmi_status_t rsmi_dev_power_ave_get (uint32_t dv_ind, uint32_t sensor_ind, uint64_t * power)`

Get the average power consumption of the device with provided device index.

Given a device index `dv_ind` and a pointer to a `uint64_t power`, this function will write the current average power consumption to the `uint64_t` in microwatts pointed to by `power`. This function requires root privilege.

Parameters

in	<i>dv_ind</i>	a device index
in	<i>sensor_ind</i>	a 0-based sensor index. Normally, this will be 0. If a device has more than one sensor, it could be greater than 0.
in, out	<i>power</i>	a pointer to <code>uint64_t</code> to which the average power consumption will be written

Return values

<code>RSMI_STATUS_SUCCESS</code>	is returned upon successful call.
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5.1.4.25 `rsmi_status_t rsmi_dev_power_cap_get (uint32_t dv_ind, uint32_t sensor_ind, uint64_t * cap)`

Get the cap on power which, when reached, causes the system to take action to reduce power.

When power use rises above the value `power`, the system will take action to reduce power use. The power level returned through `power` will be in microWatts.

Parameters

in	<i>dv_ind</i>	a device index
in	<i>sensor_ind</i>	a 0-based sensor index. Normally, this will be 0. If a device has more than one sensor, it could be greater than 0.
in, out	<i>cap</i>	a pointer to a <code>uint64_t</code> that indicates the power cap, in microwatts

Return values

<code>RSMI_STATUS_SUCCESS</code>	is returned upon successful call.
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5.1.4.26 `rsmi_status_t rsmi_dev_power_cap_range_get (uint32_t dv_ind, uint32_t sensor_ind, uint64_t * max, uint64_t * min)`

Get the range of valid values for the power cap.

This function will return the maximum possible valid power cap `max` and the minimum possible valid power cap `min`

Parameters

in	<i>dv_ind</i>	a device index
in	<i>sensor_ind</i>	a 0-based sensor index. Normally, this will be 0. If a device has more than one sensor, it could be greater than 0.
in, out	<i>max</i>	a pointer to a <code>uint64_t</code> that indicates the maximum possible power cap, in microwatts
in, out	<i>min</i>	a pointer to a <code>uint64_t</code> that indicates the minimum possible power cap, in microwatts

Return values

RSMI_STATUS_SUCCESS	is returned upon successful call.
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5.1.4.27 `rsmi_status_t rsmi_dev_power_cap_set (uint32_t dv_ind, uint32_t sensor_ind, uint64_t cap)`

Set the power cap value.

This function will set the power cap to the provided value `cap`. `cap` must be between the minimum and maximum power cap values set by the system, which can be obtained from [rsmi_dev_power_cap_range_get](#).

Parameters

in	<i>dv_ind</i>	a device index
in	<i>sensor_ind</i>	a 0-based sensor index. Normally, this will be 0. If a device has more than one sensor, it could be greater than 0.
in, out	<i>cap</i>	a <code>uint64_t</code> that indicates the desired power cap, in microwatts

Return values

RSMI_STATUS_SUCCESS	is returned upon successful call.
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5.1.4.28 `rsmi_status_t rsmi_dev_power_profile_presets_get (uint32_t dv_ind, uint32_t sensor_ind, rsmi_power_profile_status * status)`

Get the list of available preset power profiles and an indication of which profile is currently active.

Given a device index `dv_ind` and a pointer to a [rsmi_power_profile_status](#) `status`, this function will set the bits of the `rsmi_power_profile_status.available_profiles` bit field of `status` to 1 if the profile corresponding to the respective `rsmi_power_profile_preset_masks` profiles are enabled. For example, if both the VIDEO and VR power profiles are available selections, then `RSMI_PWR_PROF_PRST_VIDEO_MASK` AND'ed with [rsmi_power_profile_status.available_profiles](#) will be non-zero as will `RSMI_PWR_PROF_PRST_VR_MASK` AND'ed with [rsmi_power_profile_status.available_profiles](#). Additionally, `rsmi_power_profile_status.current` will be set to the `rsmi_power_profile_preset_masks` of the profile that is currently active.

Parameters

in	<i>dv_ind</i>	a device index
in	<i>sensor_ind</i>	a 0-based sensor index. Normally, this will be 0. If a device has more than one sensor, it could be greater than 0.
in, out	<i>status</i>	a pointer to rsmi_power_profile_status that will be populated by a call to this function

Return values

RSMI_STATUS_SUCCESS	is returned upon successful call.
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5.1.4.29 `rsmi_status_t rsmi_dev_power_profile_set (uint32_t dv_ind, uint32_t sensor_ind, rsmi_power_profile_preset_masks profile)`

Set the power profile.

Given a device index `dv_ind`, a sensor index `sensor_ind`, and a `profile`, this function will attempt to set the current profile to the provided profile. The provided profile must be one of the currently supported profiles, as indicated by a call to [rsmi_dev_power_profile_presets_get\(\)](#)

Parameters

in	<i>dv_ind</i>	a device index
in	<i>sensor_ind</i>	a 0-based sensor index. Normally, this will be 0. If a device has more than one sensor, it could be greater than 0.
in	<i>profile</i>	a <code>rsmi_power_profile_preset_masks</code> that hold the mask of the desired new power profile

Return values

RSMI_STATUS_SUCCESS	is returned upon successful call.
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5.1.4.30 `rsmi_status_t rsmi_status_string (rsmi_status_t status, const char ** status_string)`

Get a description of a provided RSMI error status.

Set the provided pointer to a const char *, `status_string`, to a string containing a description of the provided error code `status`.

Parameters

in	<i>status</i>	The error status for which a description is desired
in, out	<i>status_string</i>	A pointer to a const char * which will be made to point to a description of the provided error code

Return values

RSMI_STATUS_SUCCESS	is returned upon successful call
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5.1.4.31 `rsmi_status_t rsmi_version_get (rsmi_version * version)`

Get the build version information for the currently running build of RSMI.

Get the major, minor, patch and build string for RSMI build currently in use through `version`

Parameters

in, out	<i>version</i>	A pointer to an rsmi_version structure that will be updated with the version information upon return.
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Return values

<i>RSMI_STATUS_SUCCESS</i>	is returned upon successful call
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Index

available_profiles
 rsmi_power_profile_status, [11](#)

curr_mclk_range
 rsmi_od_volt_freq_data, [9](#)

current
 rsmi_frequencies, [8](#)
 rsmi_power_profile_status, [11](#)

frequency
 rsmi_frequencies, [8](#)

lanes
 rsmi_pcie_bandwidth, [10](#)

num_profiles
 rsmi_power_profile_status, [11](#)

num_supported
 rsmi_frequencies, [8](#)

RSMI_CLK_TYPE_MEM
 rocm_smi.h, [17](#)

RSMI_CLK_TYPE_SYS
 rocm_smi.h, [17](#)

RSMI_DEV_PERF_LEVEL_AUTO
 rocm_smi.h, [17](#)

RSMI_DEV_PERF_LEVEL_HIGH
 rocm_smi.h, [17](#)

RSMI_DEV_PERF_LEVEL_LOW
 rocm_smi.h, [17](#)

RSMI_DEV_PERF_LEVEL_MANUAL
 rocm_smi.h, [17](#)

RSMI_DEV_PERF_LEVEL_STABLE_MIN_MCLK
 rocm_smi.h, [17](#)

RSMI_DEV_PERF_LEVEL_STABLE_MIN_SCLK
 rocm_smi.h, [17](#)

RSMI_DEV_PERF_LEVEL_STABLE_PEAK
 rocm_smi.h, [17](#)

RSMI_DEV_PERF_LEVEL_STABLE_STD
 rocm_smi.h, [17](#)

RSMI_DEV_PERF_LEVEL_UNKNOWN
 rocm_smi.h, [17](#)

RSMI_FREQ_IND_INVALID
 rocm_smi.h, [18](#)

RSMI_FREQ_IND_MAX
 rocm_smi.h, [18](#)

RSMI_FREQ_IND_MIN
 rocm_smi.h, [18](#)

RSMI_MAX_FAN_SPEED
 rocm_smi.h, [16](#)

RSMI_PWR_PROF_PRST_BOOTUP_DEFAULT
 rocm_smi.h, [18](#)

RSMI_PWR_PROF_PRST_COMPUTE_MASK
 rocm_smi.h, [18](#)

RSMI_PWR_PROF_PRST_CUSTOM_MASK
 rocm_smi.h, [18](#)

RSMI_PWR_PROF_PRST_LAST
 rocm_smi.h, [18](#)

RSMI_PWR_PROF_PRST_POWER_SAVING_MASK
 rocm_smi.h, [18](#)

RSMI_PWR_PROF_PRST_VIDEO_MASK
 rocm_smi.h, [18](#)

RSMI_PWR_PROF_PRST_VR_MASK
 rocm_smi.h, [18](#)

RSMI_STATUS_FILE_ERROR
 rocm_smi.h, [17](#)

RSMI_STATUS_INIT_ERROR
 rocm_smi.h, [17](#)

RSMI_STATUS_INPUT_OUT_OF_BOUNDS
 rocm_smi.h, [17](#)

RSMI_STATUS_INTERNAL_EXCEPTION
 rocm_smi.h, [17](#)

RSMI_STATUS_INVALID_ARGS
 rocm_smi.h, [16](#)

RSMI_STATUS_NOT_SUPPORTED
 rocm_smi.h, [16](#)

RSMI_STATUS_NOT_YET_IMPLEMENTED
 rocm_smi.h, [17](#)

RSMI_STATUS_OUT_OF_RESOURCES
 rocm_smi.h, [17](#)

RSMI_STATUS_PERMISSION
 rocm_smi.h, [17](#)

RSMI_STATUS_SUCCESS
 rocm_smi.h, [16](#)

RSMI_STATUS_UNKNOWN_ERROR
 rocm_smi.h, [17](#)

RSMI_TEMP_CRIT_MIN_HYST
 rocm_smi.h, [18](#)

RSMI_TEMP_CRIT_MIN
 rocm_smi.h, [18](#)

RSMI_TEMP_CRITICAL_HYST
 rocm_smi.h, [18](#)

RSMI_TEMP_CRITICAL
 rocm_smi.h, [18](#)

RSMI_TEMP_CURRENT
 rocm_smi.h, [18](#)

RSMI_TEMP_EMERGENCY_HYST
 rocm_smi.h, [18](#)

RSMI_TEMP_EMERGENCY
 rocm_smi.h, [18](#)

RSMI_TEMP_HIGHEST
 rocm_smi.h, 18
 RSMI_TEMP_LOWEST
 rocm_smi.h, 18
 RSMI_TEMP_MAX_HYST
 rocm_smi.h, 18
 RSMI_TEMP_MAX
 rocm_smi.h, 18
 RSMI_TEMP_MIN_HYST
 rocm_smi.h, 18
 RSMI_TEMP_MIN
 rocm_smi.h, 18
 RSMI_TEMP_OFFSET
 rocm_smi.h, 18
 rocm_smi.h, 13
 RSMI_CLK_TYPE_MEM, 17
 RSMI_CLK_TYPE_SYS, 17
 RSMI_DEV_PERF_LEVEL_AUTO, 17
 RSMI_DEV_PERF_LEVEL_HIGH, 17
 RSMI_DEV_PERF_LEVEL_LOW, 17
 RSMI_DEV_PERF_LEVEL_MANUAL, 17
 RSMI_DEV_PERF_LEVEL_STABLE_MIN_MCLK,
 17
 RSMI_DEV_PERF_LEVEL_STABLE_MIN_SCLK,
 17
 RSMI_DEV_PERF_LEVEL_STABLE_PEAK, 17
 RSMI_DEV_PERF_LEVEL_STABLE_STD, 17
 RSMI_DEV_PERF_LEVEL_UNKNOWN, 17
 RSMI_FREQ_IND_INVALID, 18
 RSMI_FREQ_IND_MAX, 18
 RSMI_FREQ_IND_MIN, 18
 RSMI_MAX_FAN_SPEED, 16
 RSMI_PWR_PROF_PRST_BOOTUP_DEFAULT,
 18
 RSMI_PWR_PROF_PRST_COMPUTE_MASK, 18
 RSMI_PWR_PROF_PRST_CUSTOM_MASK, 18
 RSMI_PWR_PROF_PRST_LAST, 18
 RSMI_PWR_PROF_PRST_POWER_SAVING_↔
 MASK, 18
 RSMI_PWR_PROF_PRST_VIDEO_MASK, 18
 RSMI_PWR_PROF_PRST_VR_MASK, 18
 RSMI_STATUS_FILE_ERROR, 17
 RSMI_STATUS_INIT_ERROR, 17
 RSMI_STATUS_INPUT_OUT_OF_BOUNDS, 17
 RSMI_STATUS_INTERNAL_EXCEPTION, 17
 RSMI_STATUS_INVALID_ARGS, 16
 RSMI_STATUS_NOT_SUPPORTED, 16
 RSMI_STATUS_NOT_YET_IMPLEMENTED, 17
 RSMI_STATUS_OUT_OF_RESOURCES, 17
 RSMI_STATUS_PERMISSION, 17
 RSMI_STATUS_SUCCESS, 16
 RSMI_STATUS_UNKNOWN_ERROR, 17
 RSMI_TEMP_CRIT_MIN_HYST, 18
 RSMI_TEMP_CRIT_MIN, 18
 RSMI_TEMP_CRITICAL_HYST, 18
 RSMI_TEMP_CRITICAL, 18
 RSMI_TEMP_CURRENT, 18
 RSMI_TEMP_EMERGENCY_HYST, 18
 RSMI_TEMP_EMERGENCY, 18
 RSMI_TEMP_HIGHEST, 18
 RSMI_TEMP_LOWEST, 18
 RSMI_TEMP_MAX_HYST, 18
 RSMI_TEMP_MAX, 18
 RSMI_TEMP_MIN_HYST, 18
 RSMI_TEMP_MIN, 18
 RSMI_TEMP_OFFSET, 18
 rsmi_clk_type, 17
 rsmi_dev_busy_percent_get, 20
 rsmi_dev_fan_reset, 25
 rsmi_dev_fan_rpms_get, 25
 rsmi_dev_fan_speed_get, 25
 rsmi_dev_fan_speed_max_get, 26
 rsmi_dev_fan_speed_set, 26
 rsmi_dev_gpu_clk_freq_get, 23
 rsmi_dev_gpu_clk_freq_set, 23
 rsmi_dev_id_get, 21
 rsmi_dev_name_get, 24
 rsmi_dev_od_volt_curve_regions_get, 27
 rsmi_dev_od_volt_info_get, 27
 rsmi_dev_overdrive_level_get, 22
 rsmi_dev_overdrive_level_set, 22
 rsmi_dev_pci_bandwidth_get, 19
 rsmi_dev_pci_bandwidth_set, 20
 rsmi_dev_pci_id_get, 20
 rsmi_dev_perf_level, 17
 rsmi_dev_perf_level_get, 21
 rsmi_dev_perf_level_set, 22
 rsmi_dev_power_ave_get, 27
 rsmi_dev_power_cap_get, 28
 rsmi_dev_power_cap_range_get, 28
 rsmi_dev_power_cap_set, 29
 rsmi_dev_power_profile_presets_get, 29
 rsmi_dev_power_profile_set, 29
 rsmi_dev_temp_metric_get, 24
 rsmi_freq_ind, 18
 rsmi_init, 19
 rsmi_num_monitor_devices, 19
 rsmi_power_profile_preset_masks, 18
 rsmi_shut_down, 19
 rsmi_status_string, 30
 rsmi_status_t, 16
 rsmi_temperature_metric, 17
 rsmi_version_get, 30
 rsmi_clk_type
 rocm_smi.h, 17
 rsmi_dev_busy_percent_get
 rocm_smi.h, 20
 rsmi_dev_fan_reset
 rocm_smi.h, 25
 rsmi_dev_fan_rpms_get
 rocm_smi.h, 25
 rsmi_dev_fan_speed_get
 rocm_smi.h, 25
 rsmi_dev_fan_speed_max_get
 rocm_smi.h, 26
 rsmi_dev_fan_speed_set

- rocm_smi.h, 26
- rsmi_dev_gpu_clk_freq_get
 - rocm_smi.h, 23
- rsmi_dev_gpu_clk_freq_set
 - rocm_smi.h, 23
- rsmi_dev_id_get
 - rocm_smi.h, 21
- rsmi_dev_name_get
 - rocm_smi.h, 24
- rsmi_dev_od_volt_curve_regions_get
 - rocm_smi.h, 27
- rsmi_dev_od_volt_info_get
 - rocm_smi.h, 27
- rsmi_dev_overdrive_level_get
 - rocm_smi.h, 22
- rsmi_dev_overdrive_level_set
 - rocm_smi.h, 22
- rsmi_dev_pci_bandwidth_get
 - rocm_smi.h, 19
- rsmi_dev_pci_bandwidth_set
 - rocm_smi.h, 20
- rsmi_dev_pci_id_get
 - rocm_smi.h, 20
- rsmi_dev_perf_level
 - rocm_smi.h, 17
- rsmi_dev_perf_level_get
 - rocm_smi.h, 21
- rsmi_dev_perf_level_set
 - rocm_smi.h, 22
- rsmi_dev_power_ave_get
 - rocm_smi.h, 27
- rsmi_dev_power_cap_get
 - rocm_smi.h, 28
- rsmi_dev_power_cap_range_get
 - rocm_smi.h, 28
- rsmi_dev_power_cap_set
 - rocm_smi.h, 29
- rsmi_dev_power_profile_presets_get
 - rocm_smi.h, 29
- rsmi_dev_power_profile_set
 - rocm_smi.h, 29
- rsmi_dev_temp_metric_get
 - rocm_smi.h, 24
- rsmi_freq_ind
 - rocm_smi.h, 18
- rsmi_freq_volt_region, 7
- rsmi_frequencies, 7
 - current, 8
 - frequency, 8
 - num_supported, 8
- rsmi_init
 - rocm_smi.h, 19
- rsmi_num_monitor_devices
 - rocm_smi.h, 19
- rsmi_od_vddc_point, 8
- rsmi_od_volt_freq_data, 9
 - curr_mclk_range, 9
- rsmi_pcie_bandwidth, 10
 - lanes, 10
 - transfer_rate, 10
- rsmi_power_profile_preset_masks
 - rocm_smi.h, 18
- rsmi_power_profile_status, 10
 - available_profiles, 11
 - current, 11
 - num_profiles, 11
- rsmi_range, 11
- rsmi_shut_down
 - rocm_smi.h, 19
- rsmi_status_string
 - rocm_smi.h, 30
- rsmi_status_t
 - rocm_smi.h, 16
- rsmi_temperature_metric
 - rocm_smi.h, 17
- rsmi_version, 12
- rsmi_version_get
 - rocm_smi.h, 30
- transfer_rate
 - rsmi_pcie_bandwidth, 10