**Angstromers Engineering Solutions Pvt. Ltd.** Cambrian Incubator, Jai Bhuvaneshwari Layout Rd.,

KR Puram, Bangalore-560036

Email: cambrianlabs@cambridge.edu.in Contact: 7829949076 / 6366236363

## Students are requested to form 10 teams and select topics from the following list of projects

SI No	Title	Reference Paper	Github
1	Vectorizing posit operations on RISC-V for faster deep neural networks: experiments and comparison with ARM SVE	<u>Paper</u>	<u>Github</u>
2	CNN using RVV extension	<u>Paper</u>	Github
3	Png filter using RVV	<u>Paper</u>	Github.
4	FIR implementation using RISC-V RVV	<u>Paper</u>	Github.
5	Object Detection Based on RISC-V	<u>Paper</u>	<u>Github</u>
6	Number Theoretic Transform for Encryption Decryption Acceleration using RISC-V	<u>Paper</u>	<u>Github</u>
7	Kyber and Dilithium algorithms implementation using RISC-V	<u>Paper</u>	Github
8	Mobilenet Architecture V2 on RISC-V using RVV	<u>Paper</u>	Github
9	Facial recognition on RISC-V using mobile net architecture	<u>Paper</u>	Github
10	Vector acceleration in extreme-edge computing	<u>Paper</u>	Github
11	Eigen function acceleration RISC-V RVV	<u>Paper</u>	Github
12	Spiking Neural Network on RISC-V	<u>Paper</u>	Github
13	DNN on RISC-V using RVV	<u>Paper</u>	Github
14	Matrix Multiplication Acceleration using RISC-V RVV	<u>Paper</u>	Github
15	ANN accelerator using RISC-V	<u>Paper</u>	Github
16	Accelerating Machine learning algorithm using RISC-V RVV	<u>Paper</u>	Github
17	Efficient RNN-based 5G using RISC-V	<u>Paper</u>	Github
18	Image processing in RISC-V	<u>Paper</u>	Github
19	Optimization of multiplication in the RISC-V architecture using bitmap extension	<u>Paper</u>	<u>Github</u>
20	RISC-V Vector Extension for the Classic McEliece Post-Quantum Cryptosystem	<u>Paper</u>	Github