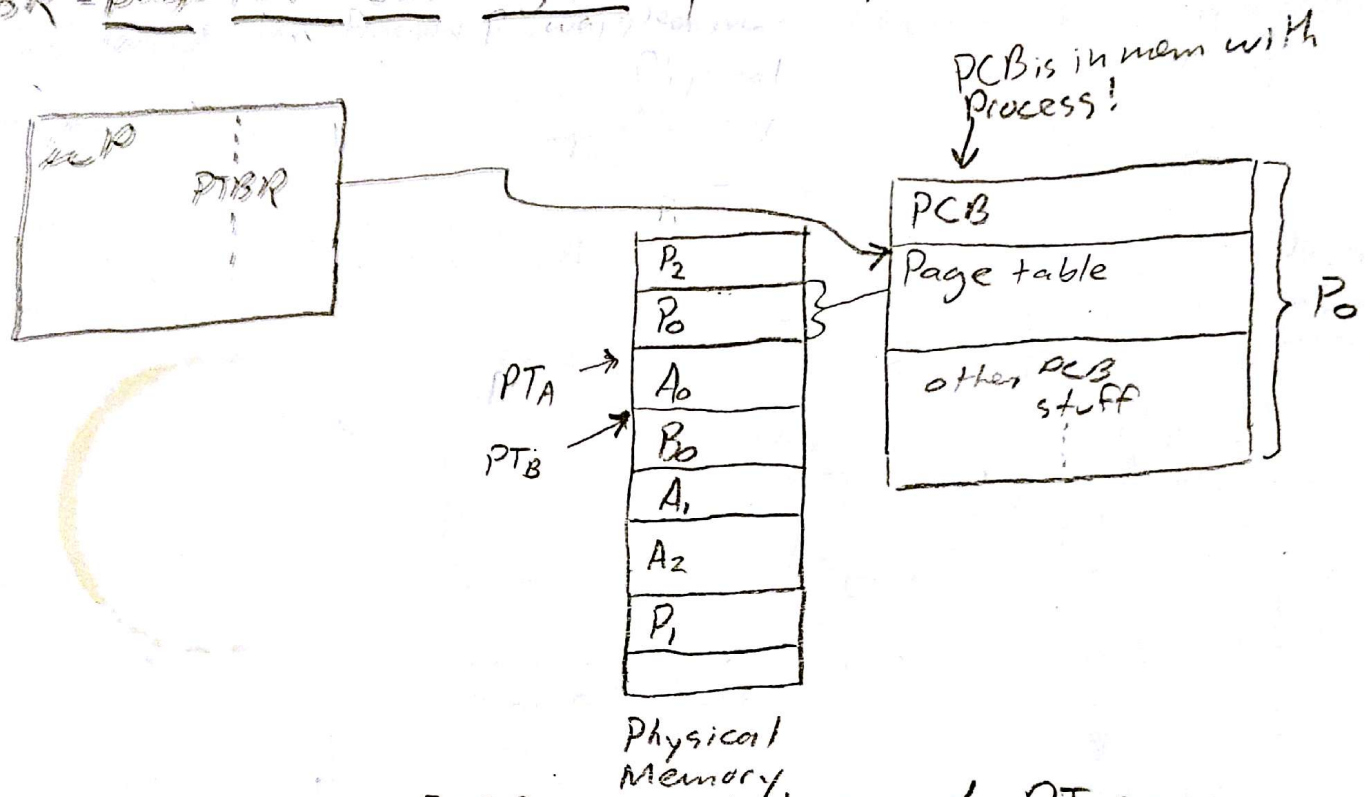


(How Processes stored & accessed in mem?)
 It's in the PCB (part of mem but how is it used?)

PTBR - page table base register. Points to page table in mem.



- OS will switch **PTBR** to point to correct PT on process swap

- Get to store your program all over memory

tradeoff is 2 memory accesses { PT lookup & then
 Physical address build
 then physical address
 lookup

Page Sharing

Threads can share host process memory to communicate
what about processes?

Map shared memory

