- 1 entry per page in virtual address space

even; I the address space is intised

- Entry contains frame # + bits that hell

us; I the page is accessable (in

memory or not, dirty etc.).

large enough to select a frame.

Virtual memory 3

Virtual memory 3

Page size of entry (95, les = 325, ls)

+ypical

4-Kb

232

= 2 32 2 12 * 4 byles = 2 20 * 4 = 4 MByle

Problem; space bable is large even; A process uses very 1846 memory

so our 2 Mb appressos a page table of 4 Mbyles

What about 64 bit address agrace?



= 3.6 x 10 16 byles => bit more Hance have (Smithings)

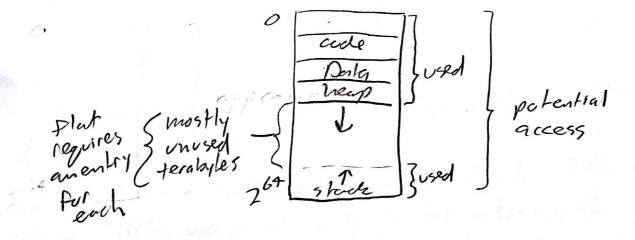
Multi level page table

- reduce the FIZE of Plat page bubles

- Size propertional to how much mem application can address, not how much its using!

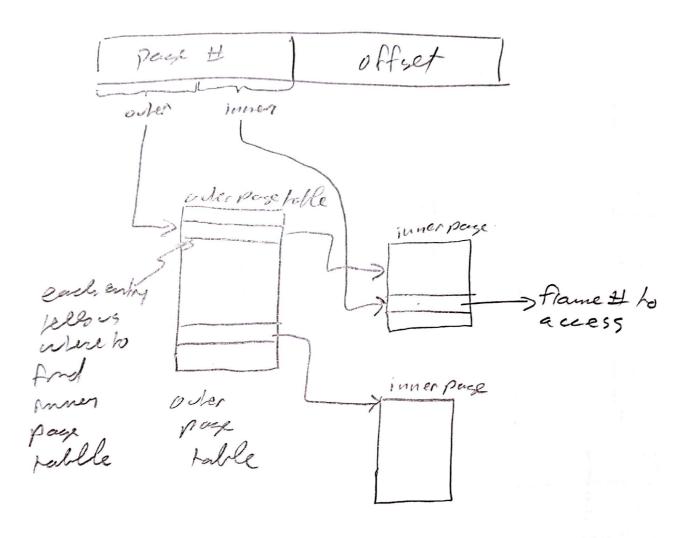
32 bit 2 4-8 Mbyles 64 bit 2 hoobig

how to solve



MolH level

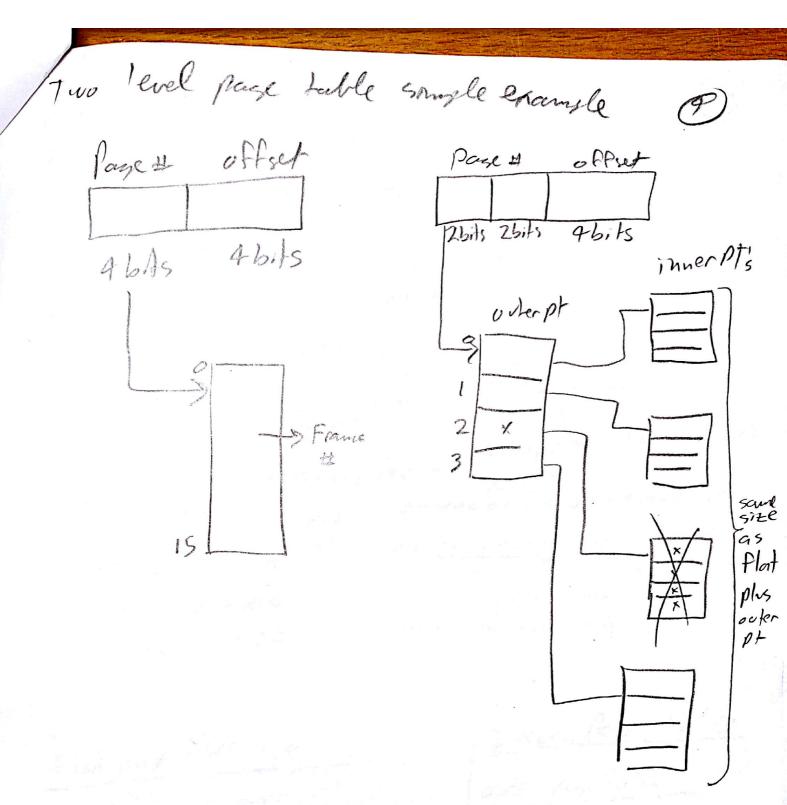
use bits to ordex tables avoid entries for unused entries



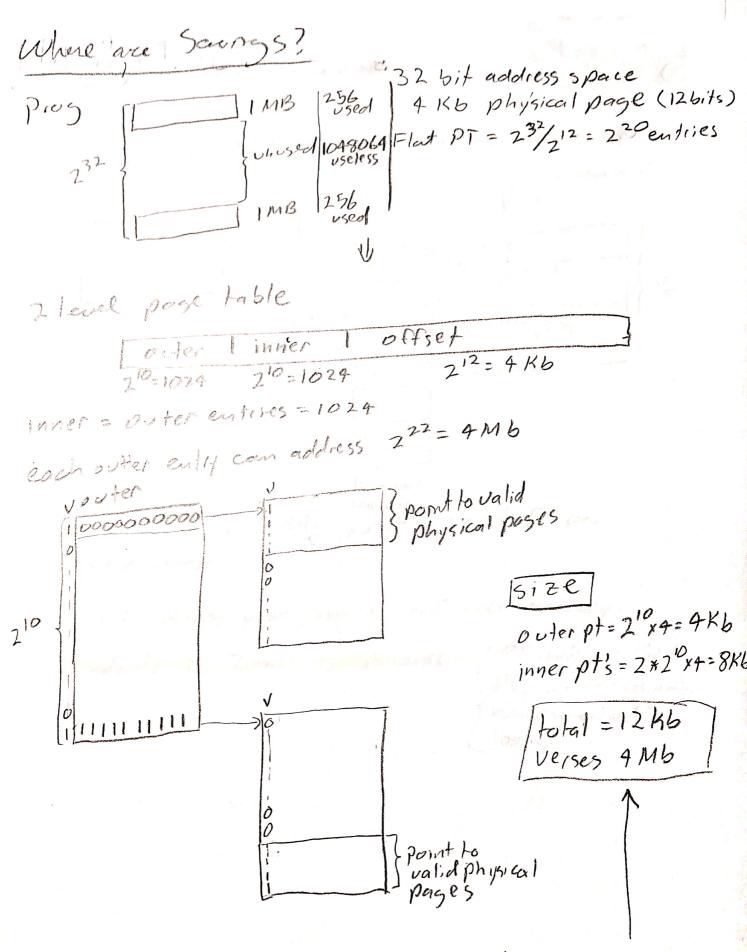
outer page # tello us which of the small muer page 4's to use.

page tables we need to look for a frame #

seems like are garned nothing have just as many page bables (plus the order page bable) as flat bable



Savings? Luben outer page table points
to miner page tables that have no entries
don't need to have those miner page trables
trave! outer page table is young miner page tables



This is why multilevel page tables are used &