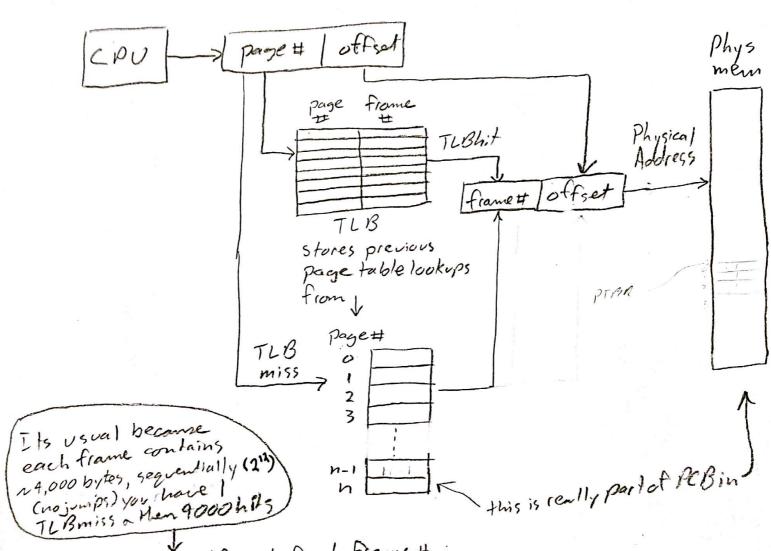
(How Processes stered raciessed m mem?) It's on the PCB (Part of mem but how is it used?) PTBR-page table base register. Ponts topage table on mem. PCBis in mem with Process! Physical PTBR to point to correct PT on - 05 will switch Process swap - Get to strope your program all over memory trade off is 2 memory accesses (PT look-po o then
Physical address build
then physical address
look-p Lets see If we can fix that Translation Lookaside Buffer (TLB) Fast Cache (wften on chip) much fuster than memory stores recent fromglations of logical address -> physical address Small



TLB hit (usual) get fast frame &

TLB miss (unusual) get it from men (page table of process mmen,

That's nice! what happens when context switches?

- 1) Flish entire TLB & start anew (x86) makes sense since all TLB entires are for smithled out process
- (2) Attach process ID to each TLB entry
 -requires additional space (Process ID)
 -only useful if have TLB that is mostly empty

Page Sharing
Threads can share host process memory to communicate
what about processes?
Map shared memory

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