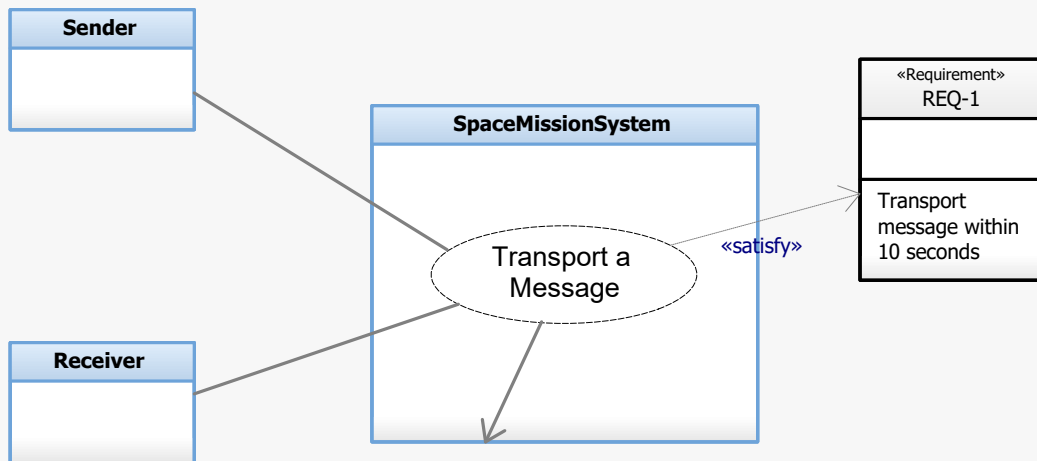
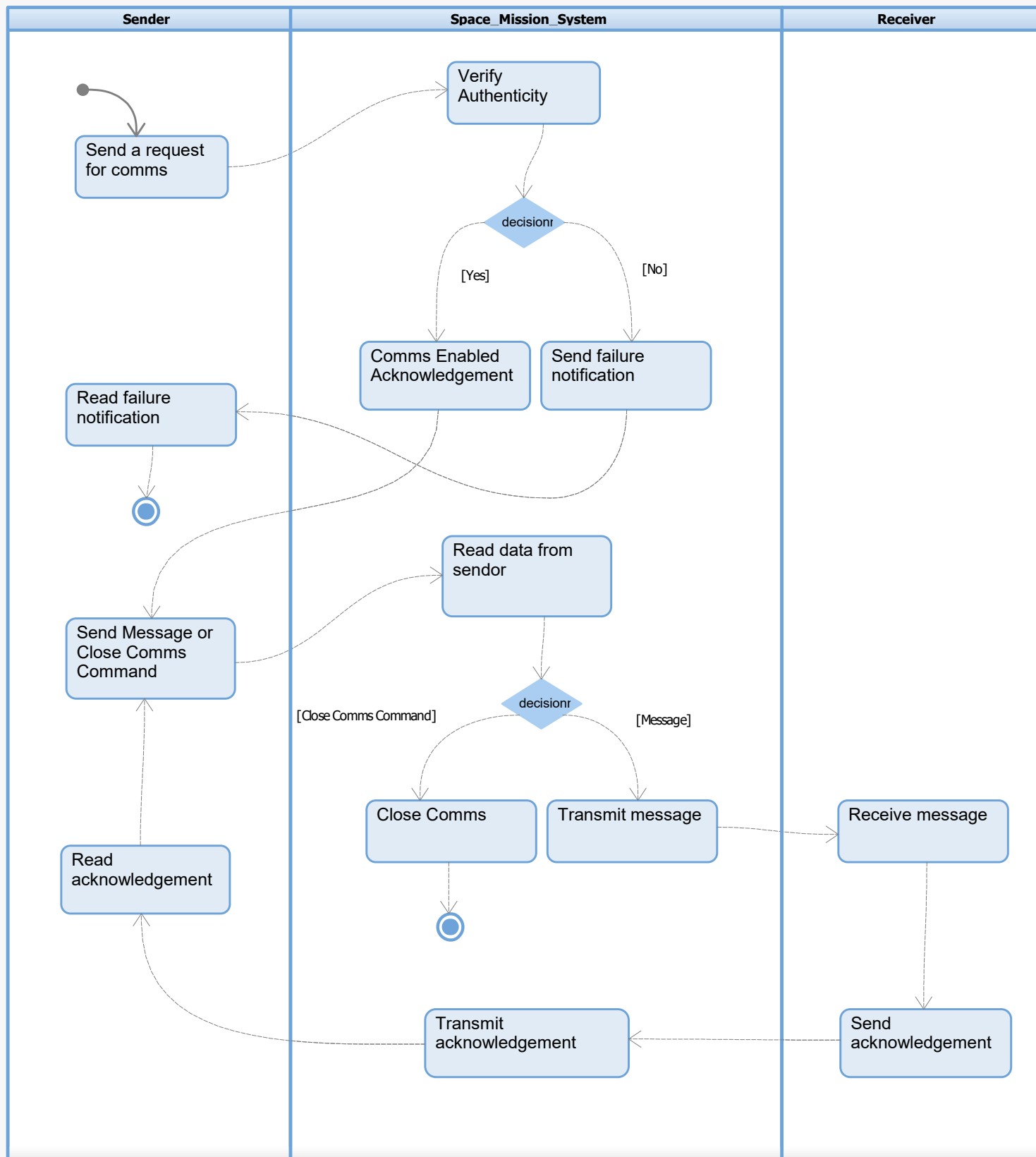


Deliverable 1a, 1b and 1c: Use Case Model
Shows the relationships between the use case, actors and system
of interest - satisfying the given requirement "REQ-1"



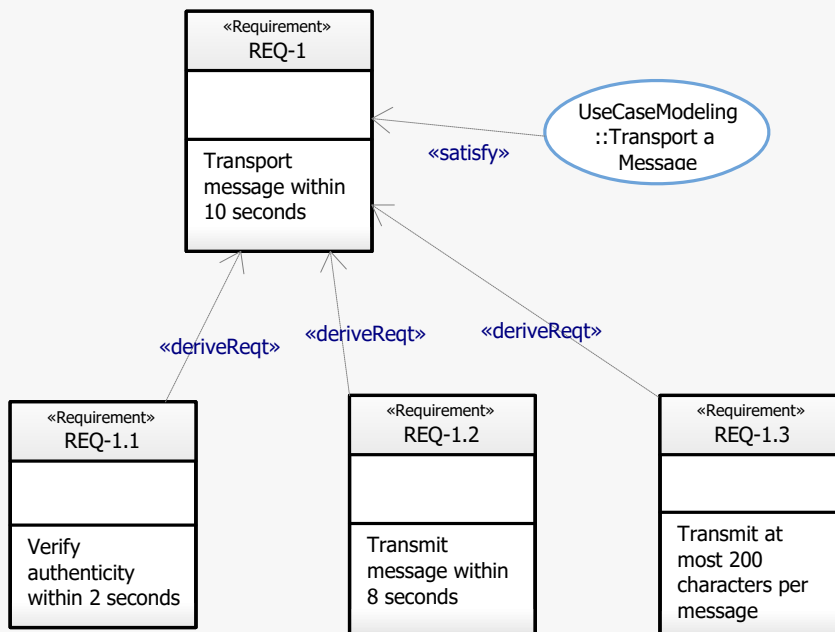
Deliverable 2a: Use Case Scenario using SysML Activity Diagram with swim lanes for actors



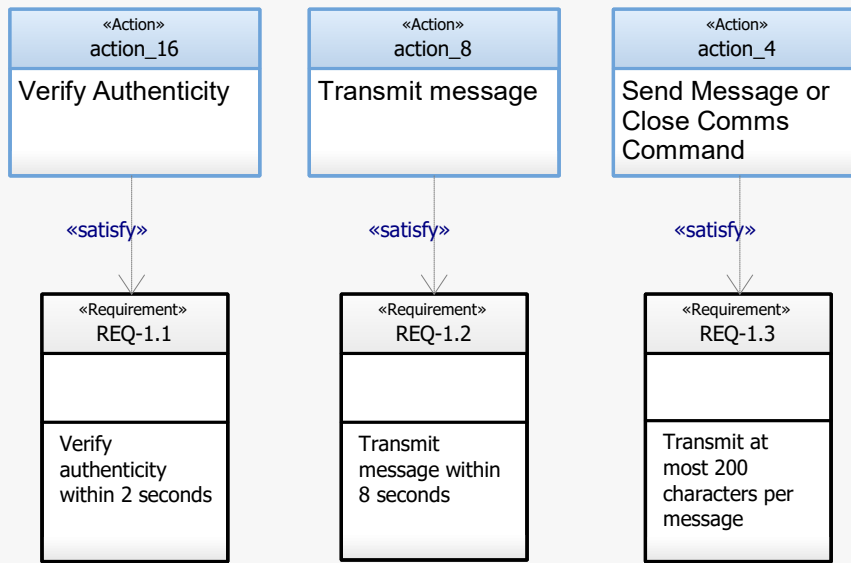
Deliverables 2b and 2c: Dependency relationship between use case and scenario using a Block Definition Diagram.



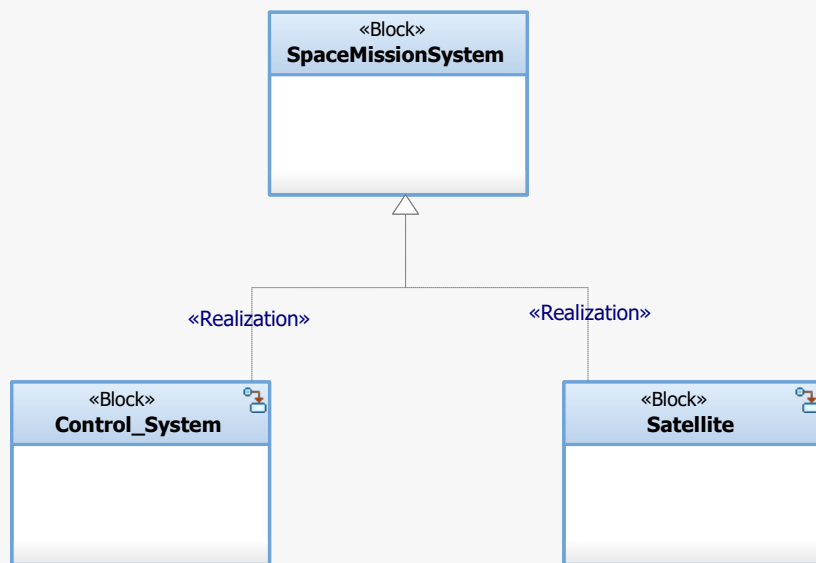
Deliverable 1b and 2d: Requirements Diagram
Represents relationships between the use case and requirements



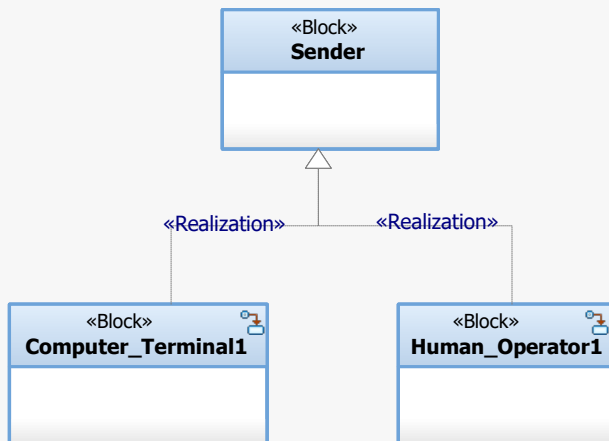
Deliverable 2e: Derived Requirements for scenario actions using a SysML requirements diagram.



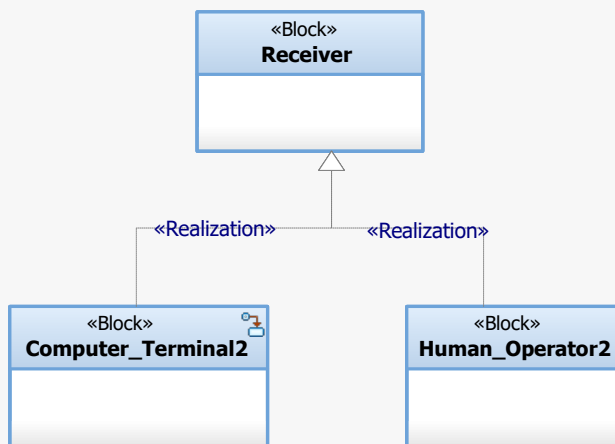
Deliverable 3a: System-level Block Definition Diagram to show realization relationship between the components of Space Mission System. Default behaviors are represented using Finite State Machines for all realizations with complete traceability.



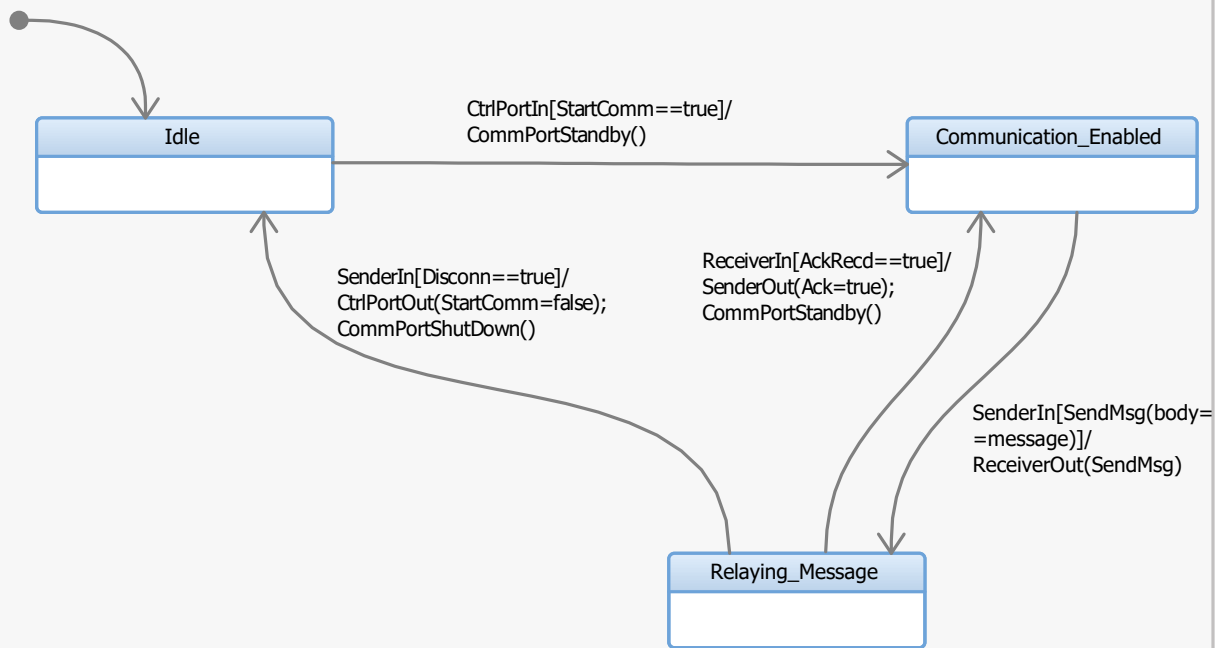
Deliverable 3b: System-level Block Definition Diagram to show realization relationship between the components of Sender. Default behaviors are represented using Finite State Machines for all realizations with complete traceability.



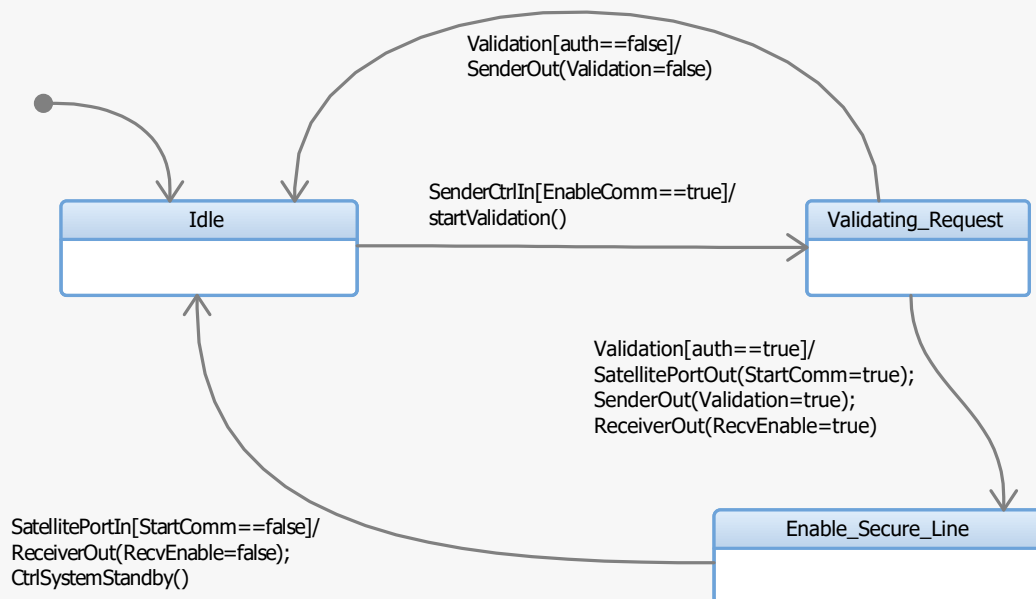
Deliverable 3c: System-level Block Definition Diagram to show realization relationship between the components of Receiver. Default behaviors are represented using Finite State Machines for all realizations with complete traceability.



Deliverable 3d (ii): SysML Finite State Diagram for Satellite

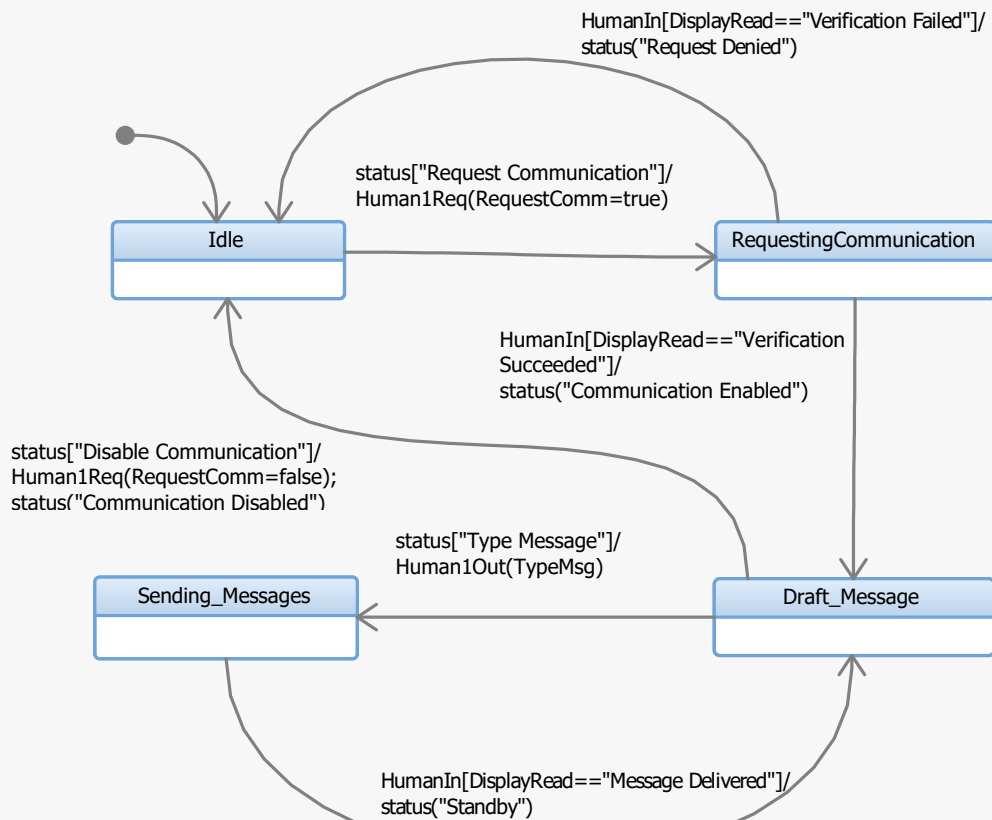


Deliverable 3d (i): SysML Finite State Diagram for Control System

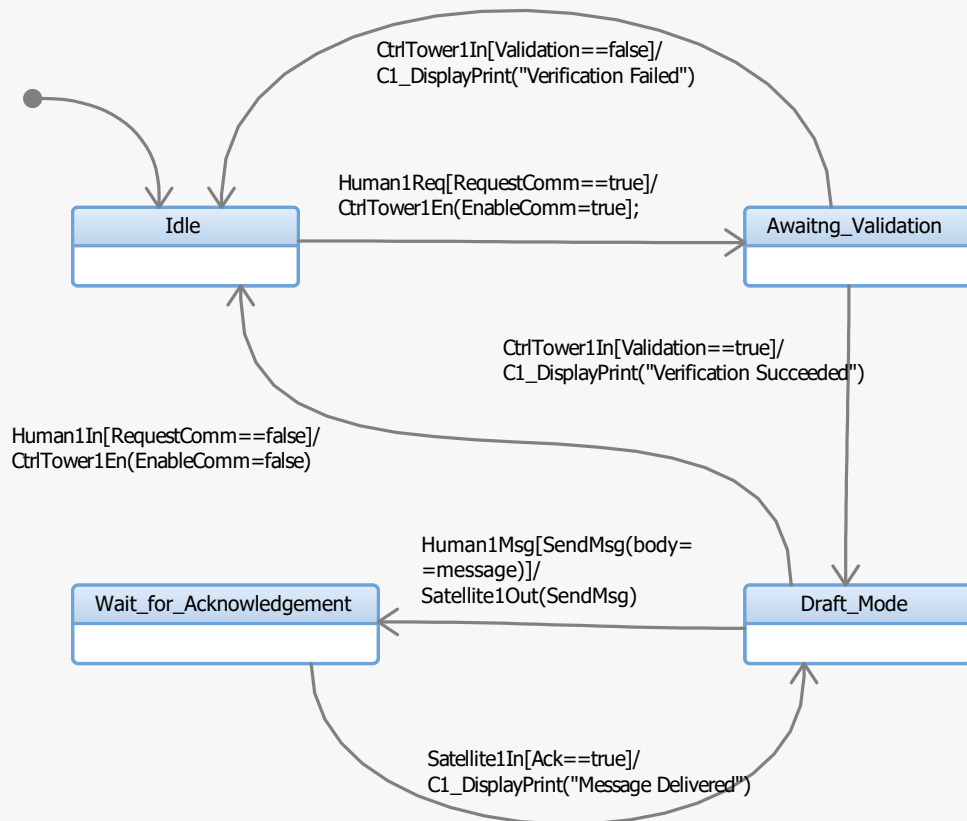


Deliverable 3d (iii): SysML Finite State Diagram for Human Operator1.

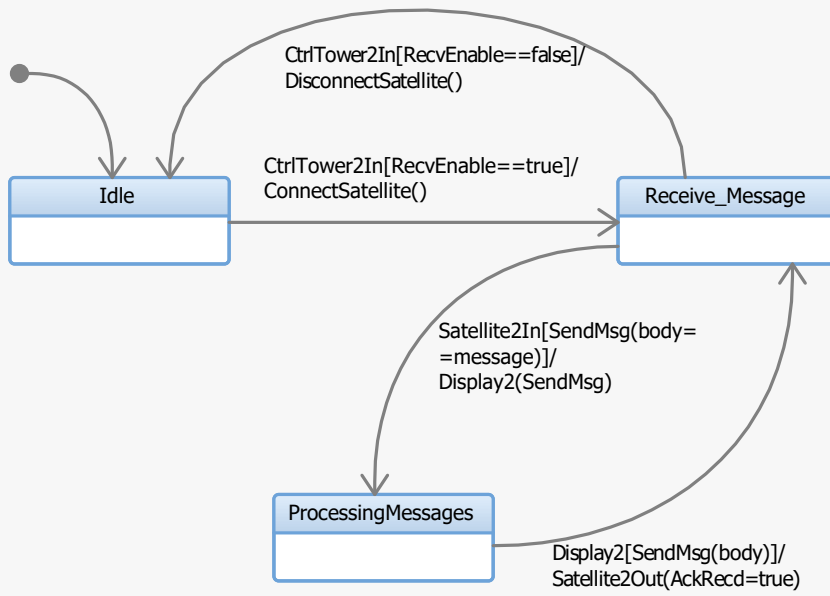
Note: Although a human will not act like a machine, the closest possible action resembling the human's "Thoughts" are represented here using the status() SysML function.



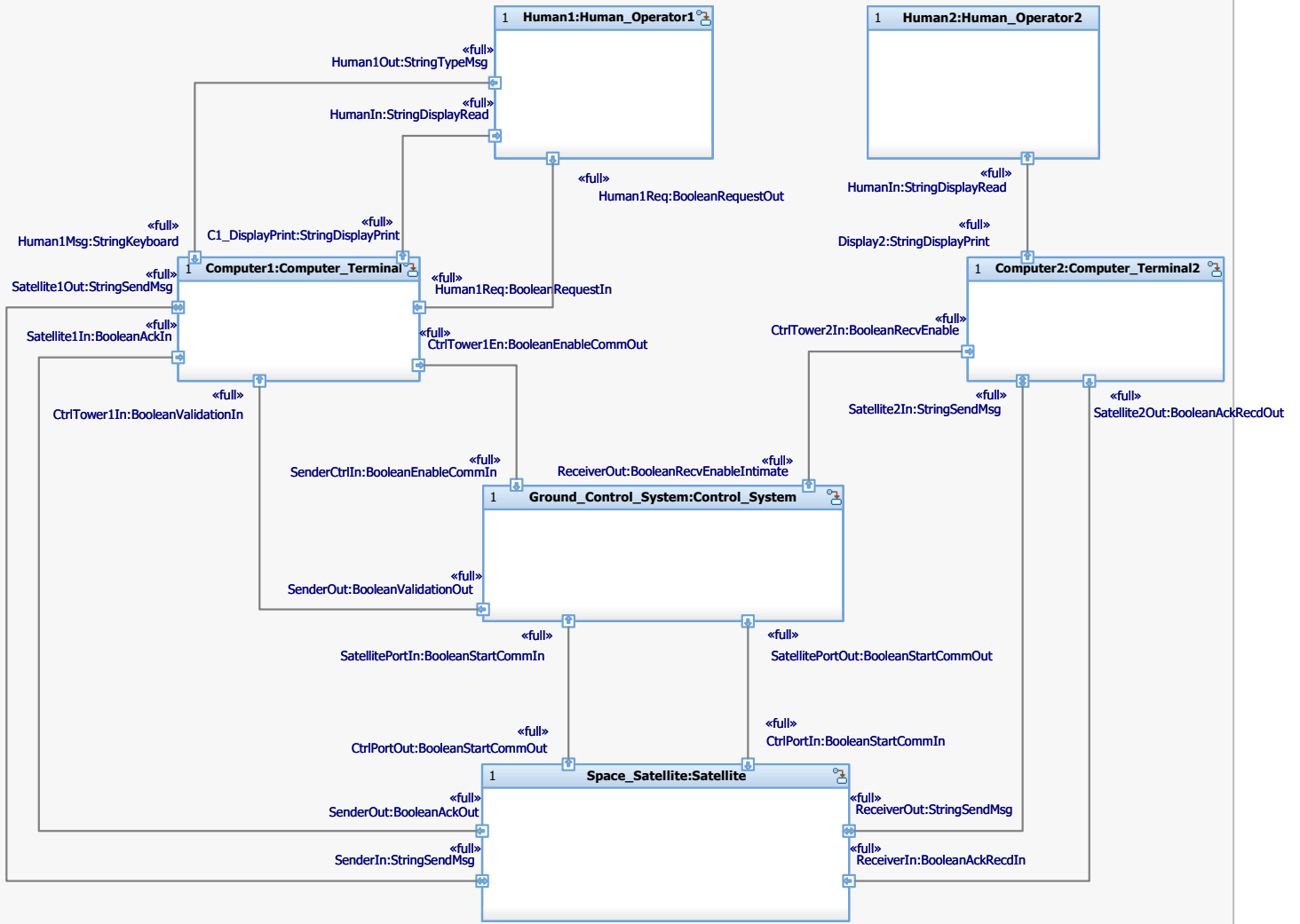
Deliverable 3d (iv): SysML Finite State Diagram for Computer Terminal1.



Deliverable 3d (v): SysML Finite State Diagram for Computer Terminal2.



Deliverable 3f: Internal Block Diagram traceable with all behavior diagrams



Deliverable 3g and 3h: System-level Block Definition Diagram with respective relationships with requirements.

