

NUCLEO64 ext-SMPS

MB1319

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U_mb1319_Top
mb1319_Top.SchDoc



Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.

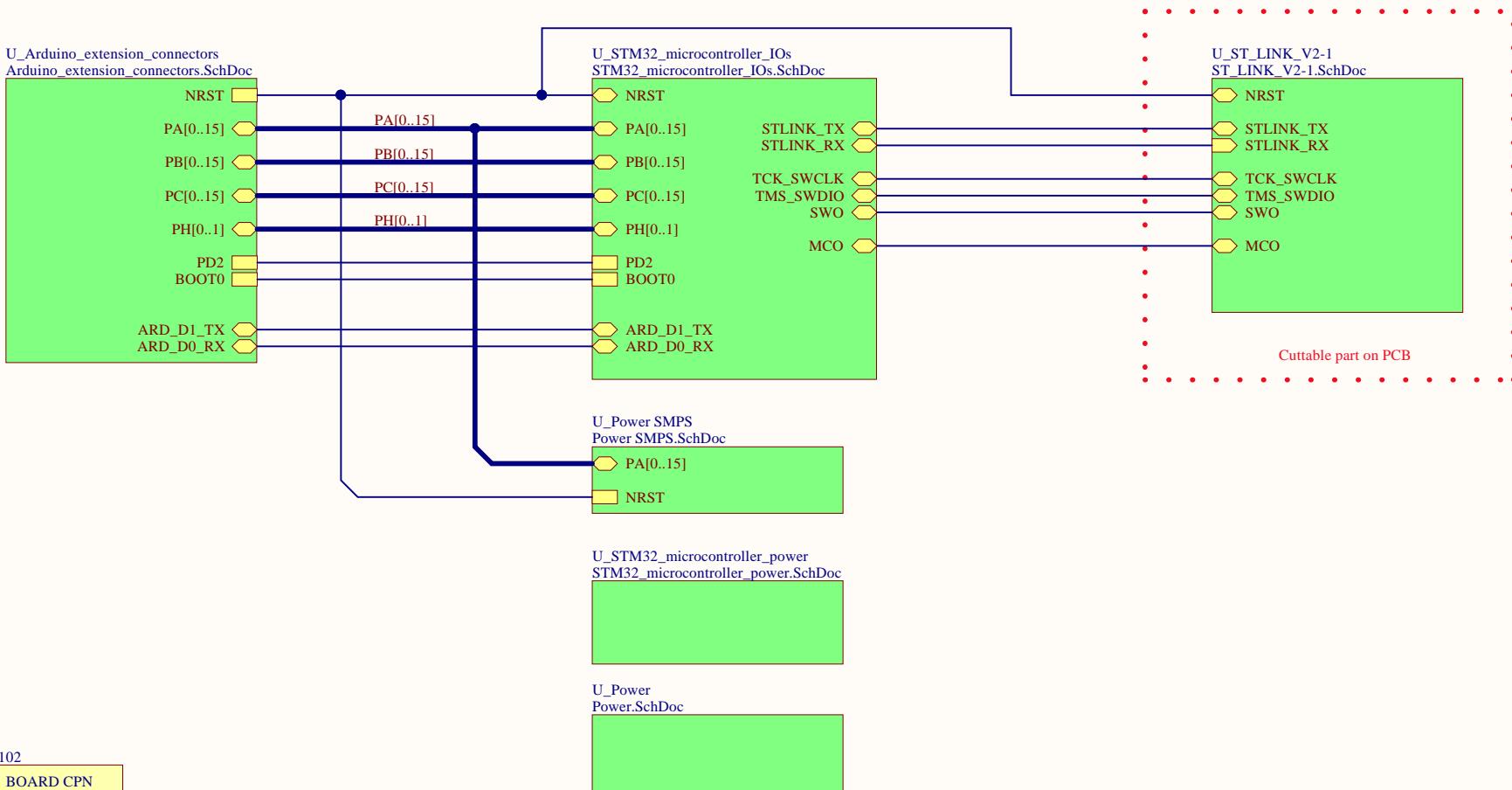
▲ Notes to generate the board layout.

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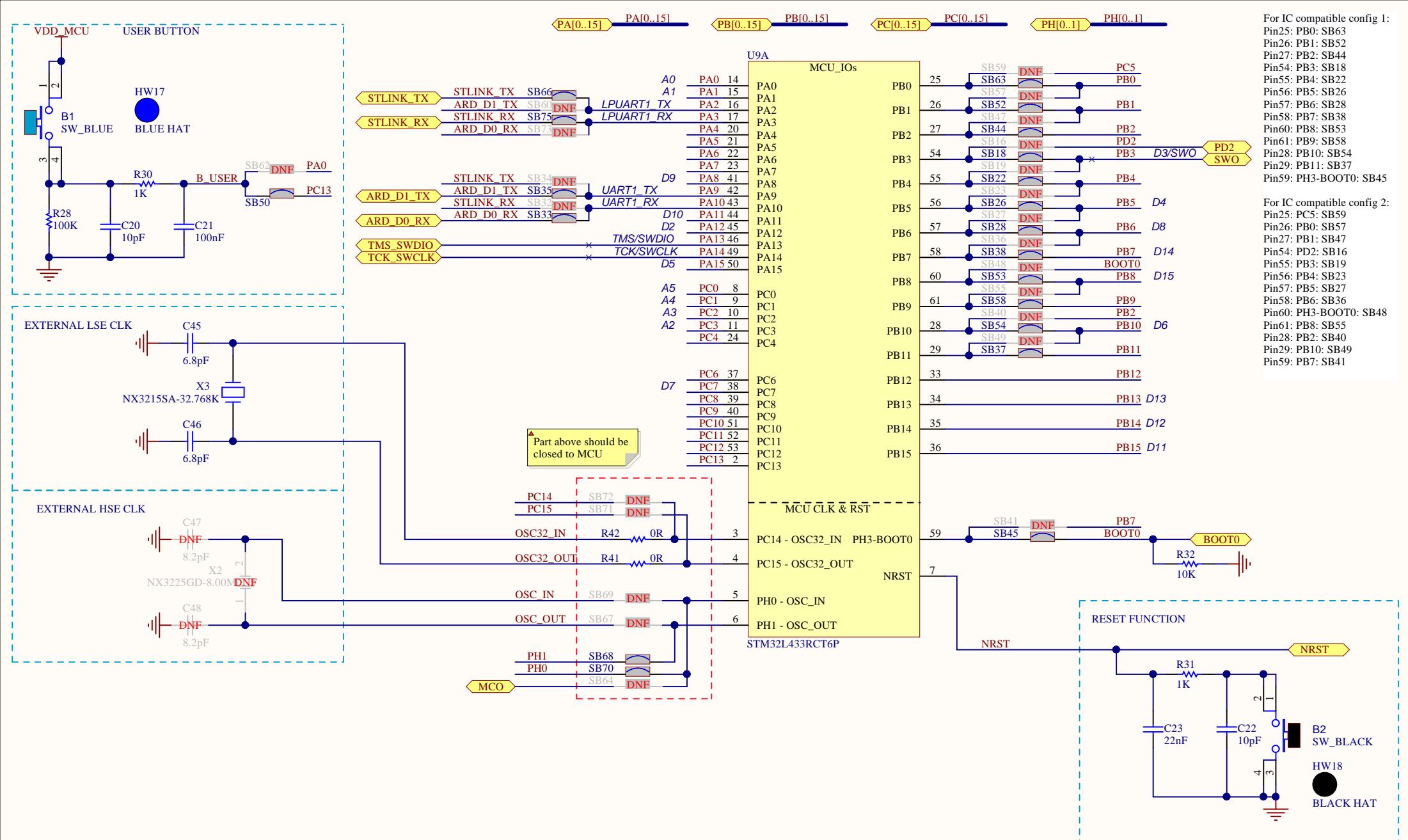
HW101
BOARD REF
mb1319B-01 xxxx

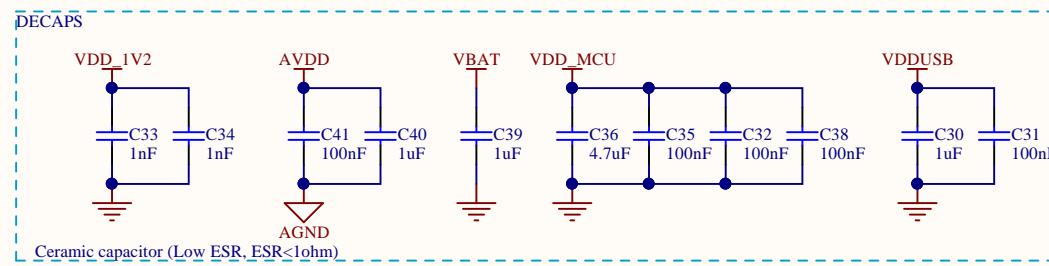
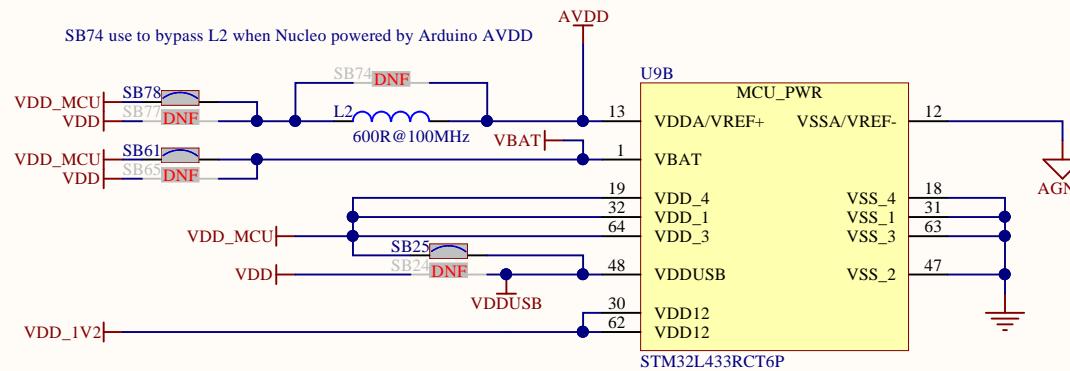
HW102
BOARD CPN
NUCLEO-L433RC-P

HW100
PCB
mb1319B

HW103
BLISTER
NUCLEO64_BLISTER

HW104 DNF LOGO NUCLEO	HW105 DNF LOGO ST	HW106 DNF LOGO CE	HW107 DNF LOGO ESD	HW108 DNF LOGO ROHS	HW109 DNF LOGO STM32
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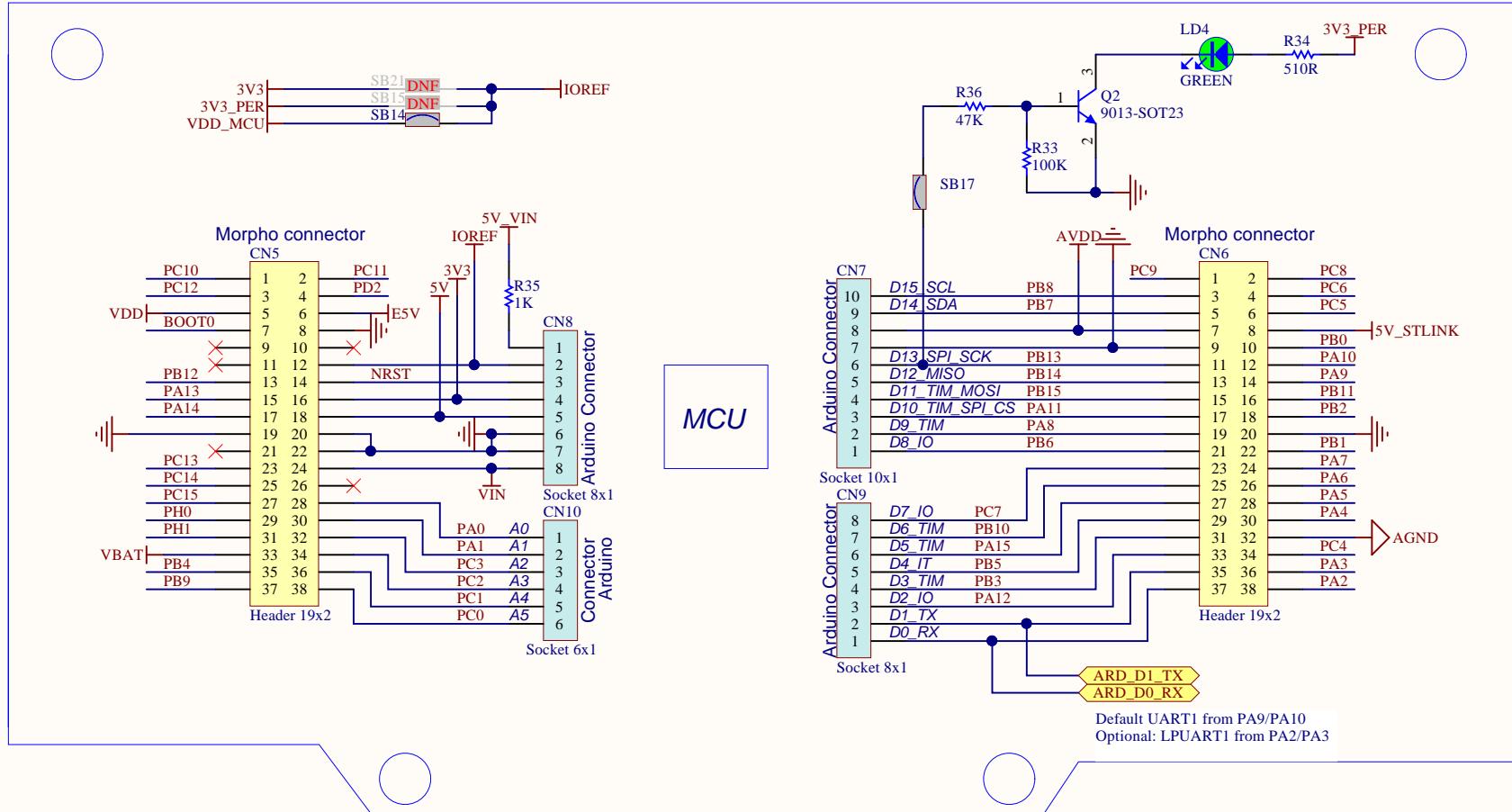




IOREF SELECTION	SB21	SB15	SB14
IOREF = 3V3	Closed	Open	Open
IOREF = 3V3_PER	Open	Closed	Open
IOREF = VDD_MCU Mainly for V8 compatibility	Open	Open	Closed

Default

- PA[0..15]
- PB[0..15]
- PC[0..15]
- PD2
- NRST
- BOOT0
- PH[0..1]



Title: Arduino extension connectors

Project: NUCLEO64 ext-SMPS

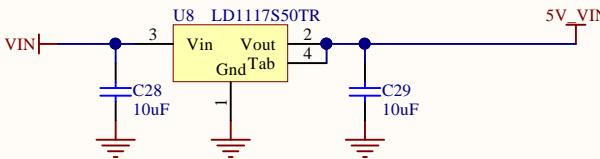
Variant: L433RC_P

Revision: B-01

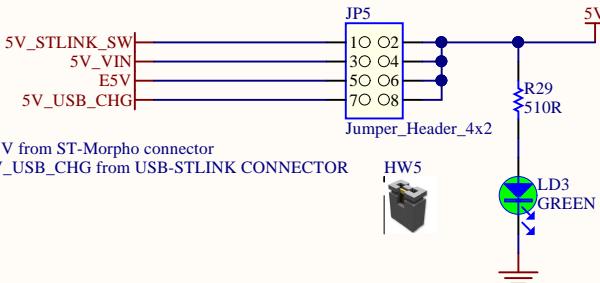
Reference: MB1319

Size: A4

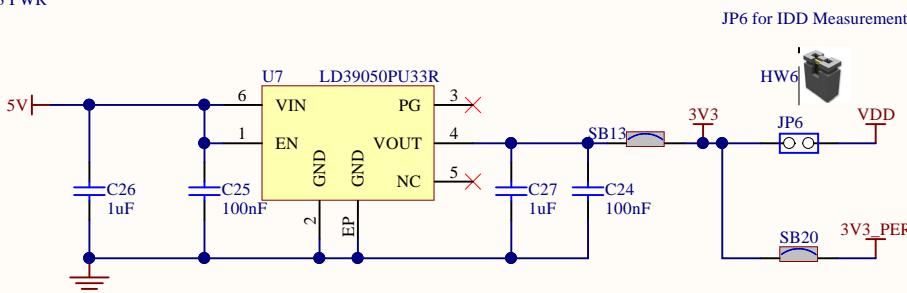
Date: 17-March-17

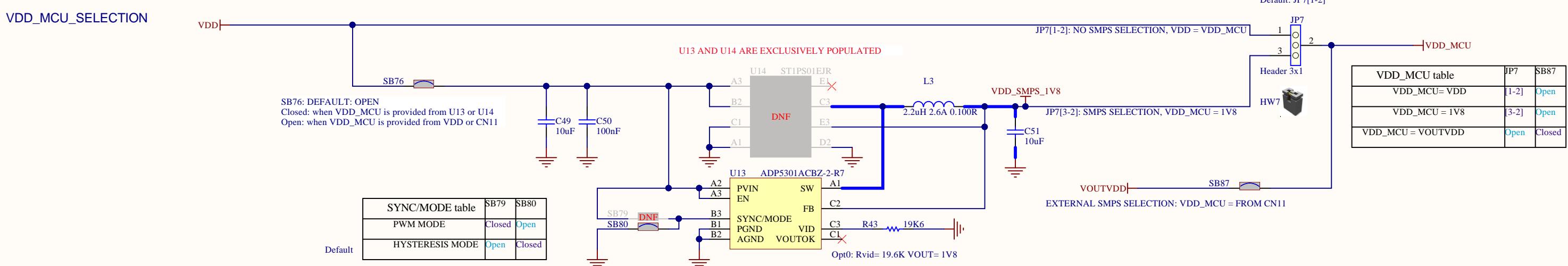


5V PWR SELECTION



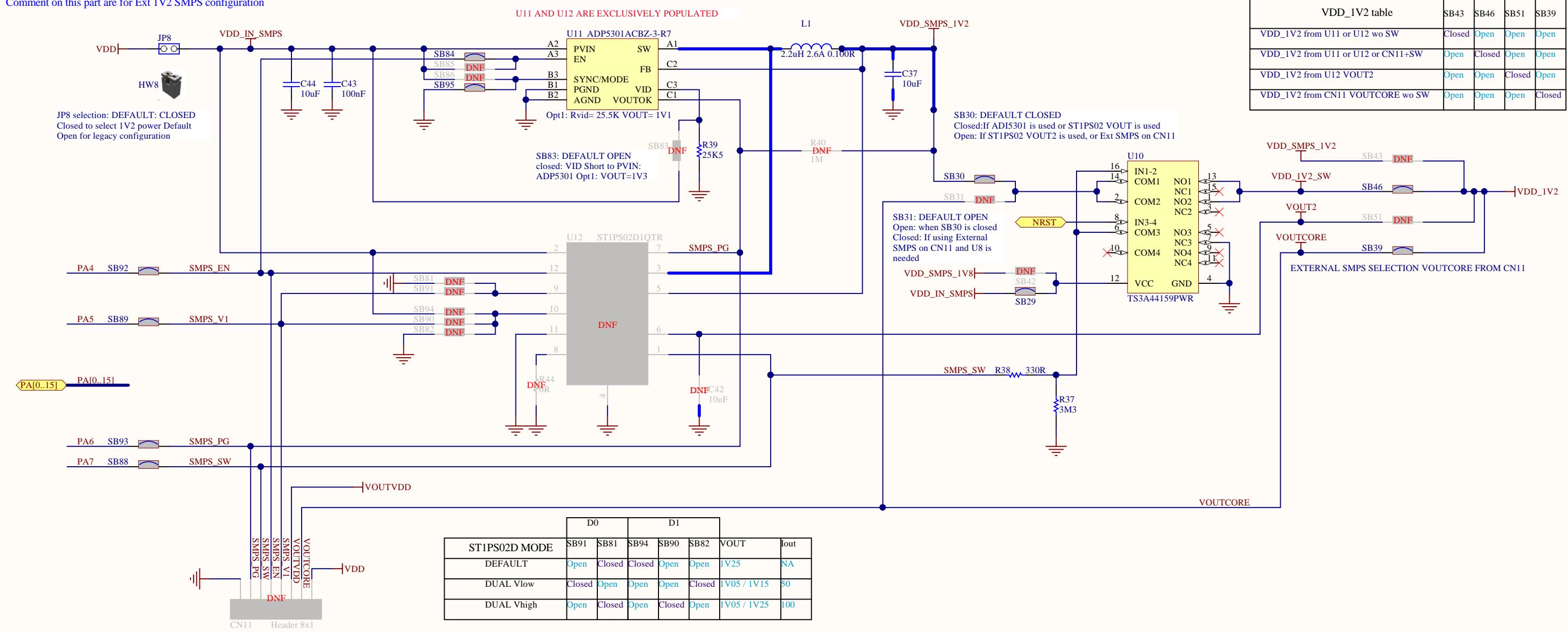
3V3 PWR

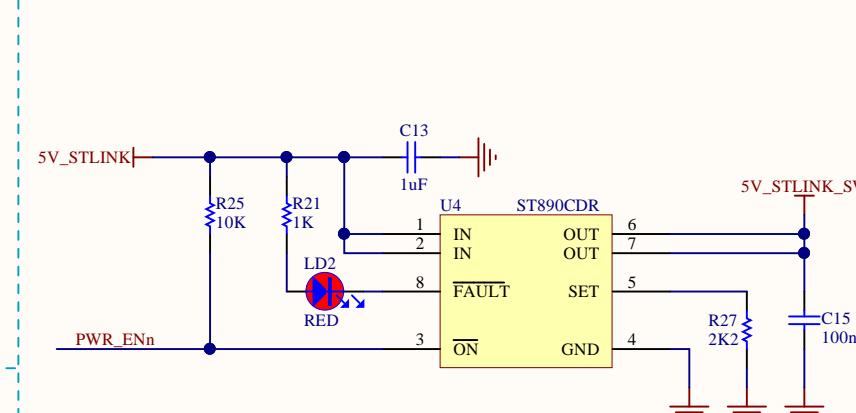
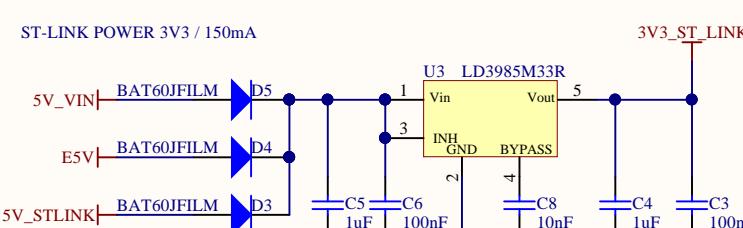
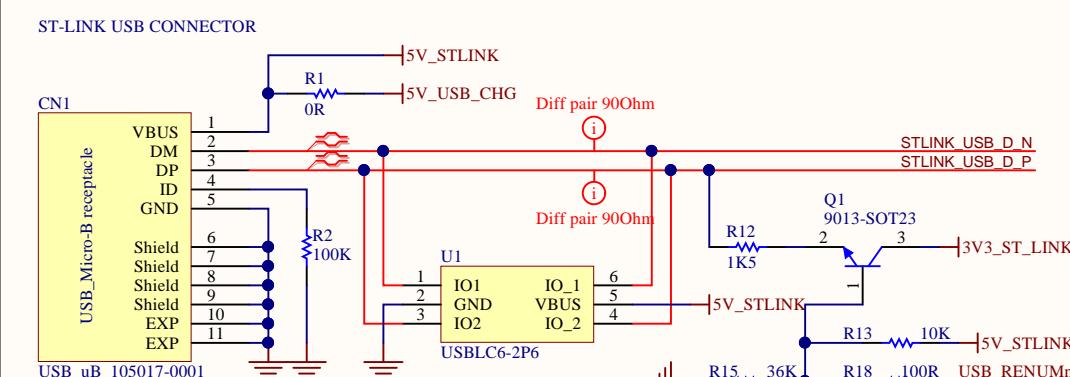
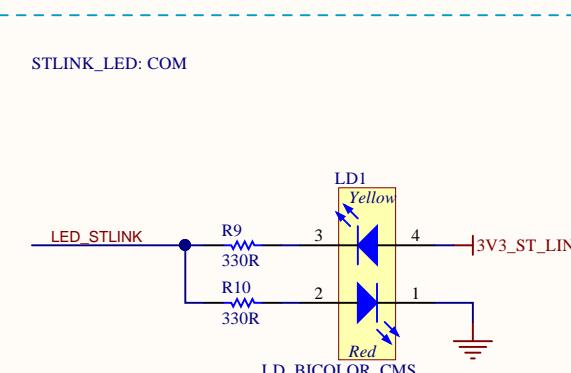
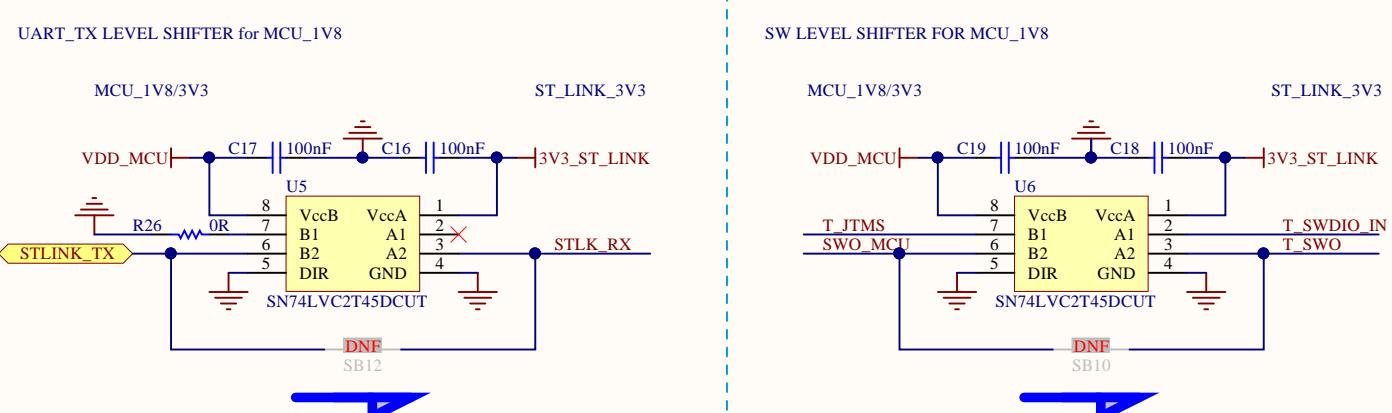
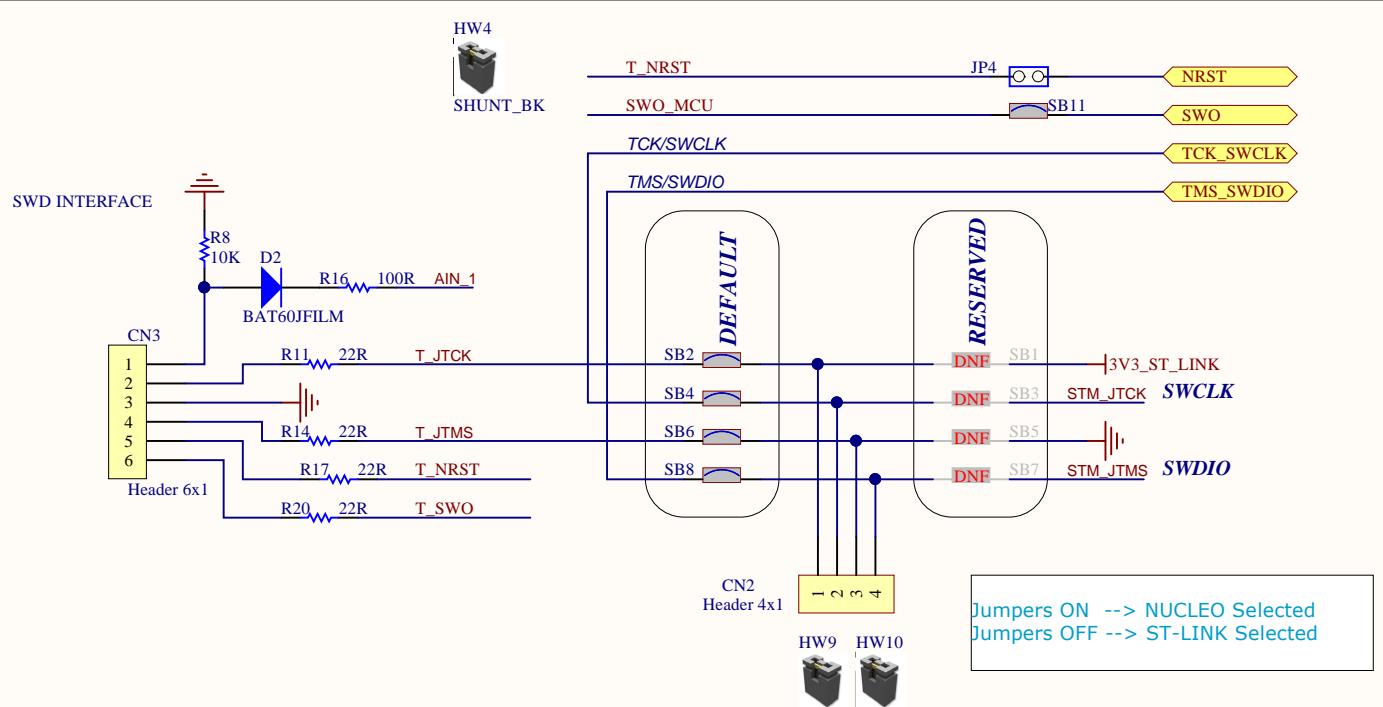
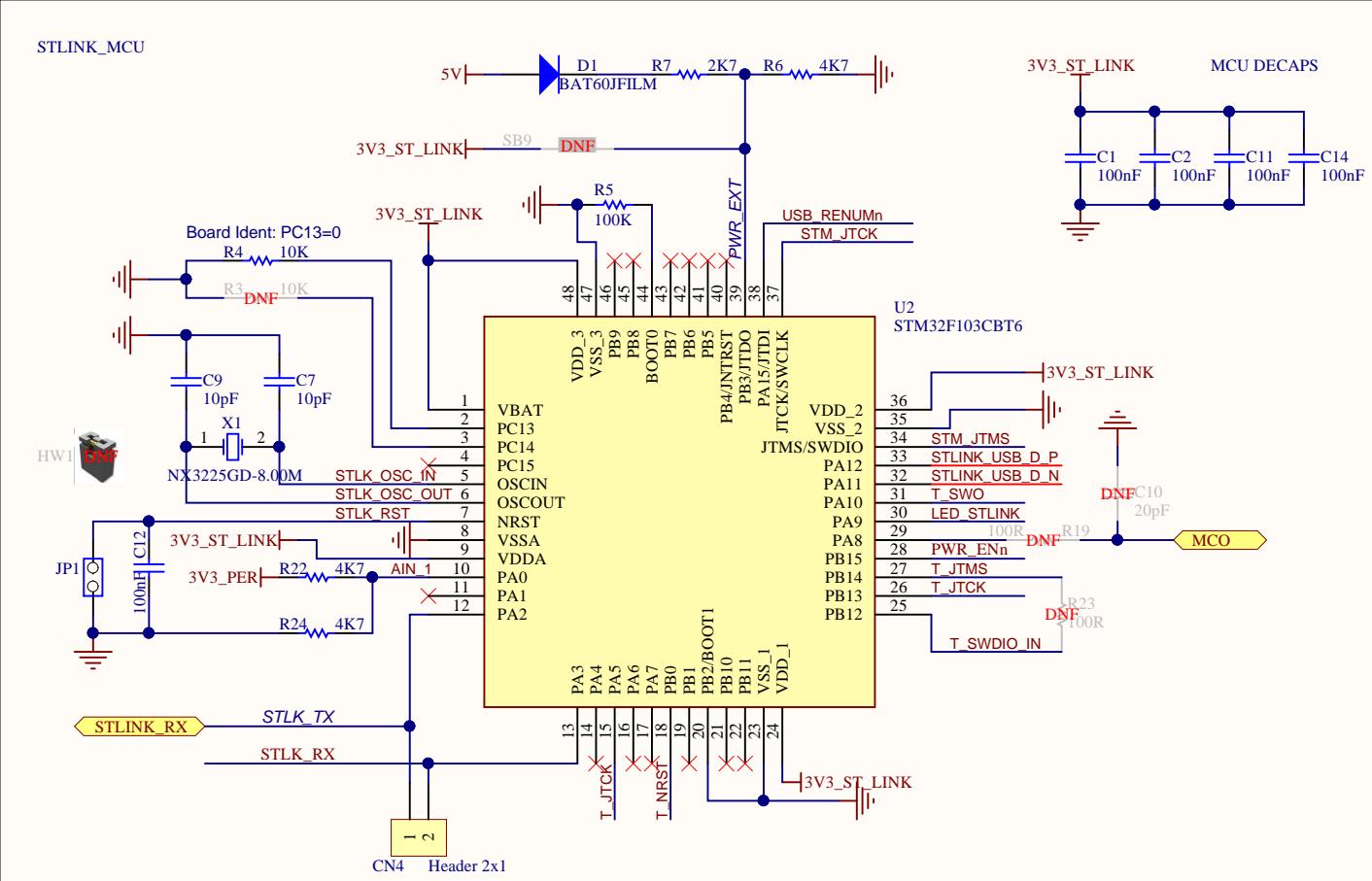




SMPS SELECTION, VDD_1V2

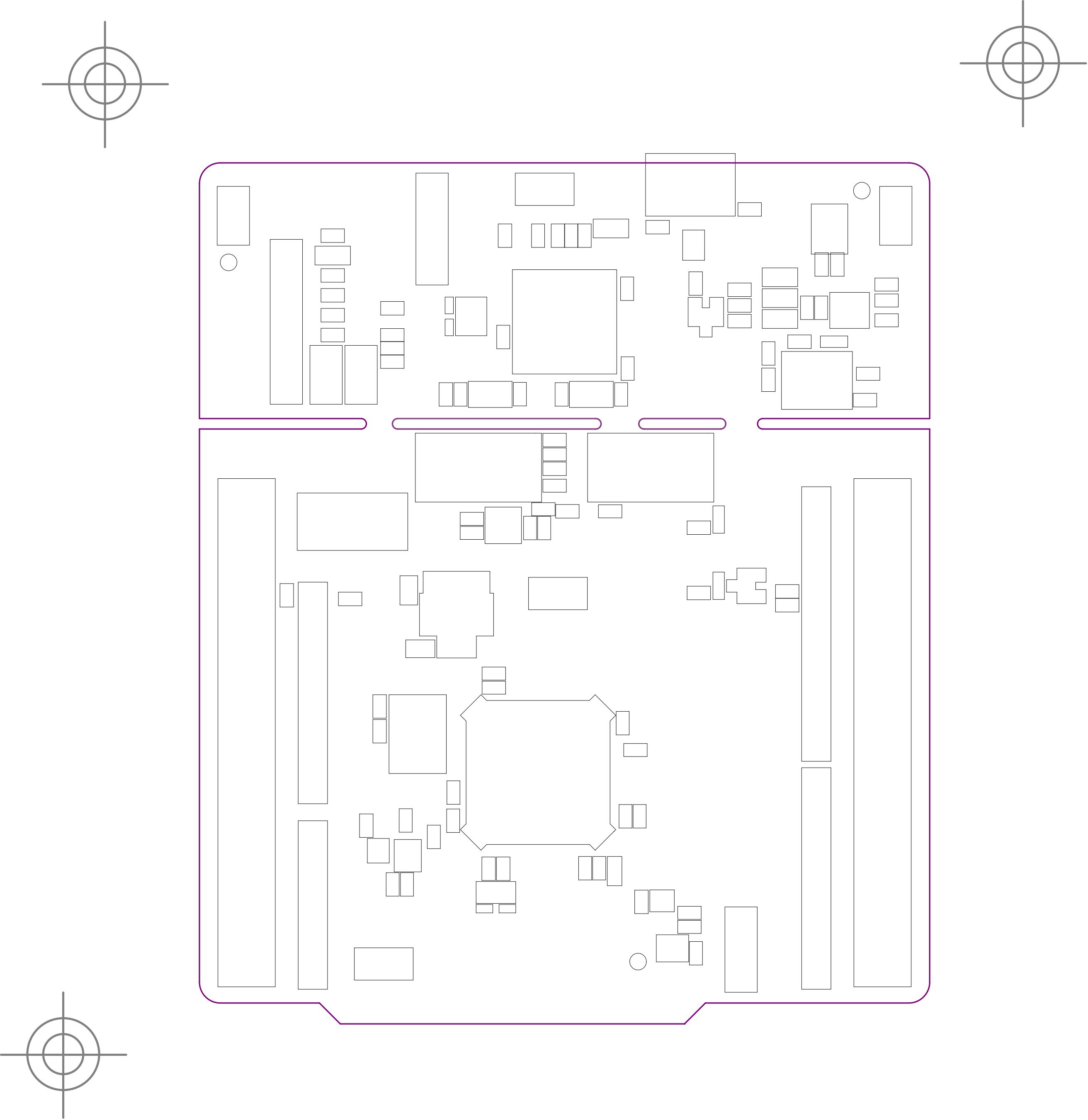
Comment on this part are for Ext 1V2 SMPS configuration

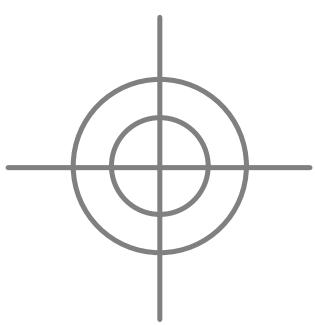
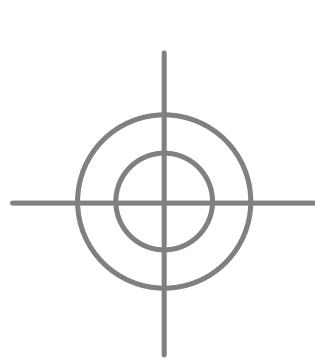
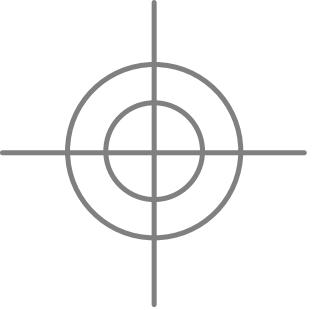
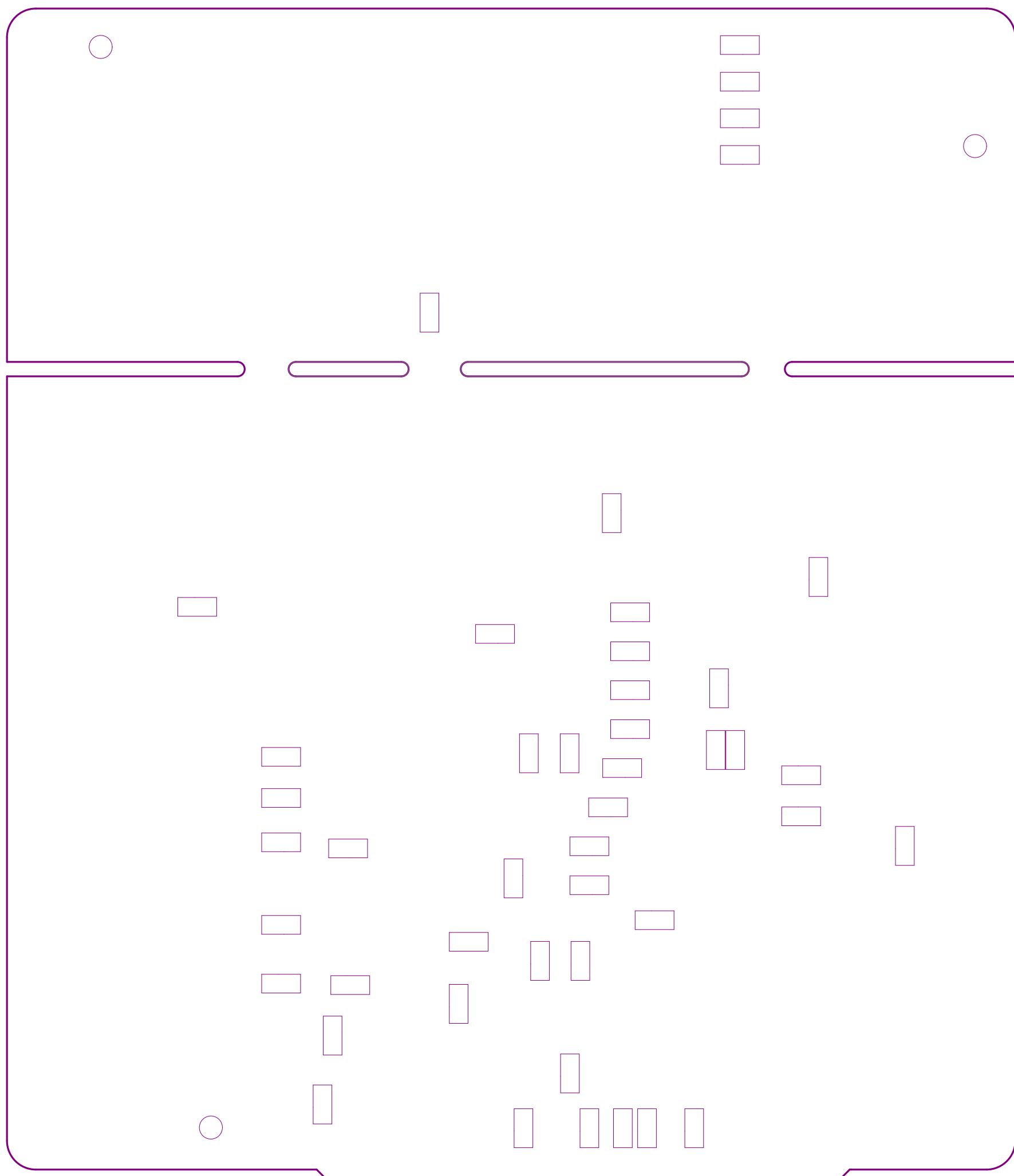




All the sheet are the CUTTABLE part on PCB

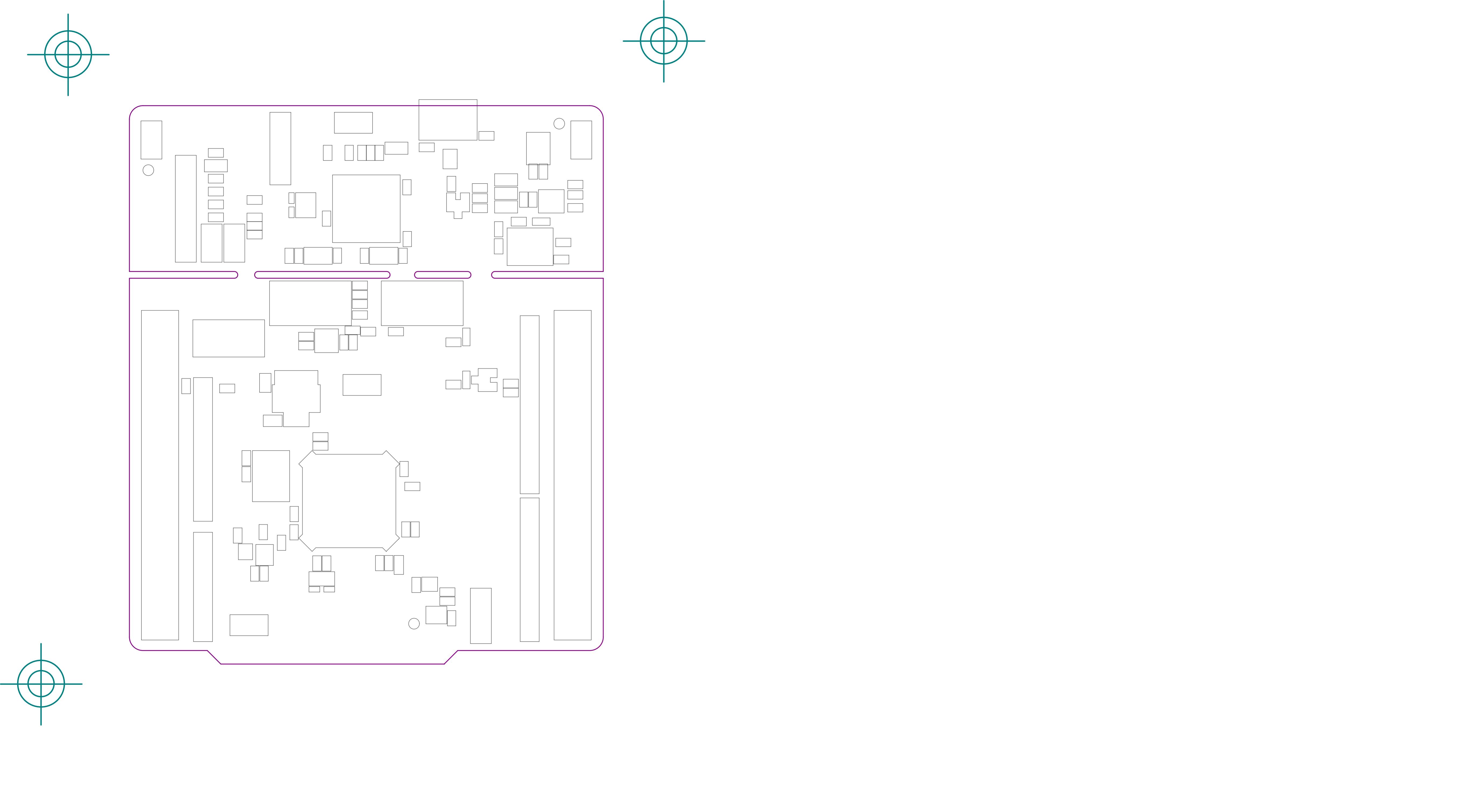
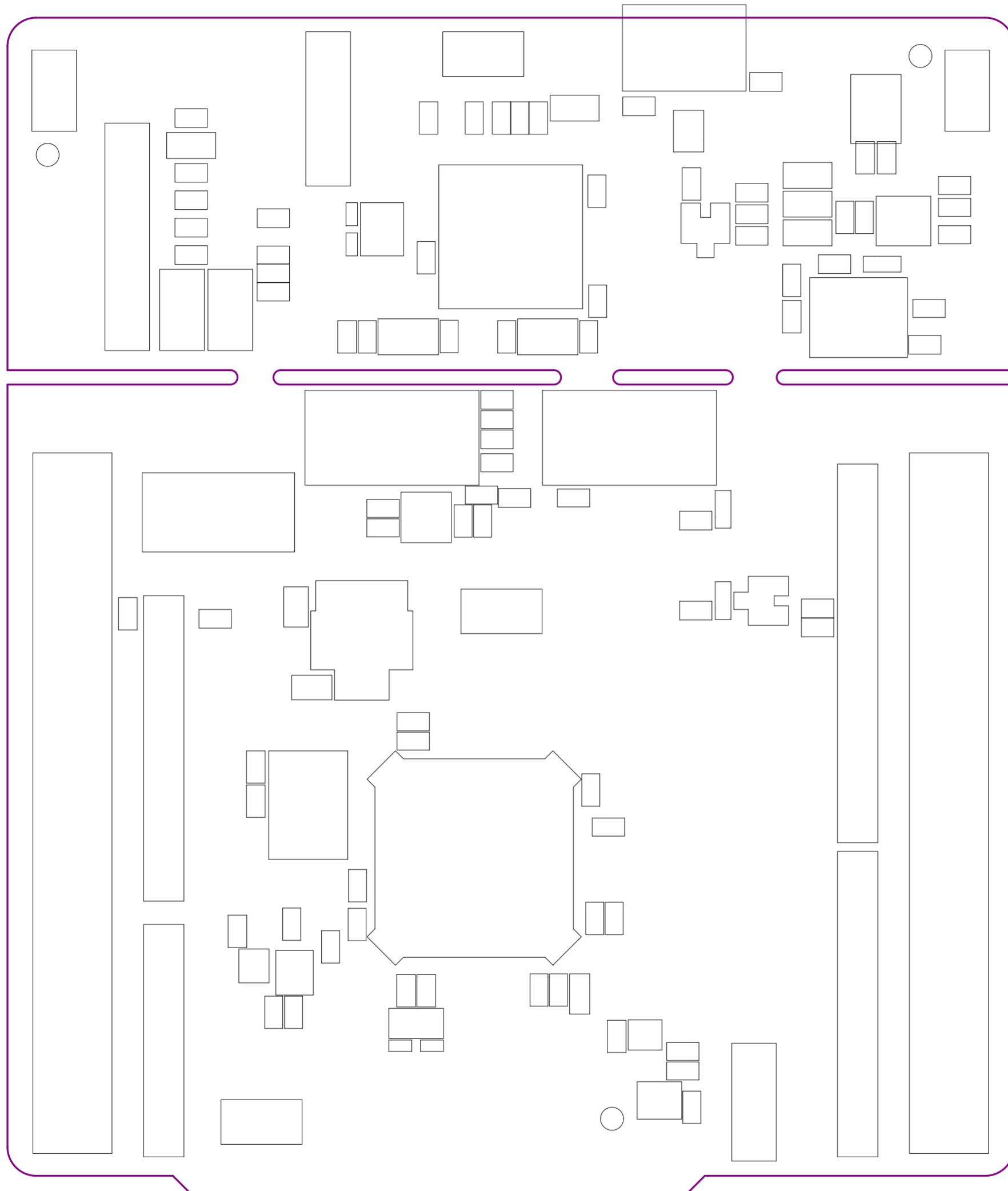
Title: ST-LINK/V2-1	Project: NUCLEO64 ext-SMP	Variant: L433RC_P	 life.augmented
Revision: B-01	Reference: MB1319		
Size: A4	Date: 17-March-17	Sheet: 8	of 8

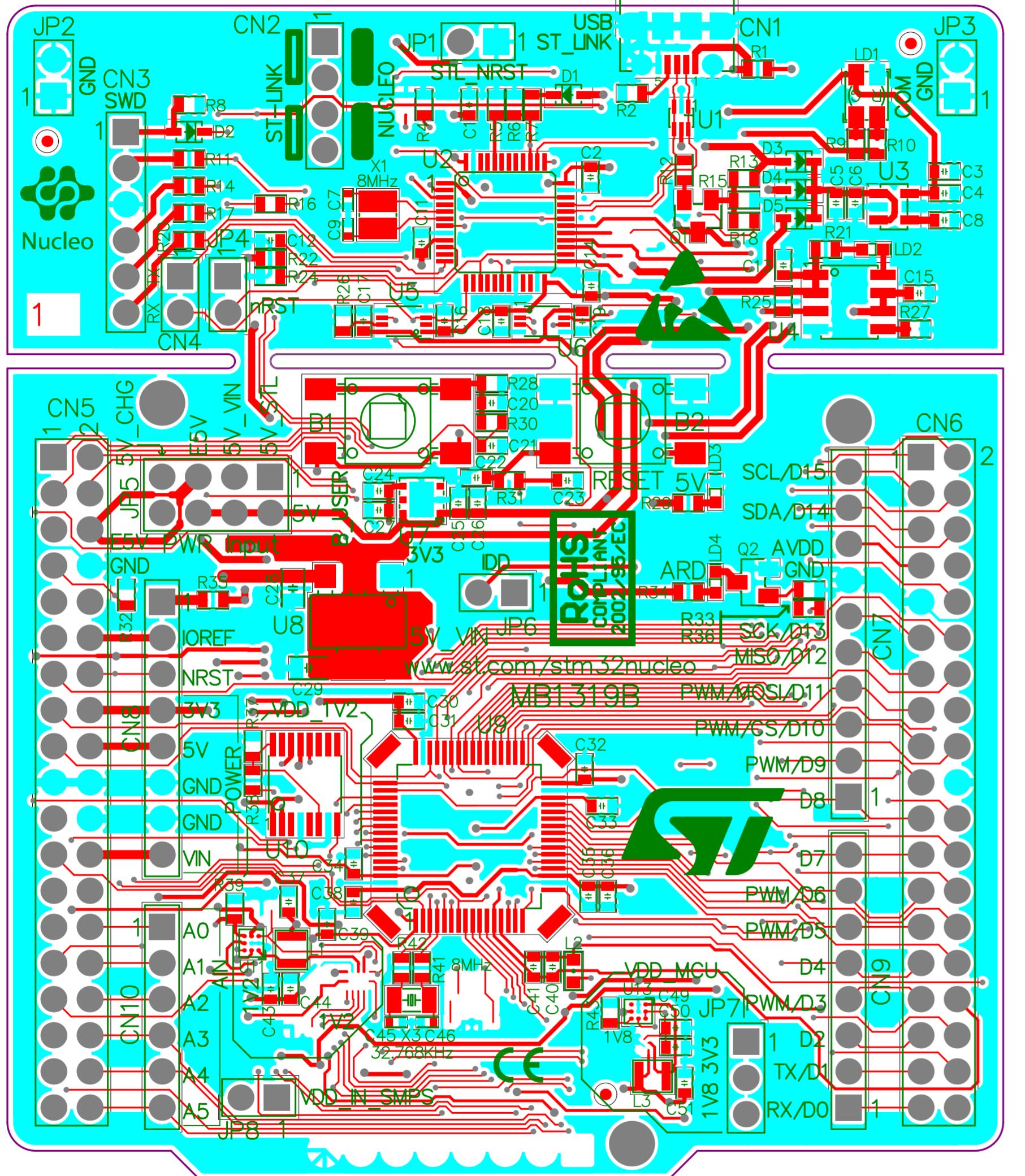


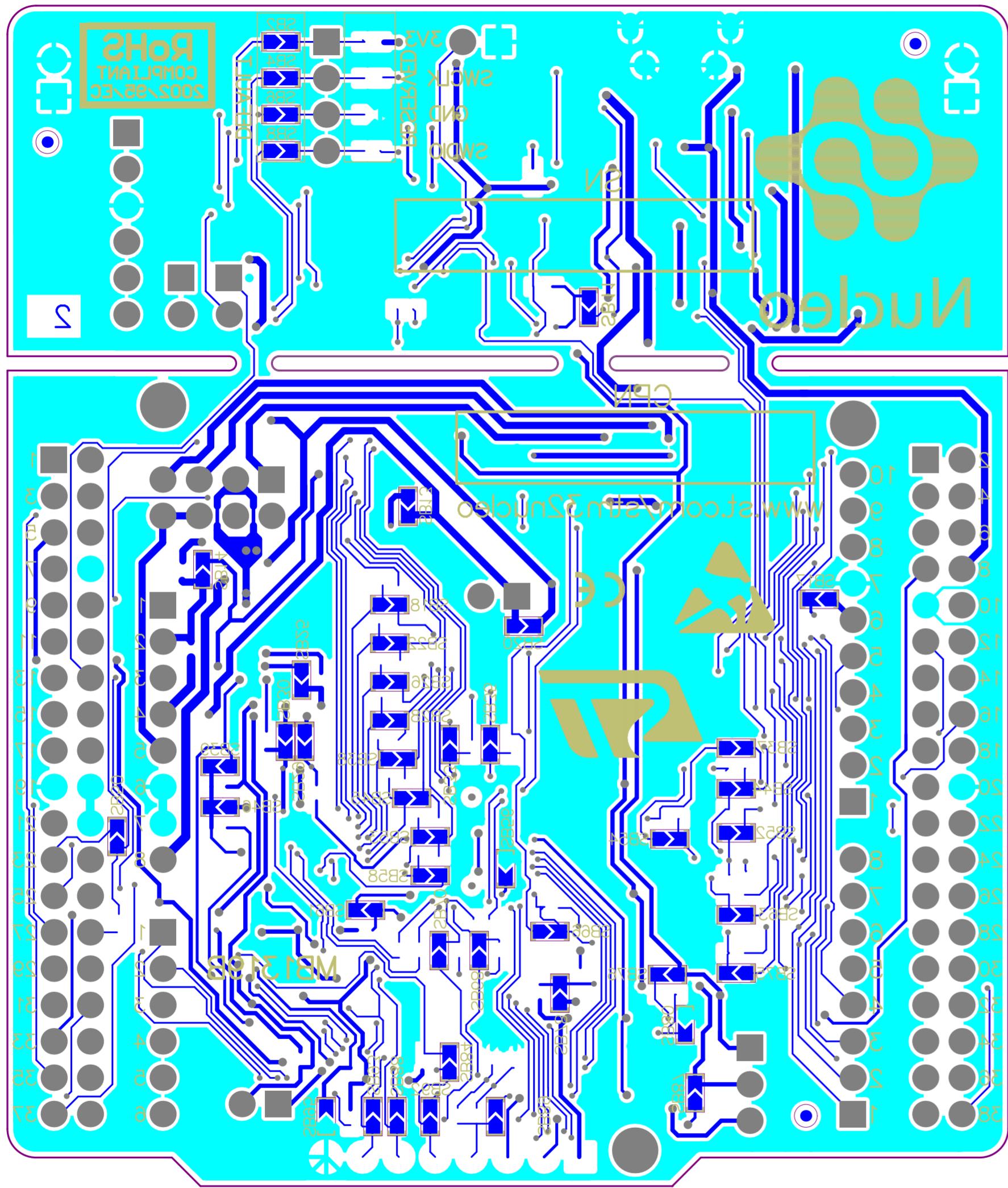


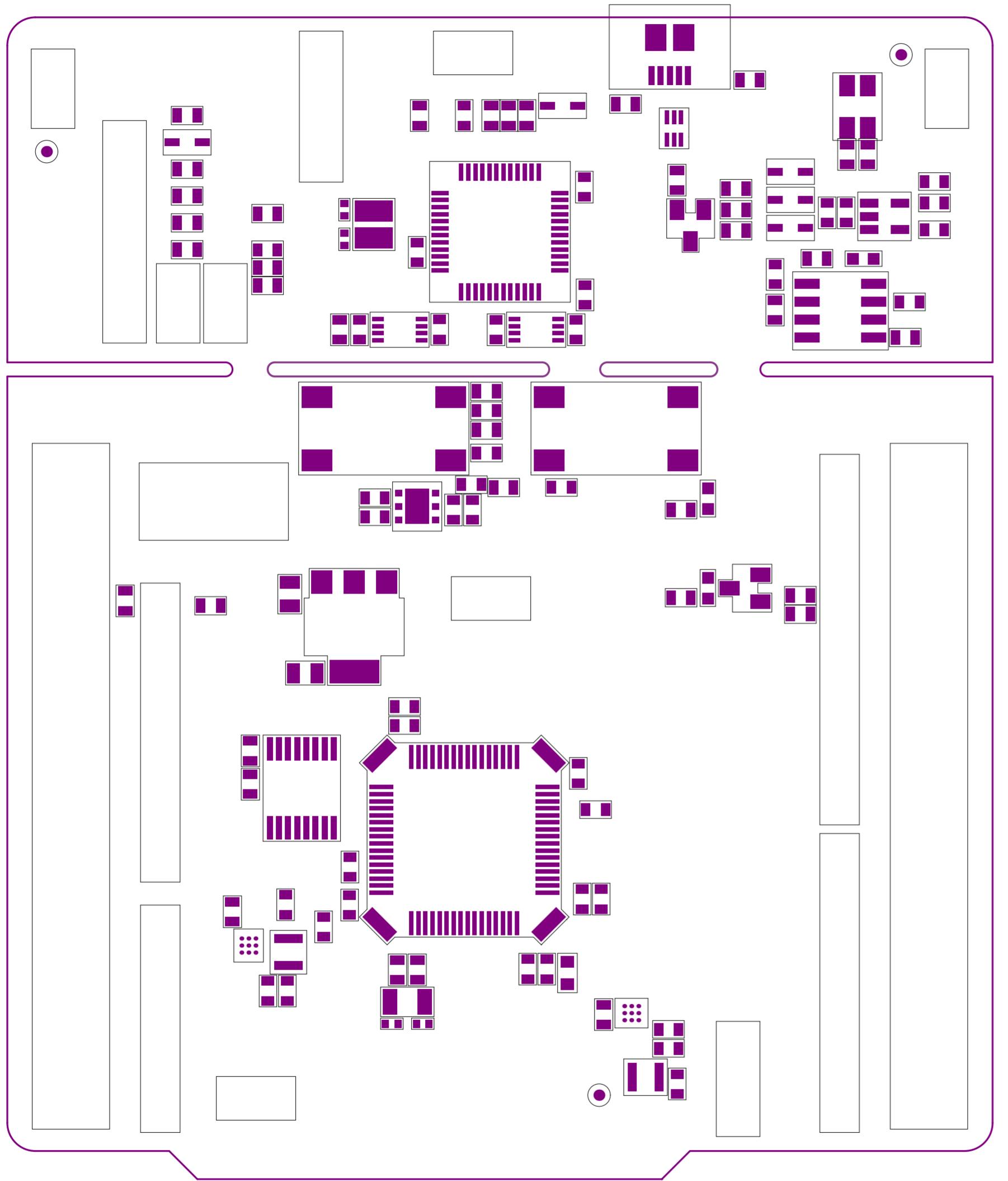
Board Stack Report

Stack Up				Layer Stack			
Layer	Board Layer Stack	Board Layer Stack	Board Layer Stack	Name	Material	Thickness	Constant
1				Top Paste			
2				Top Overlay			
3				Top Solder	Solder Resist	0,010mm	3,5
4	Orange	Orange	Orange	Top Layer	Copper	0,036mm	
5	Yellow	Yellow	Yellow	Dielectric1	FR-4	1,500mm	4,8
6	Orange	Orange	Orange	Bottom Layer	Copper	0,036mm	
7				Bottom Solder	Solder Resist	0,010mm	3,5
8				Bottom Overlay			
9				Bottom Paste			
	Height : 1,591mm	Height : 1,591mm	Height : 1,591mm				

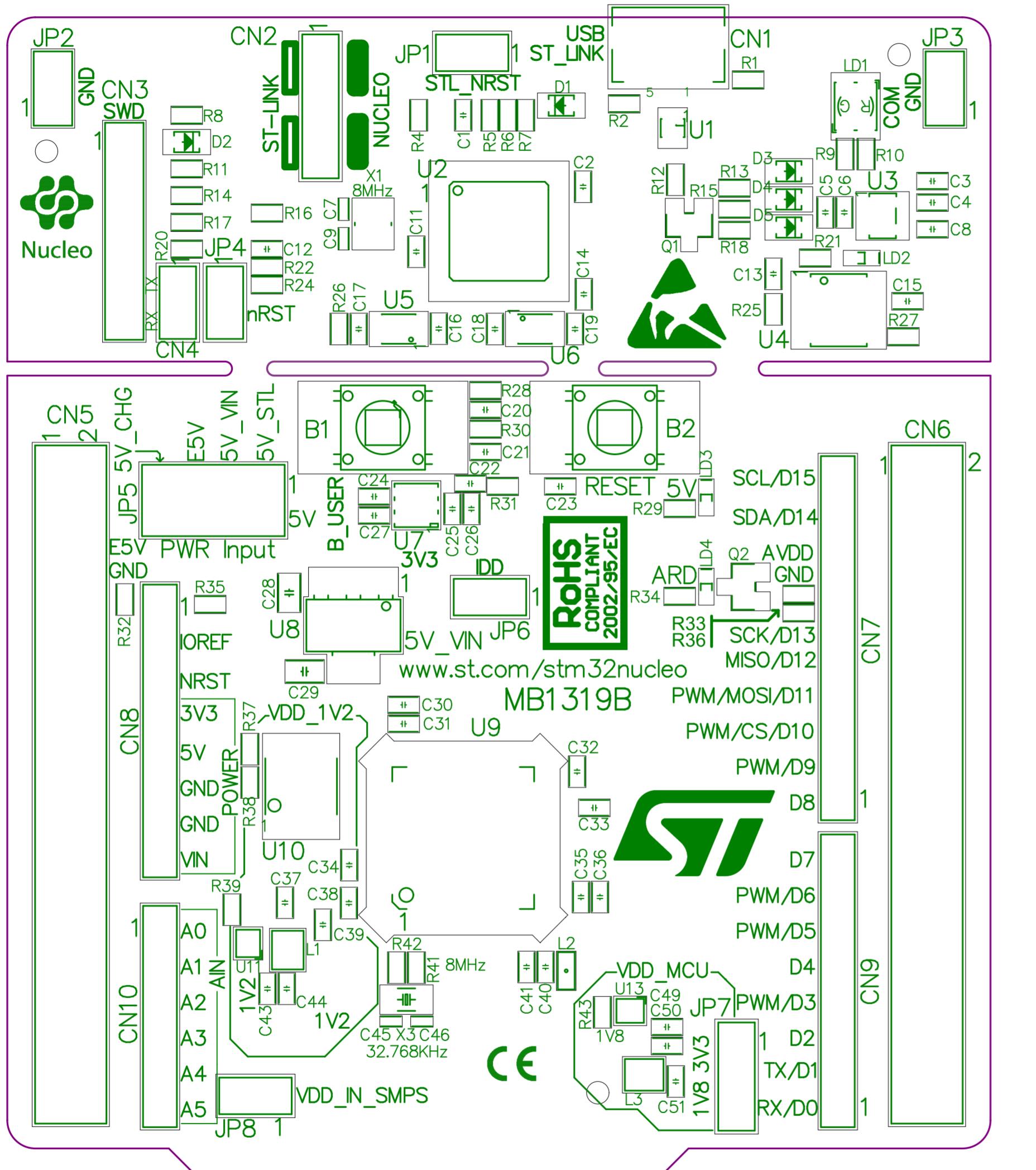




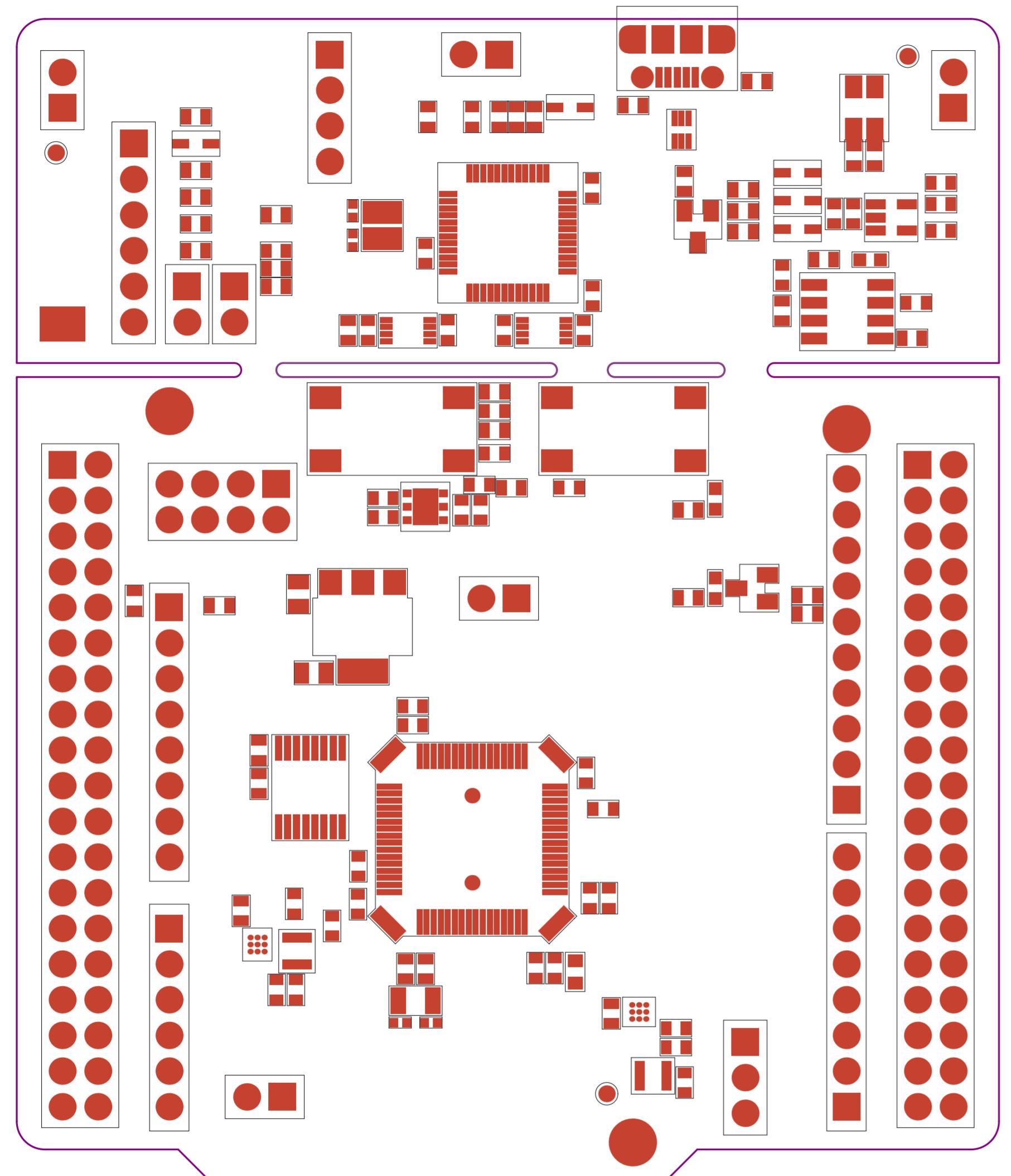




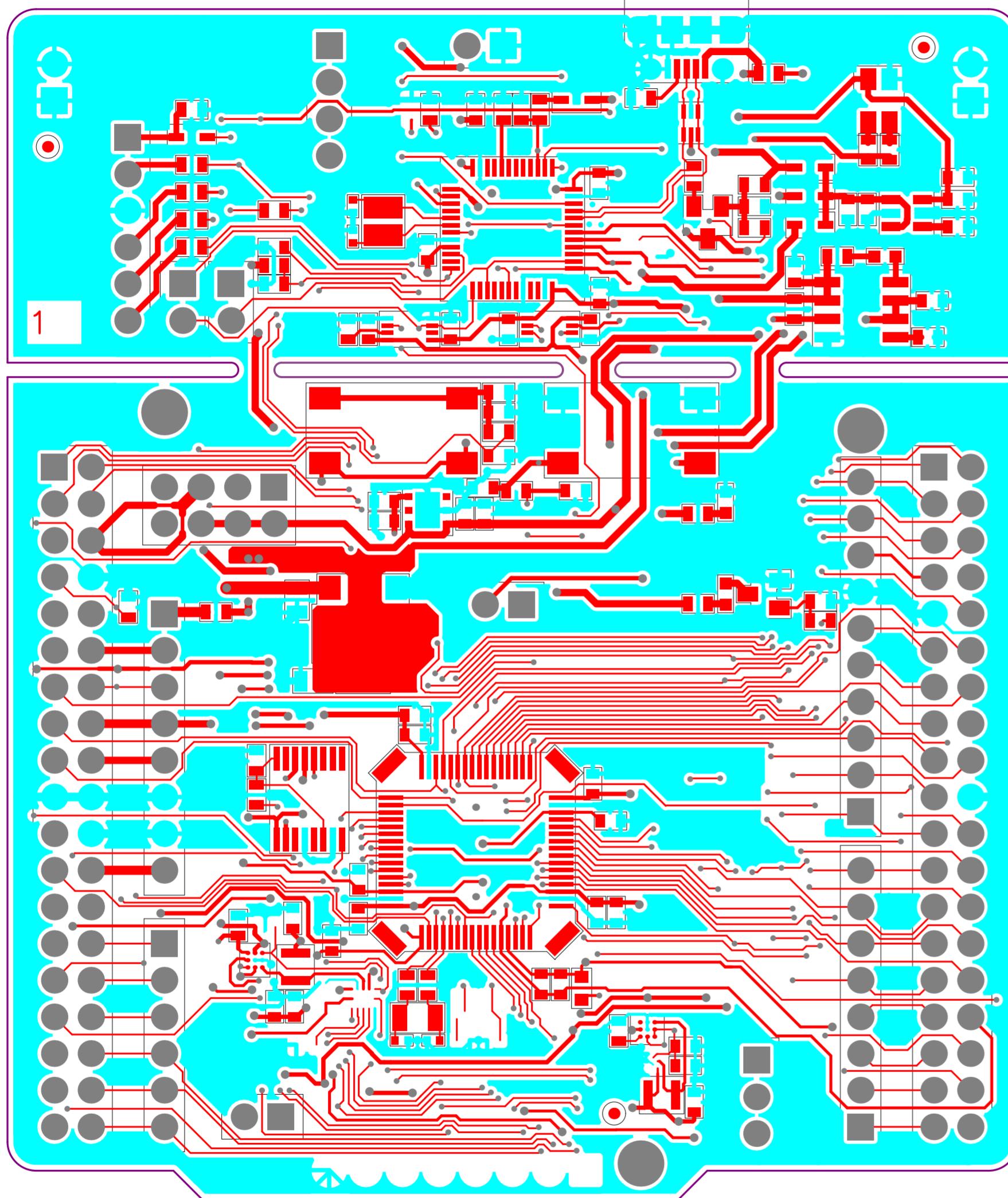
SOLDER PASTE, (PRIMARY)



SILKSCREEN, (PRIMARY)

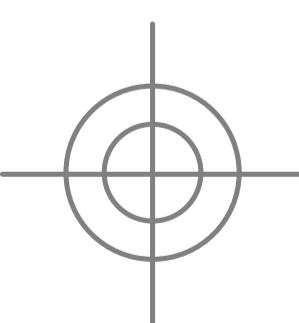
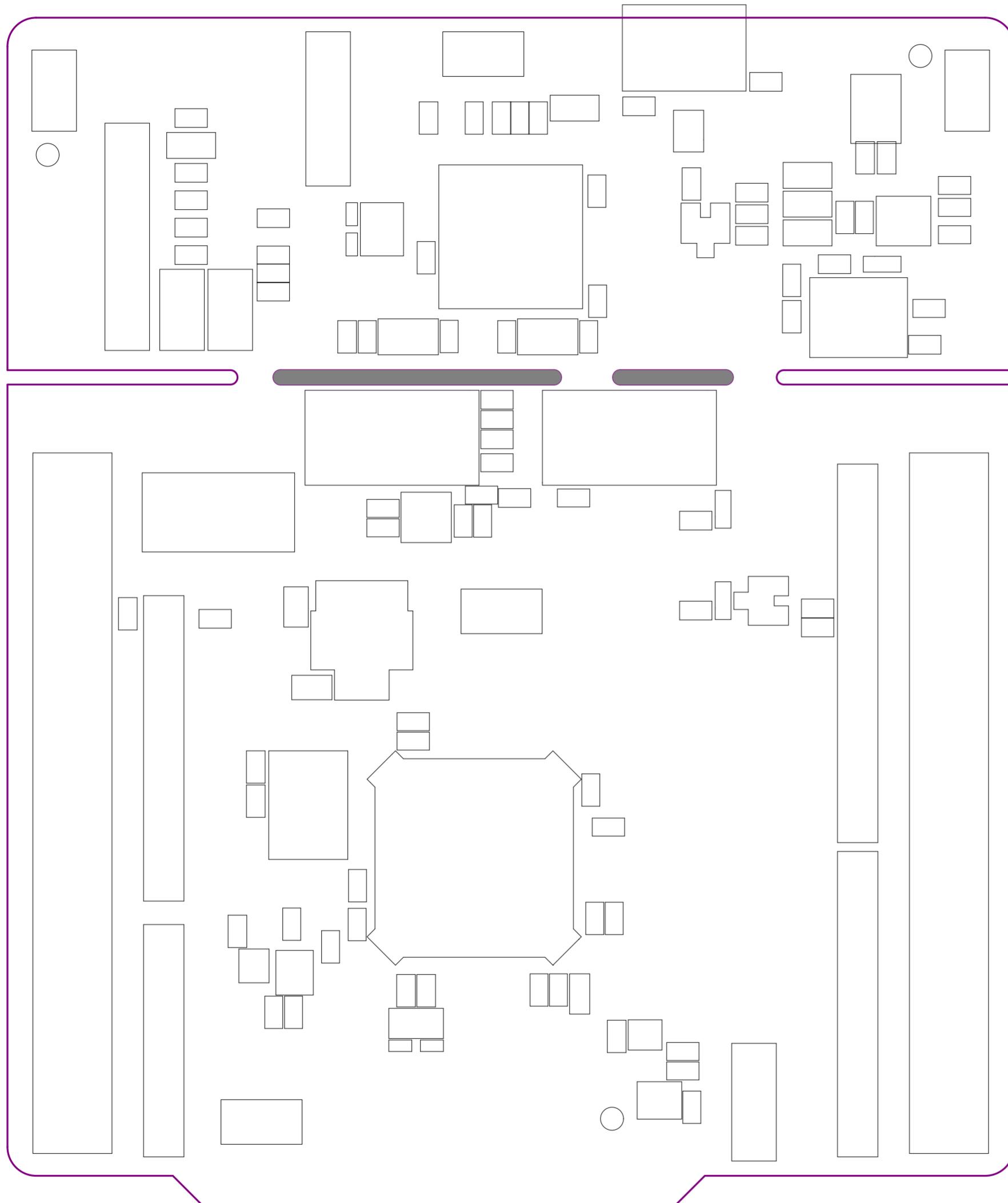


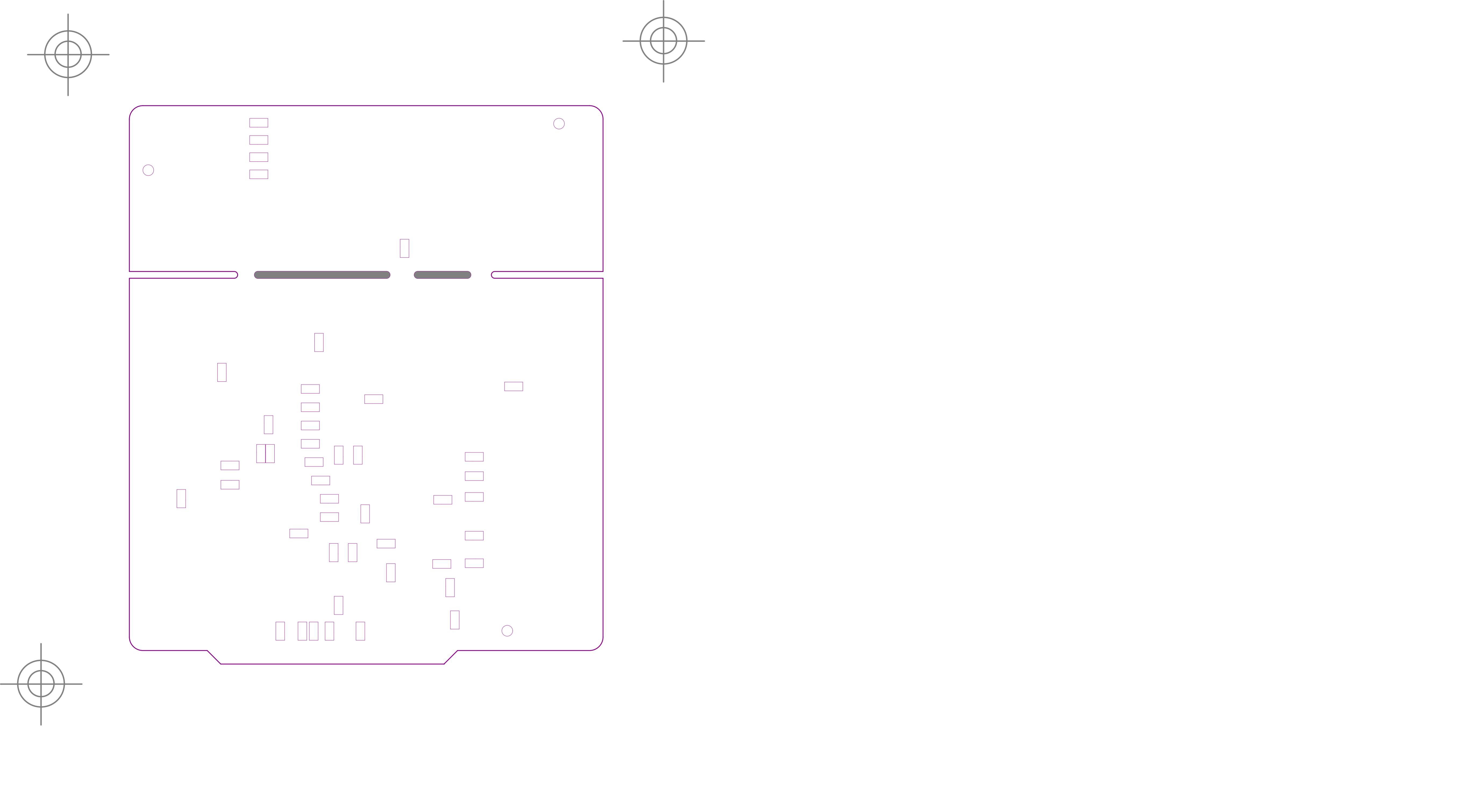
SOLDER MASK, (PRIMARY)

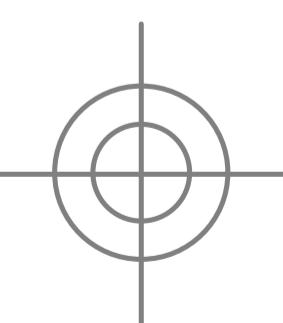
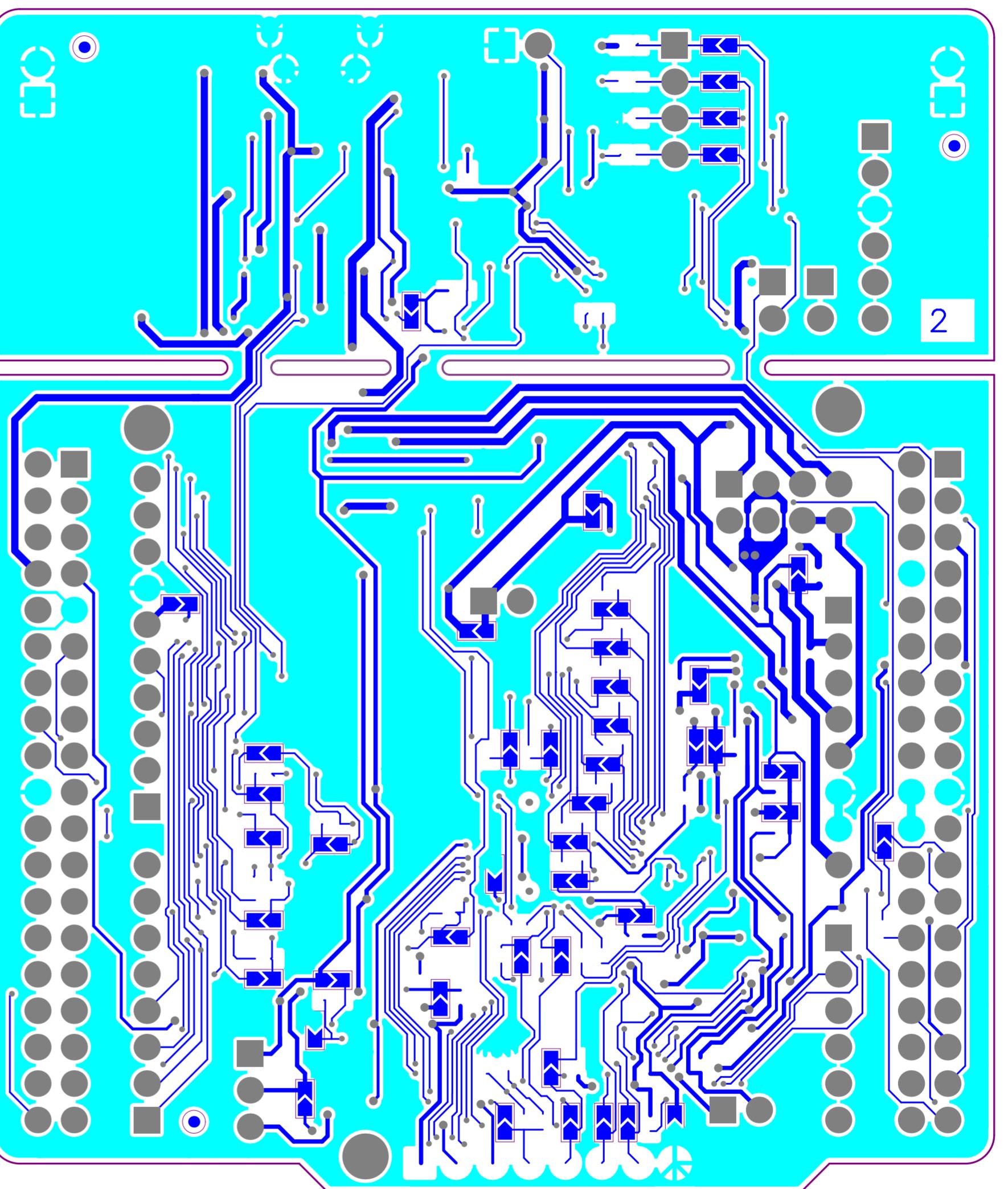
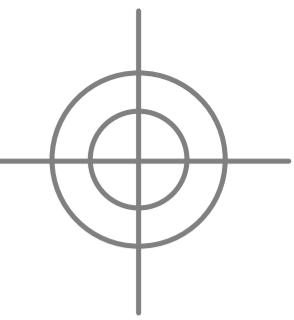


1

PRIMARY SIDE



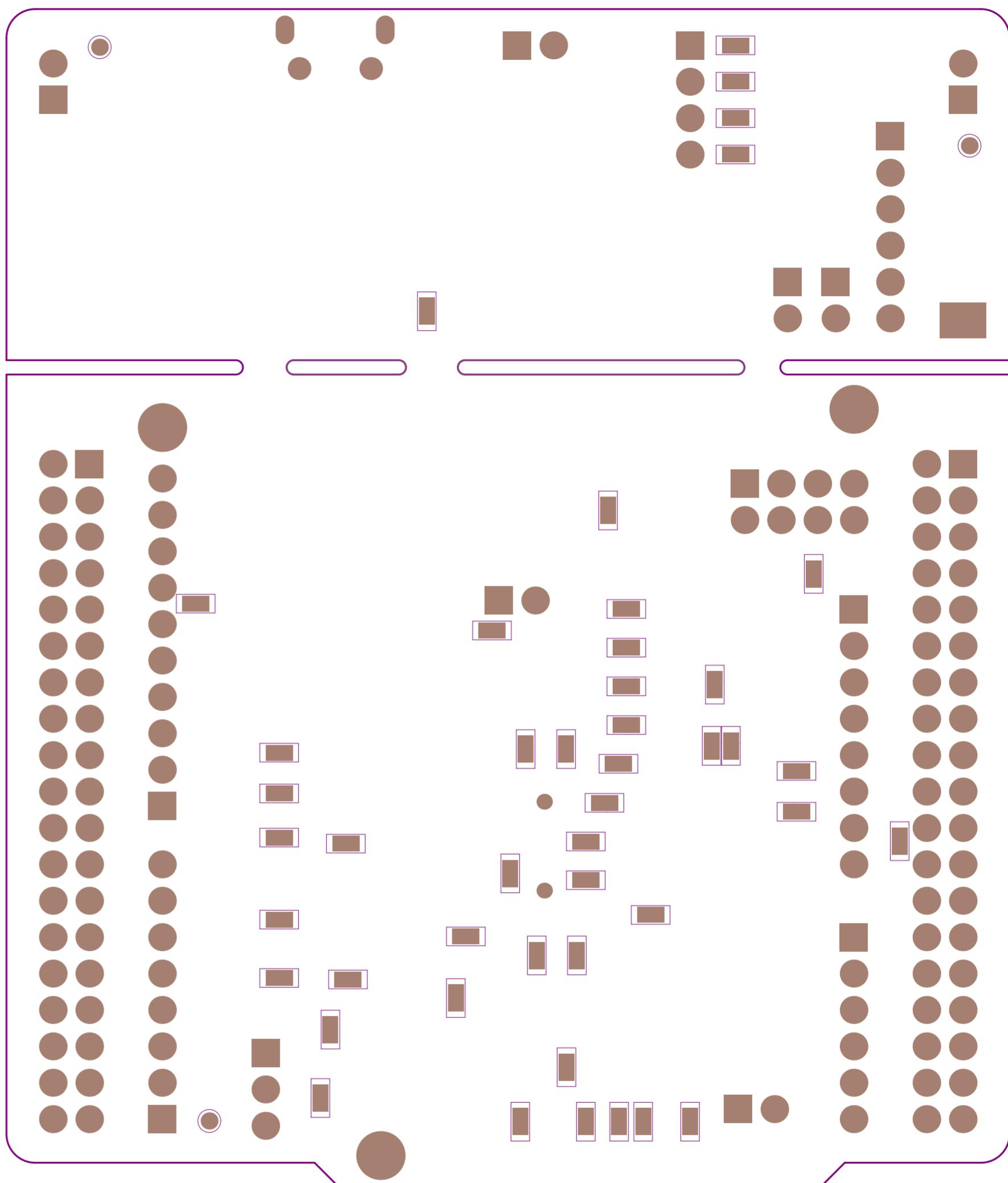


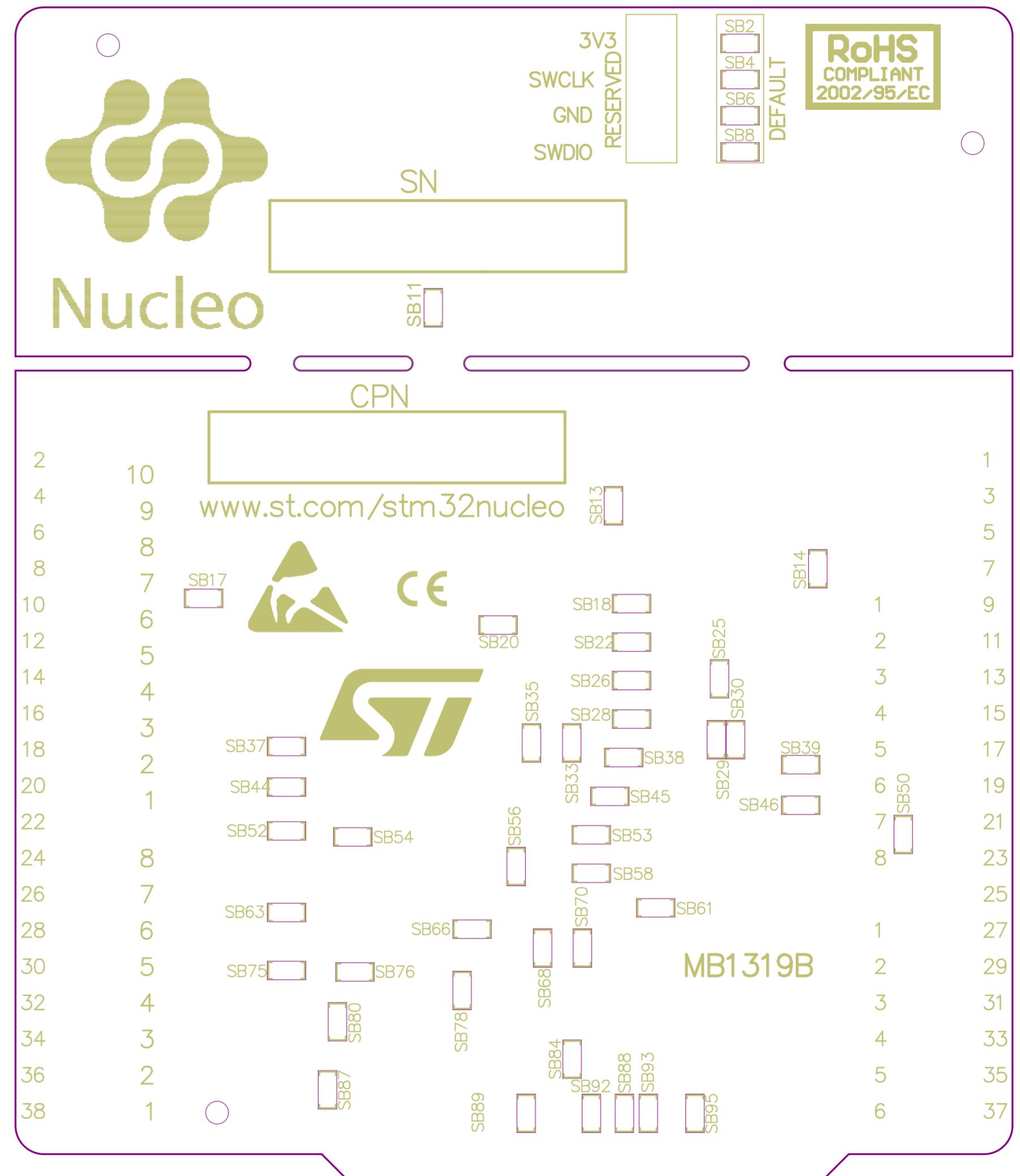


2

SECONDARY SIDE

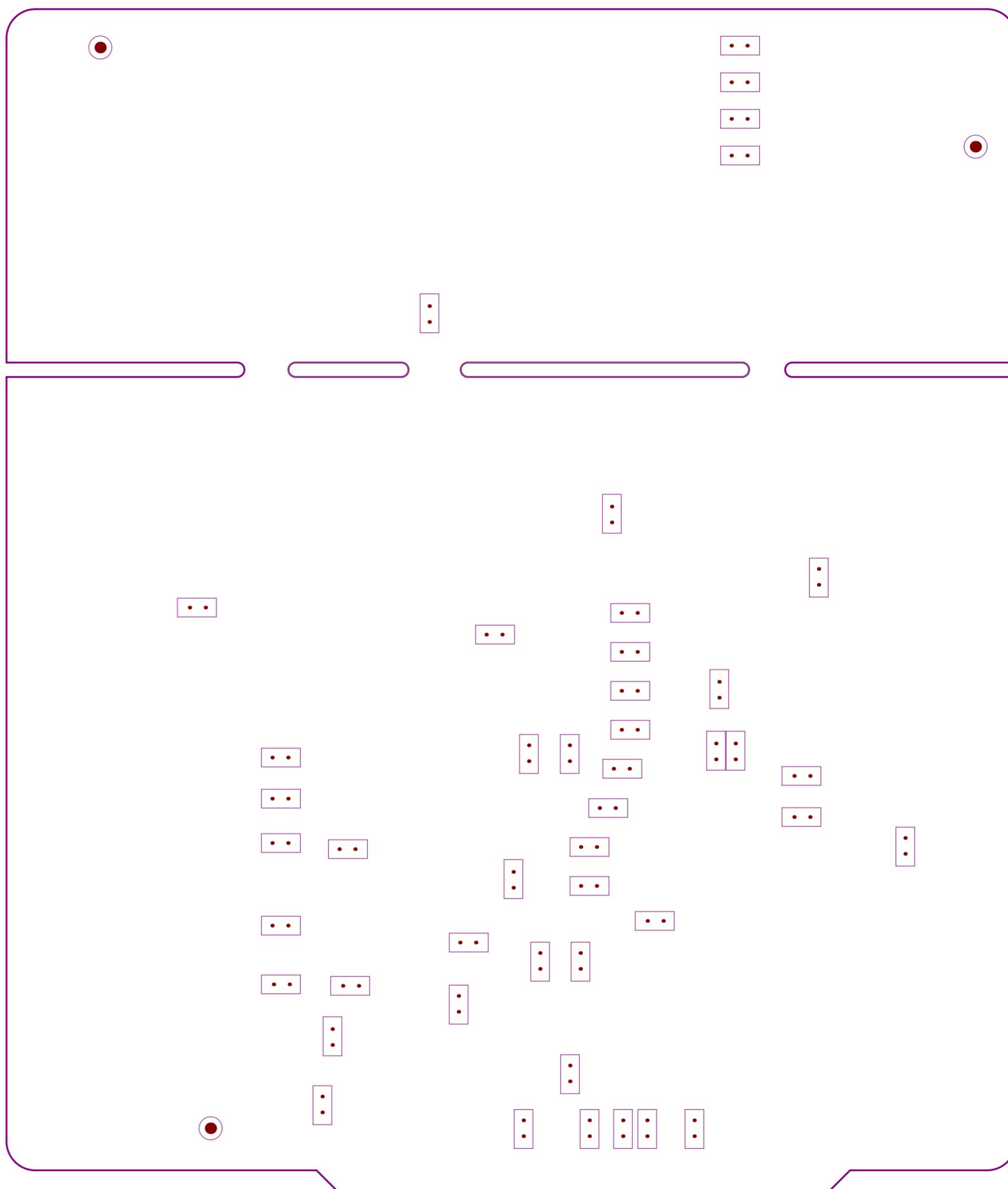
SOLDIER MASK (SECONDARY)

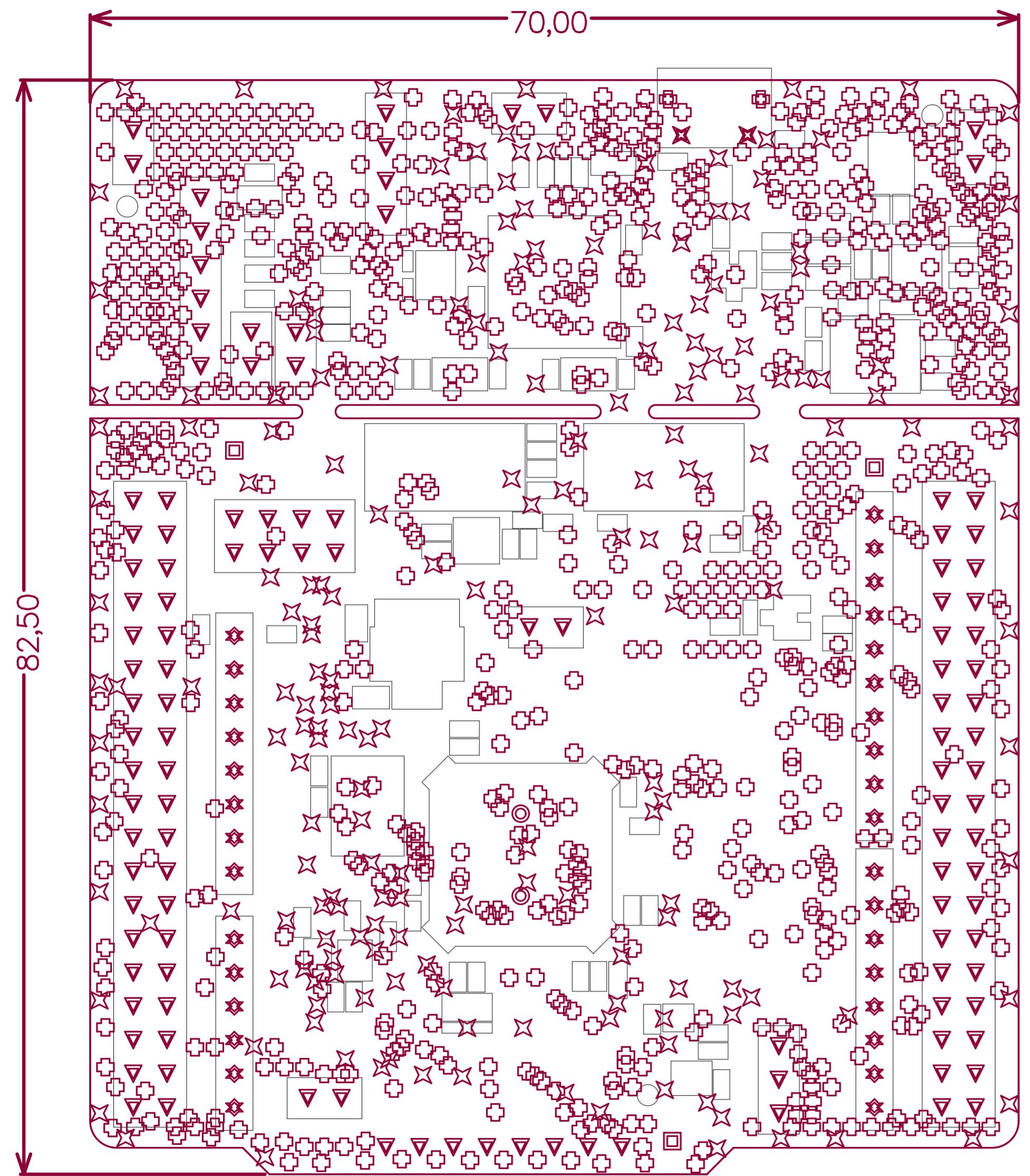




SILKSCREEN, (SECONDARY)

SOLDIER PASTÉ (SECONDARY)





Symbol	Count	Hole Size	Plated	Hole Type	Hole Length	Routed Path Length
+	2	0,600mm (23,62mil)	PTH	Slot	1,300mm (51,18mil)	0,700mm (27,56mil)
×	2	0,900mm (35,43mil)	PTH	Round	—	—
○	2	1,000mm (39,37mil)	NPTH	Round	—	—
□	3	3,200mm (125,98mil)	NPTH	Round	—	—
◊	32	1,100mm (43,31mil)	PTH	Round	—	—
▽	119	1,000mm (39,37mil)	PTH	Round	—	—
✖	198	0,300mm (11,81mil)	PTH	Round	—	—
+	808	0,200mm (7,87mil)	PTH	Round	—	—
1166 Total						

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

Layer Stack Up Detail for: MB1319B.PcbDoc

Layer Name	Gerber Document	Copper Thickness	Dielectric Height	Dielectric Material	Dielectric Constant	Dielectric Type
Top Solder Mask	(.GTS)		0.4mil	Solder Resist	3.50	
Top Layer	(.GTL)	1.4mil				
Bottom Layer	(.GBL)	1.4mil	59.055mil	FR-4	4.80	Core
Bottom Solder Mask	(.GBS)		0.4mil	Solder Resist	3.50	

Notes:

Silkscreen color : Blue ink PANTONE 2955

Solder resist color : White

Plating type : lead Gold

