LAB-1 (Writing Final Assembly code)

Instruction	— Operands	- Description
MOV	- REG, memory, REG, Immediate	- The mov instruction cannot set the value of the cs and Ip registers. - copy value of one segment register to another segment register. Algorithm operand1 = operand2
ADD	- REG, memory, immediate	- Adds two numbers Algorithm: op-1 = op.1 + op 2

LAB-2

Creating Variables:

name DB value (Define Byte)

name DW value. (Define Word)

example

```
K EQU 5
MOV AX, K
```

Creating Array.

```
a DB 48h, 65h, 6Ch,
b DB 'Hellow 0.
MOV AL , a[3]
MOV SI , 3
MOV AL . a[SI]
```

SUB: _____

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Instruction:	Operands	Description
INC	REG/MEM	Increment
		Algorithm
		operand = operand + 1
		Example: MOV AL, 9
DEC	REG/MEM	Decrement.
LEA	REG/MEM	Load Effective Address

LAB-9

```
. MODEL SMALL
. STACK 100H

. DATA
  STRING_1  DB 'I hate CSE331'
  STRING_2  DB 'BUT I LOVE Karachi'
```

```
.CODE
MAIN PROC
MOV AX, @DATA
MOV DS , AX
LEA DX, STRING_1
MOV AH, 9
INT 21H
MOV AH 2
MOV DL, ODA
INT 21H
MOV DL 0AH
INT 21H
LEA DX
MOV AH, 9
INT 21A
MAIN ENDP
END MAIN
```