CUDA Programming - Thread Organization

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&

"A certified CUDA instructor by NVIDIA"



Contents

- CUDA Programming Model
- Thread Organization using HelloGPU.cu
- Case study:
 - 1. vector addition
 - 2. Matrix Transpose
 - 3. 1D convolution
 - 4. 2D convolution

A typical C program

- 1. Include libraries
- 2. Declare variables
- 3. Allocate memory to the variables
- 4. Initialize variables
- 5. Perform computations
- 6. Store results
- 7. Free varables

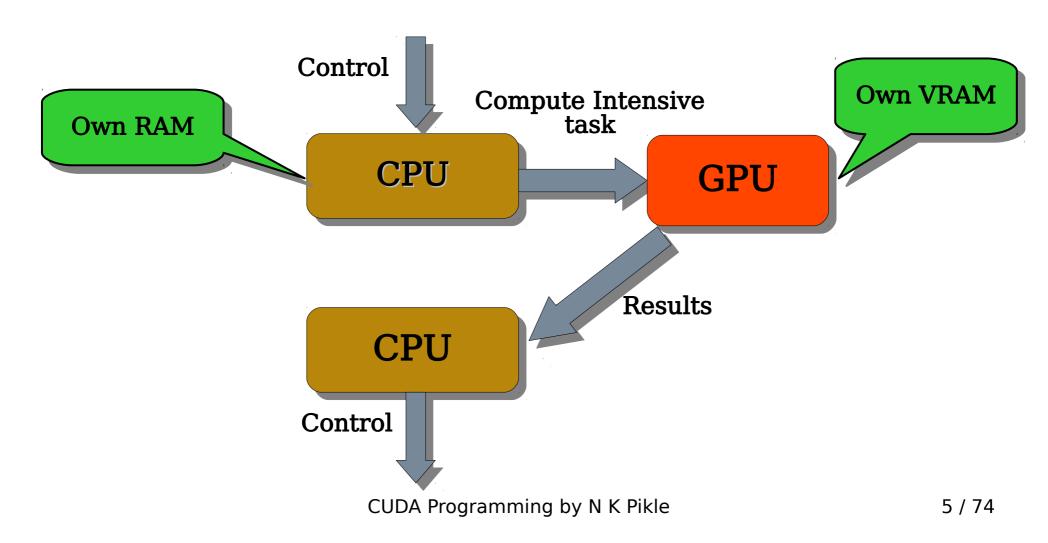
GPU is a coprocessor/ accelerator



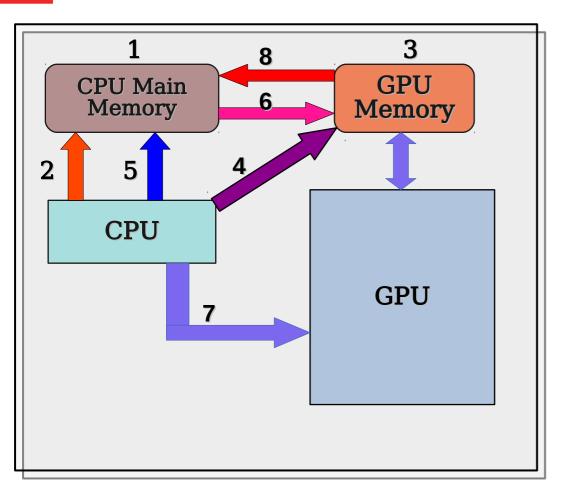
Only computationally intensive jobs are diverted towards GPU

GPU is a coprocessor/ accelerator

Only computationally intensive jobs are diverted towards GPU

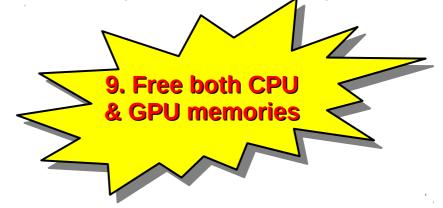


GPU Program Execution Model



- 1. Declare CPU variables
- Allocate memory to CPU variables
 Declare GPU variables

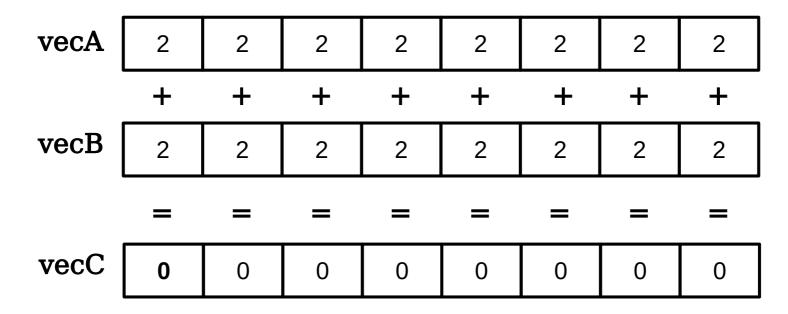
- 4. Allocate memory to GPU variables5. Initialize data in CPU memory6. Copy data from CPU memory to **GPU** memory
- 7. CPU instruct to GPU for parallel Execution
- 8. Copy results back from GPU Memory to CPU memory

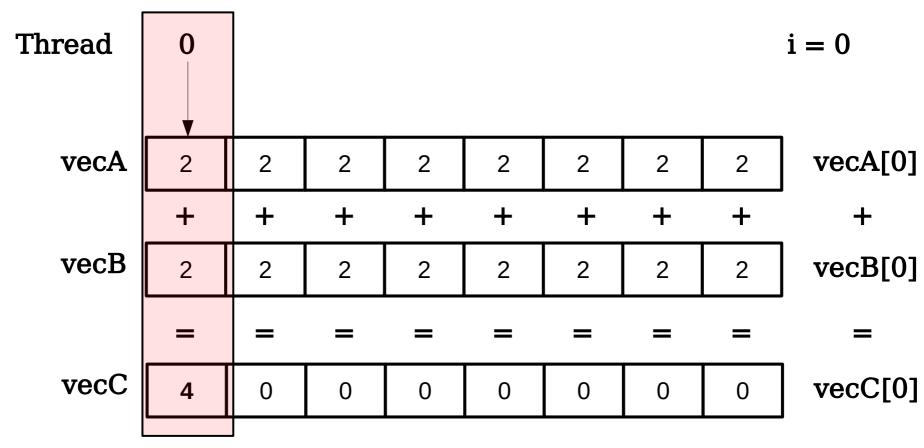


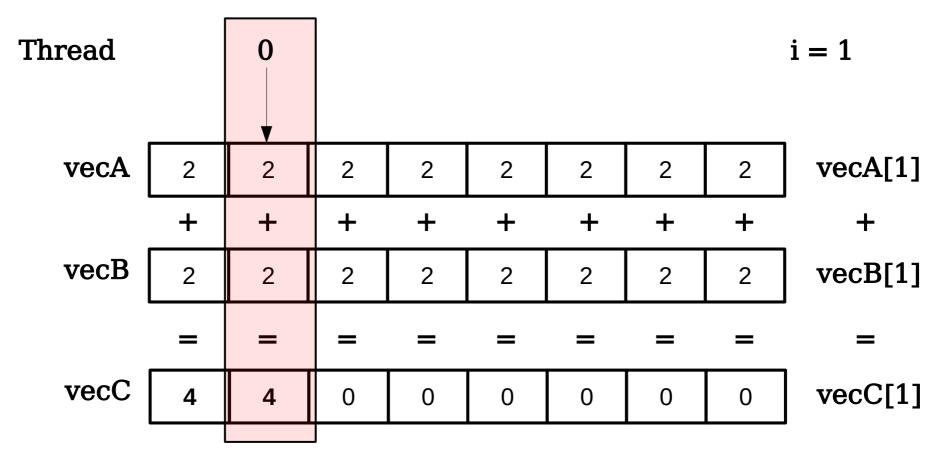
CUDA

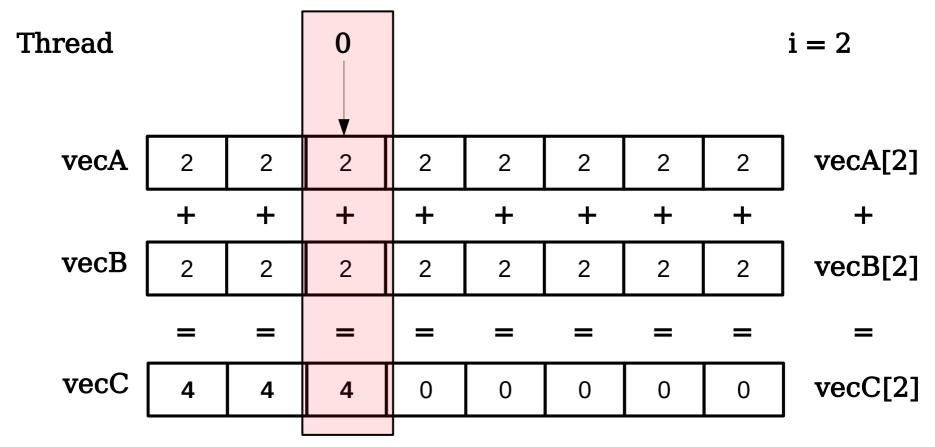
- Compute Unified Device Architecture
 - Used to code on NVIDIA GPUs
- File name
 - file name.cu
- Compile
 - **nvcc** flags file_name.cu
- Run
 - ./a.out

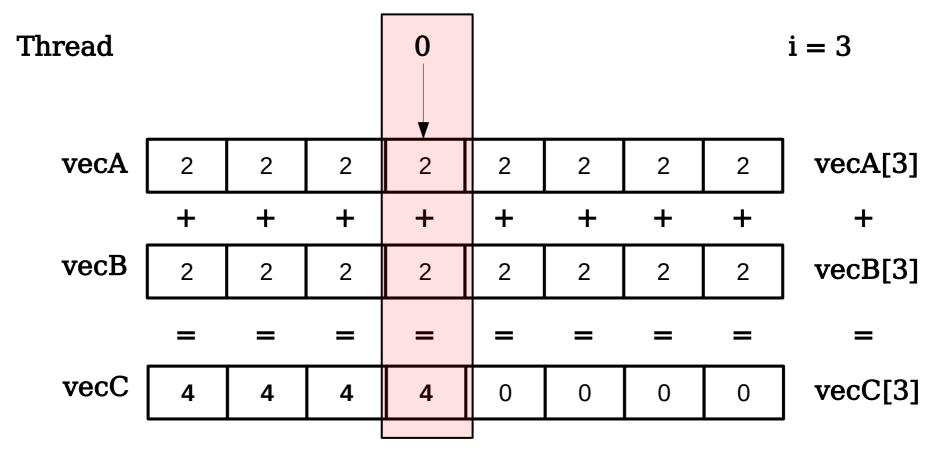
- Thread is basic execution unit on GPU
- GPU favors SIMT instructions
 Single Instruction Multiple Threads

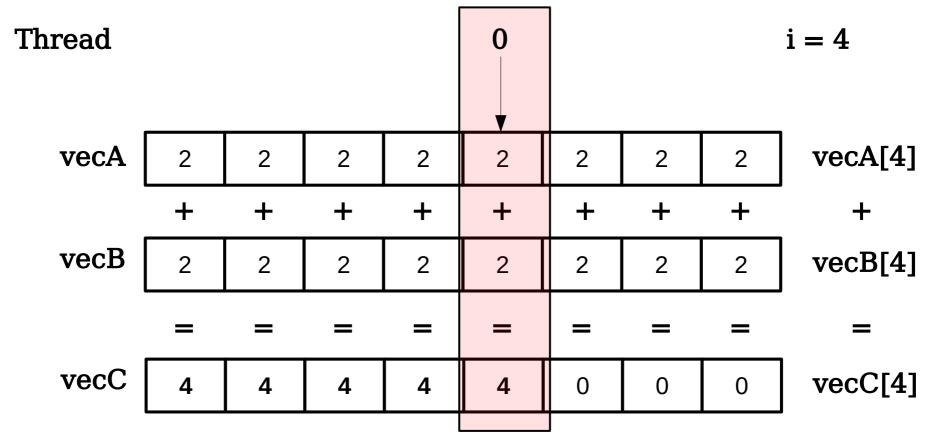


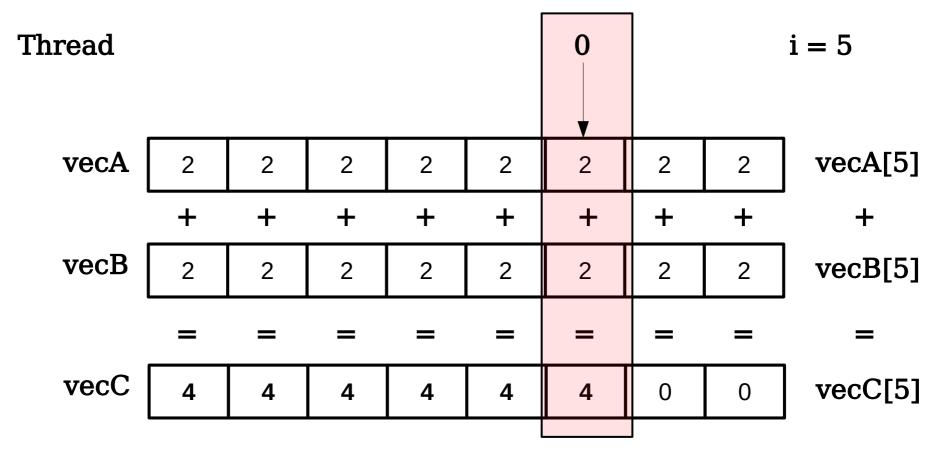


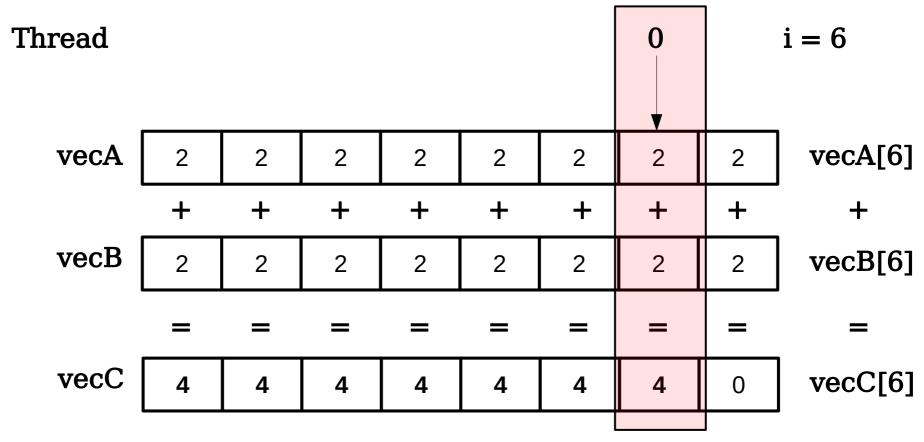


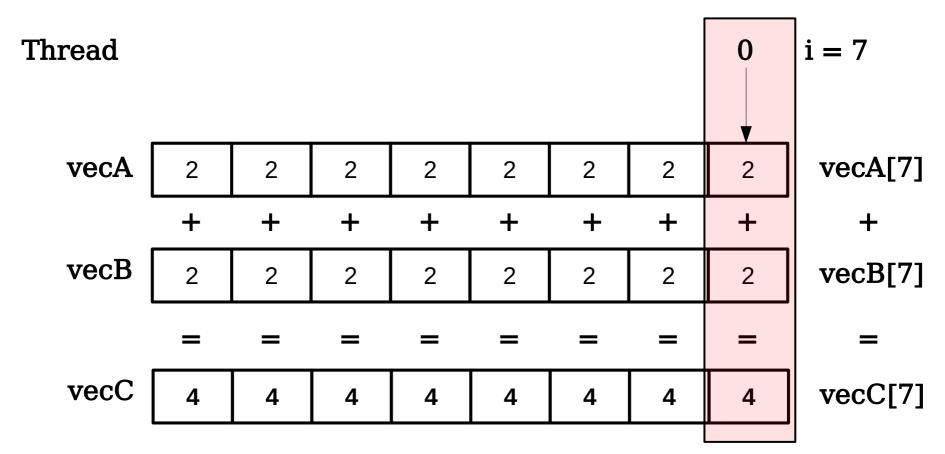


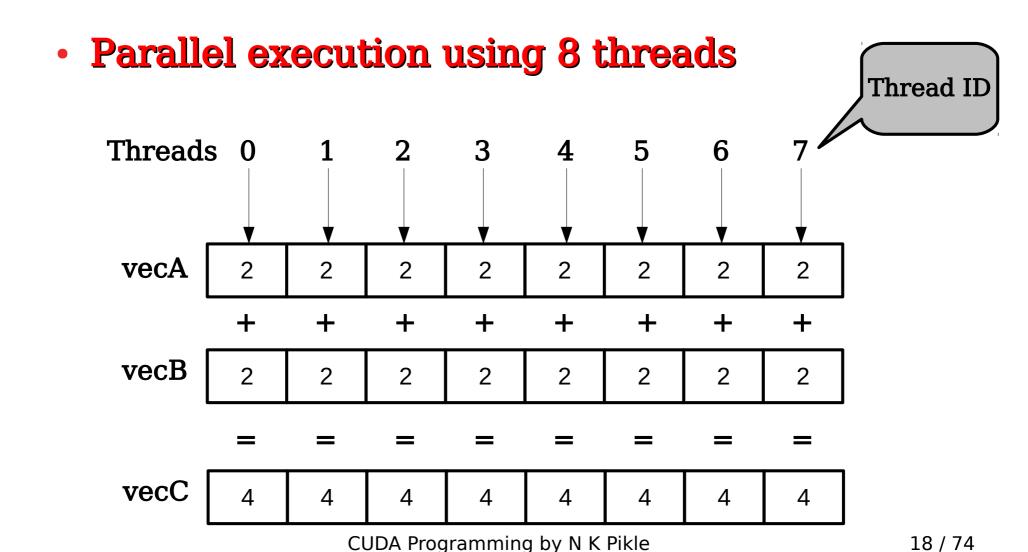




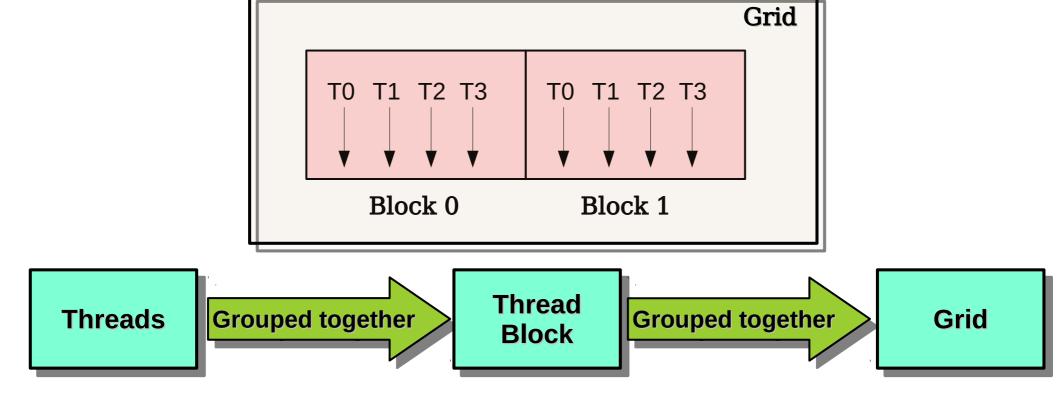






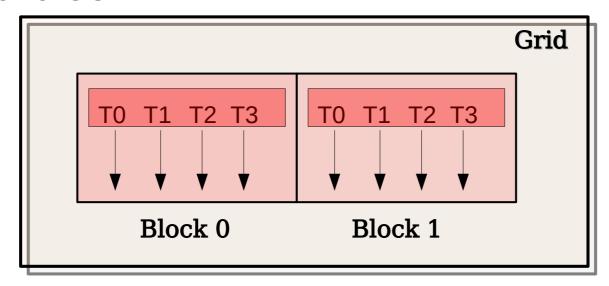


 Unlike OpenMP, threads are organized in CUDA in different way



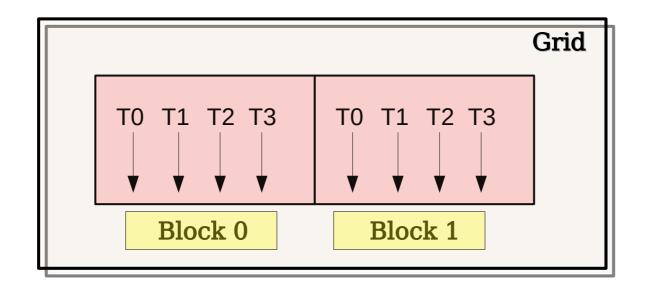
- Every thread has
 - 1. Thread Id retrieved by threadIdx.x
 - -Unique Thread ID (number) but local to block
 - 2. Block Id retrieved by blockIdx.x
 - -Unique Block ID (number)
 - 3. Block Dimensions retrieved by blockDim.x
 - Number of threads per block
 - 4. Grid Dimensions retrieved by GridDim.x
 - Number of blocks in grid

- Every thread has
 - 1. Thread Id- Gives thread number local to thread block



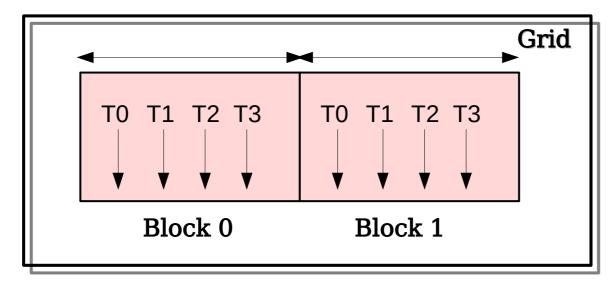
Observe: thread Ids T0, T1, T2 & T3 are numbered in both thread Blocks

- Every thread has
 - 2. Block Id- Gives block number



Observe: Block indices also starts with 0

- Every thread has
 - 3. Block Dimension- Gives number of threads in a block



Observe: Each block has 4 threads.

Each thread block has same number of threads

- Write helloGPU.cu program
- Function executed on GPU is called as kernel
- While calling fuction, thread configuration is specified

Program references

- 1. 1_helloGPU.cu2. 1_helloGPU_ThreadOrganization.cu

Case Study - 1 Vector addition in CUDA

Declare variables on CPU and GPU

```
int *h_a, *h_b, *h_c; // CPU varaibles
int *d_a, *d_b, *d_c; // GPU varaibles
```

Allocate memory for CPU variables

```
h_a = (int *)malloc(N*sizeof(int));
h_b = (int *)malloc(N*sizeof(int));
h_c = (int *)malloc(N*sizeof(int));
```

Initialize data on CPU

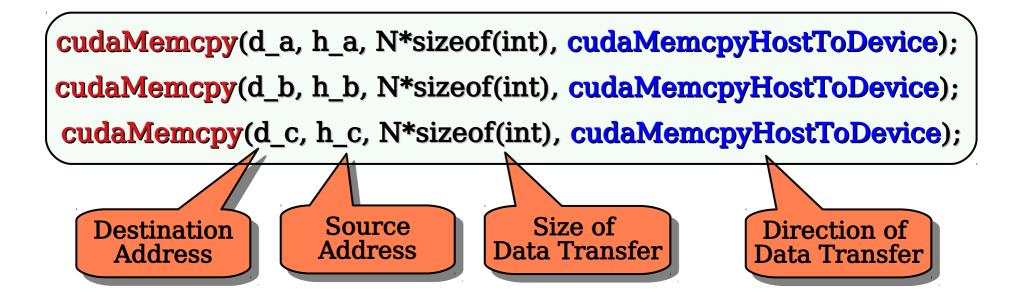
```
for(i = 0; i < N; i++)
{
  h_a[i] = 2;
  h_b[i] = 2;
  h_c[i] = 0;
}</pre>
```

Allocate memory for GPU varaibles

```
cudaMalloc((void **)&device_variable, size);
```

```
cudaMalloc((void ***)&d_a, N*sizeof(int));
cudaMalloc((void ***)&d_b, N*sizeof(int));
cudaMalloc((void ***)&d_c, N*sizeof(int));
```

Data transfer Host to Device



Kernel Launch on device

- vecAdd<<<numB, numT >>>(d_a, d_b, d_c, N);
 numT = # threads per block
 numB = total # thread blocks
- Therefore Total # threads launched = numT*numB

Eg. If
$$N = 128$$
 and $numT = 128$

Case-1: N = # threads per block A single thread block is sufficient

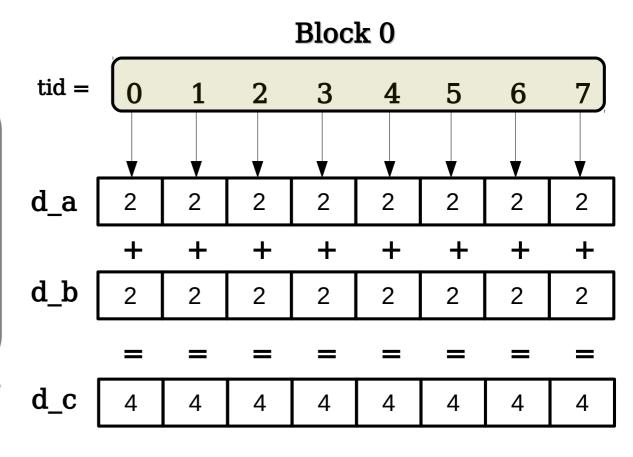
```
__global__ void vecAdd_kernel(int *d_a, int *d_b, int *d_c, int N){
  int tid = threadIdx.x;
  d_c[tid] = d_a[tid] + d_b[tid];
}
```

__global__ keyword indicates **device function** i.e. executed on **GPU**threadIdx.x returns thread Id of each thread and it is stored in **tid**

Eg: N = 8 Threads per block = 8 Number of blocks = 1

```
VecAdd <<<1,8>>>(d_a, d_b, d_c, N);
```

__global__ void vecAdd_kernel(int *d_a, int *d_b, int *d_c, int N){ int tid = threadIdx.x; d_c[tid] = d_a[tid] + d_b[tid]; }



```
• N = 32 then numT = 32 numB = 1

N = 64 then numT = 64 numB = 1

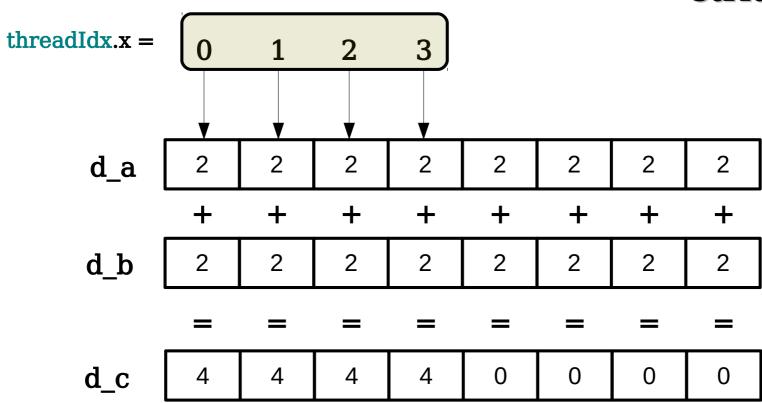
N = 1024 then numT = 1024 numB = 1
```

- Problem: There is a limit on Threads Per Block! 1024 What if N > 1024?
- Solution:
 - 1. Use stride
 - 2. Launch multiple blocks

- Single thread block
- Solution 1: Using stride

Iteration 1

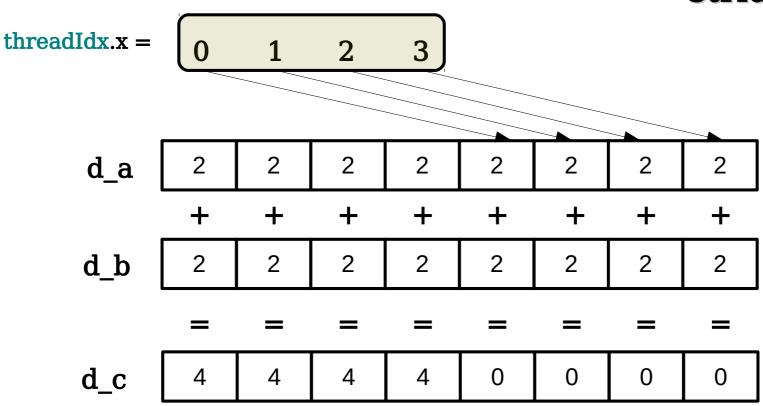
stride = 4



- Single thread block
- Solution 1: Using stride

Iteration 2

stride = 4



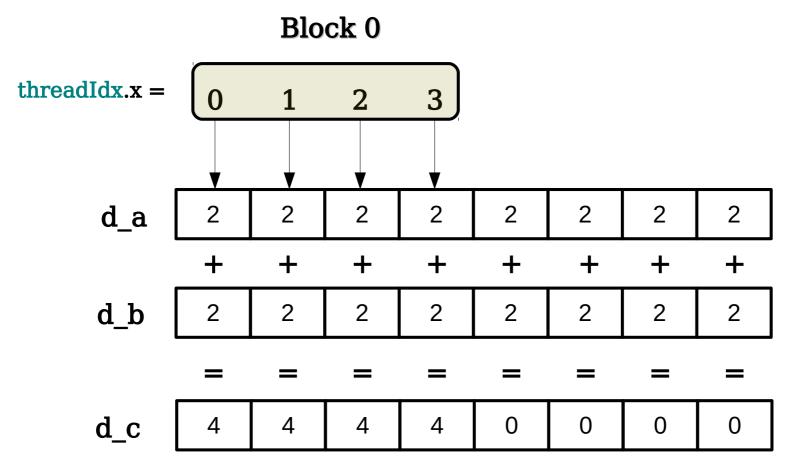
Single thread block with stride

```
global__ void vecAdd_kernel(int *d_a, int *d_b, int *d_c, int N)
 int tid = threadIdx.x;
 int i;
 for(i = 0; i < N; i+= blockDim.x)
    d c[i] = d a[i] + d b[i];
      Thread 0 performs operations on i = 0, 4, 8, \dots
      Thread 1 performs operations on i = 1, 5, 9, \dots
      Thread 2 performs operations on i = 2, 6, 10, \dots
      Thread 3 performs operations on i = 3, 7, 11, ...
```

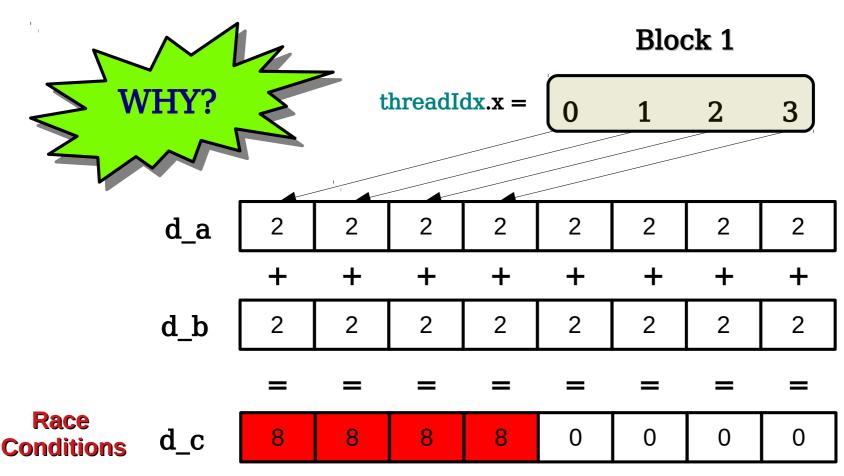
Refer code 2vecAddUsingOneBlock(N>numT).cu

- Solution 2: Use multiple thread blocks
- Eg. if N = 8 and numT = 4Then numB = 2 for size N = 8Total number of threads = numT * numB= 4 * 2= 8

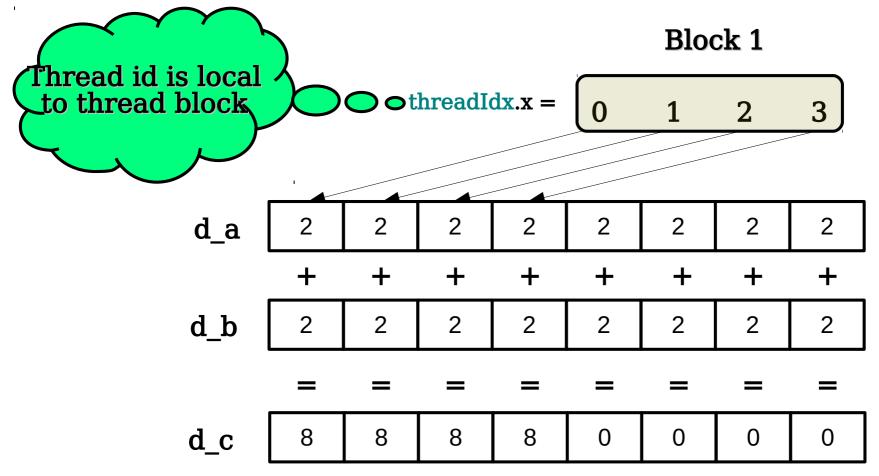
Block 0 mapping to vectors indexed from 0 to 3



Block 1 also mapping to vectors indexed from 0 to 3 WRONG!!!



Block 1 also mapping to vectors indexed from 0 to 3 **WRONG!!!**

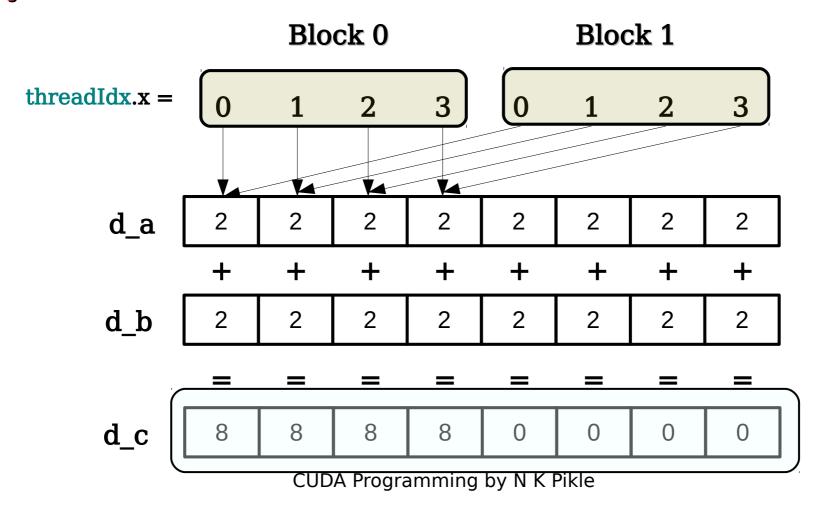


Block 1 also mapping to vectors indexed from 0 to 3 **WRONG!!!**

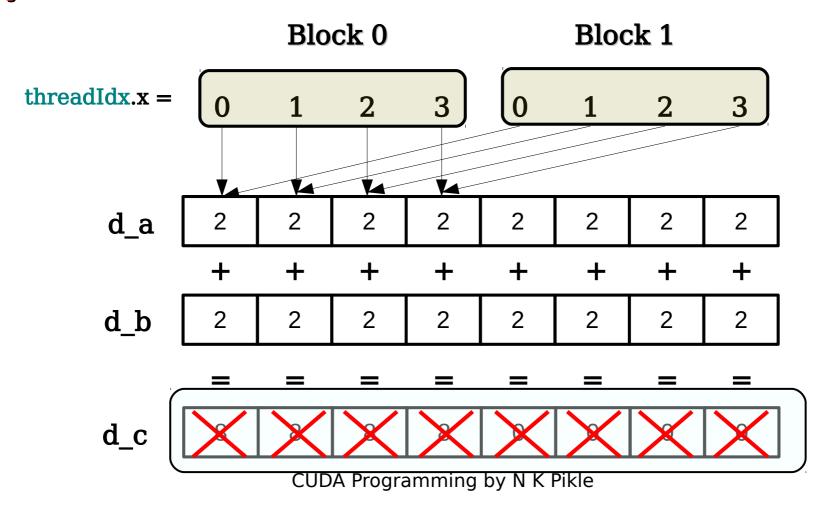
Block 1 Block 1 suuposed to map vector threadIdx.x =3 indices from 4 to 7 d_a 2 + d_b 2 2 2 2 2 2 d_c 8 8 8 0 0 0 CUDA Programming by N K Pikle

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Parallel Execution: Wrong answer as addition is performed Only on first 4 indices



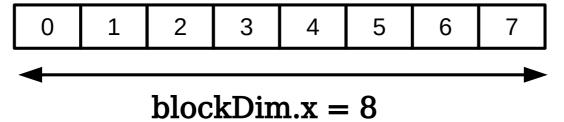
Parallel Execution: Wrong answer as addition is performed Only on first 4 indices



Solution: Compute global Thread Id

- Launching number of threads and blocks Eg. if N = 8
- 1) A single thread block with 8 threads

$$vecAdd < <<1,8>>> (d_a,d_b,d_c,N);$$

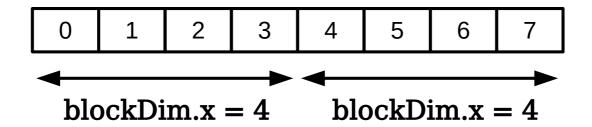


gridDim.x = 1 => Total number of thread blocks

Launching number of threads and blocks
 Eq. if N = 8

2) Two thread blocks with 4 threads

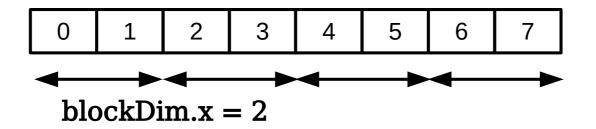
$$vecAdd < < 2,4 >>> (d_a,d_b,d_c,N);$$



gridDim.x = 2 => Total number of thread blocks

• Launching number of threads and blocks Eq. if N = 8

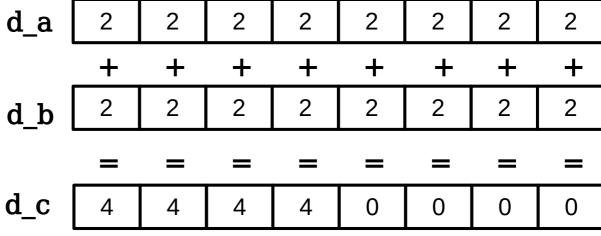
3) Four thread blocks with 2 threads



gridDim.x = 4 => Total number of thread blocks

Mapping threads from multiple blocks to data

int gid = threadIdx.x + blockIdx.x * blockDim.x

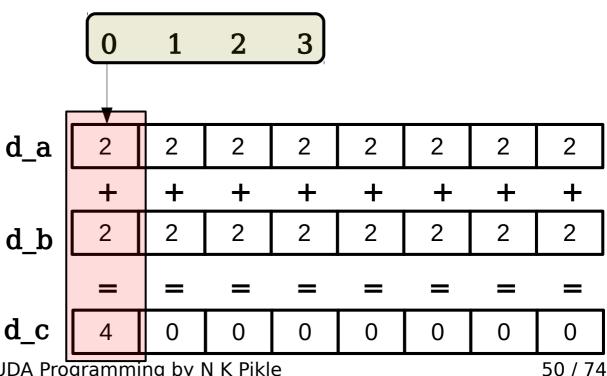


Mapping threads from multiple blocks to data

threadIdx.x = 0

blockIdx.x = 0

blockDim.x = 4



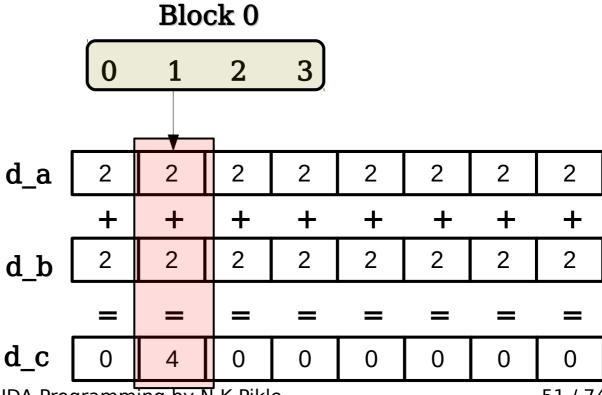
Block 0

Mapping threads from multiple blocks to data

threadIdx.x = 1

blockIdx.x = 0

blockDim.x = 4

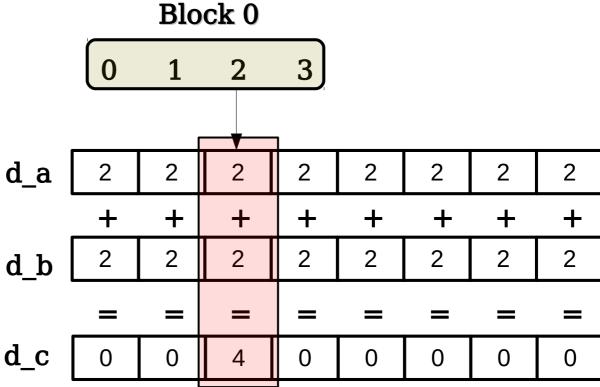


Mapping threads from multiple blocks to data

threadIdx.x = 2

blockIdx.x = 0

blockDim.x = 4

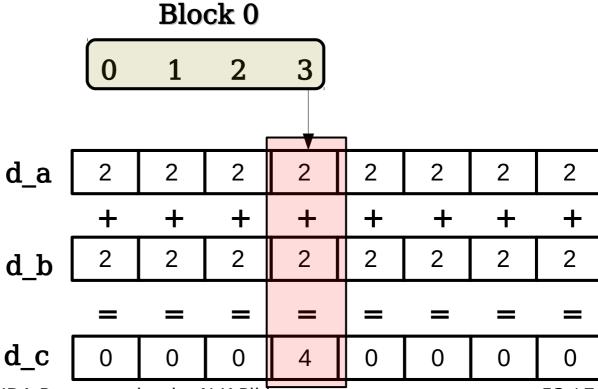


Mapping threads from multiple blocks to data

threadIdx.x = 3

blockIdx.x = 0

blockDim.x = 4



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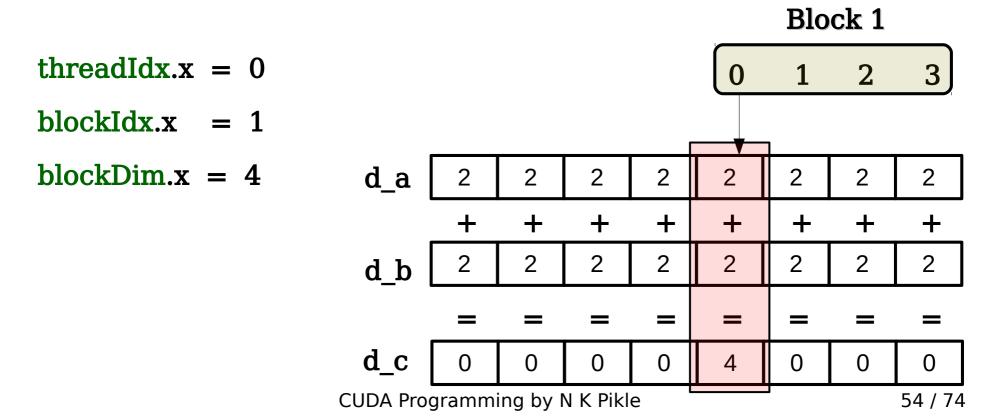
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Mapping threads from multiple blocks to data

```
int gid = threadIdx.x + blockIdx.x * blockDim.x

gid = 0 + 1 * 4

gid = 4
```



Mapping threads from multiple blocks to data

```
int gid = threadIdx.x + blockIdx.x * blockDim.x
gid = 1 + 1 * 4
gid = 5
```

 $\mathbf{d}_{\mathbf{c}}$

0

Block 1

0

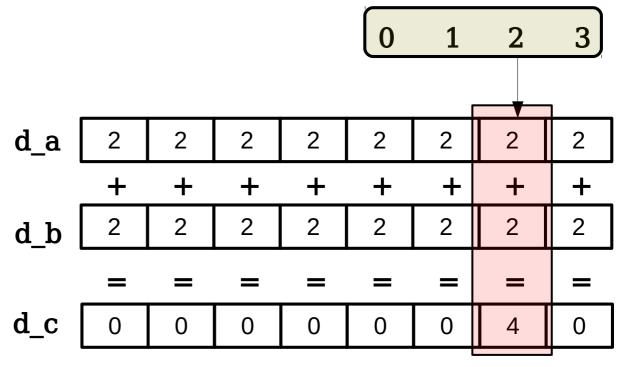
Mapping threads from multiple blocks to data

```
int gid = threadIdx.x + blockIdx.x * blockDim.x
gid = 2 + 1 * 4
gid = 6
```

threadIdx.x = 2

blockIdx.x = 1

blockDim.x = 4



Block 1

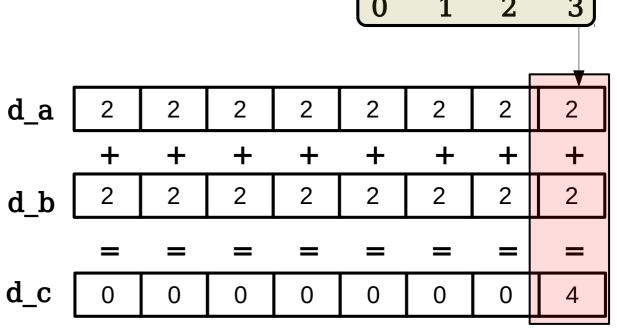
Mapping threads from multiple blocks to data

```
int gid = threadIdx.x + blockIdx.x * blockDim.x
gid = 3 + 1 * 4
gid = 7
```

threadIdx.x = 3

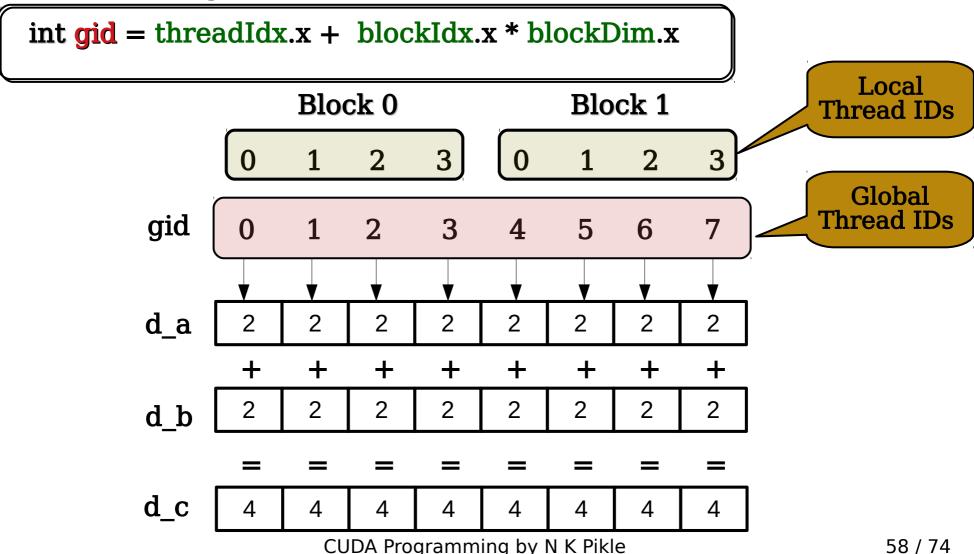
blockIdx.x = 1

blockDim.x = 4



Block 1

Mapping threads from multiple blocks to data



- Problems
- 1. What if number of threads greater than N?
- 2. What if number of threads less than N?

1. What if number of threads greater than N?

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What if number of threads greater than N?

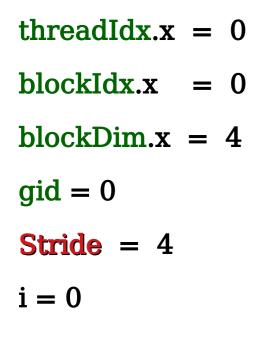
Program references

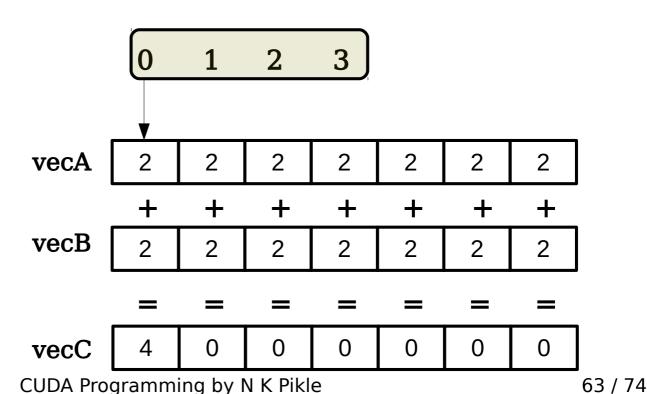
1vecAddUsingManyBlocks.cu

Vector addition using a #threads < N

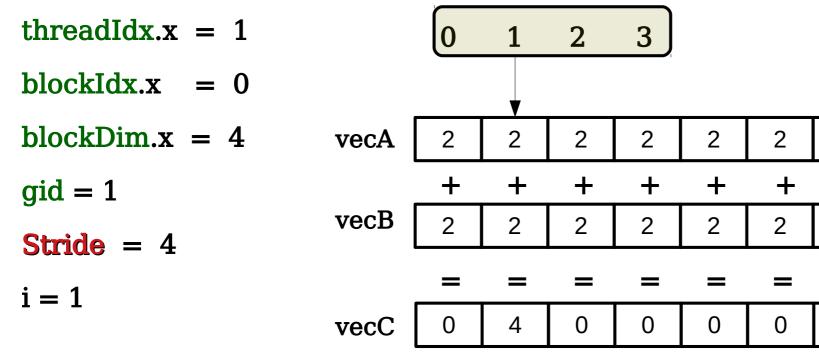
```
global void vecAdd_kernel(int *a, int *b, int *c, int N){
int gid = threadIdx.x + blockIdx.x * blockDim.x;
int stride = blockDim.x * gridDim.x;
                                             #blocks in grid
int i;
for(i = gid; i < N; i += stride){
c[i] = a[i] + b[i];
```

```
int gid = threadIdx.x + blockIdx.x * blockDim.x;
int stride = blockDim.x * gridDim.x;
for(i = gid; i < N; i += stride)</pre>
```





```
int gid = threadIdx.x + blockIdx.x * blockDim.x;
int stride = blockDim.x * gridDim.x;
for(i = gid; i < N; i += stride)</pre>
```

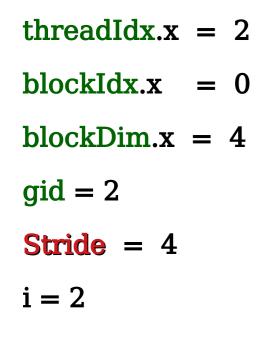


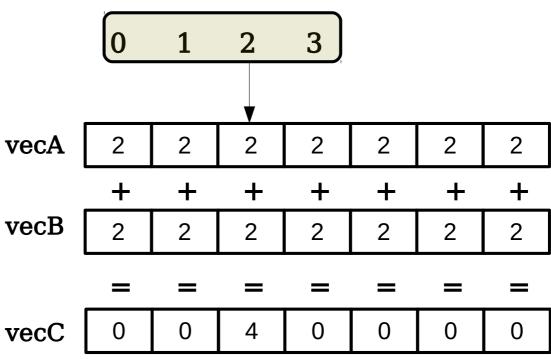
2

+

0

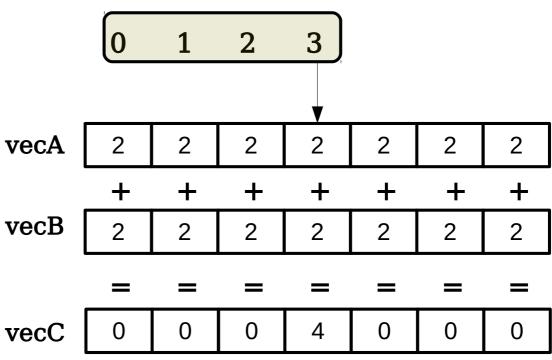
```
int gid = threadIdx.x + blockIdx.x * blockDim.x;
int stride = blockDim.x * gridDim.x;
for(i = gid; i < N; i += stride)</pre>
```





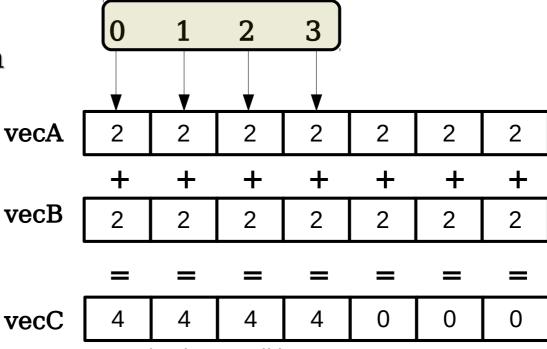
```
int gid = threadIdx.x + blockIdx.x * blockDim.x;
int stride = blockDim.x * gridDim.x;
for(i = gid; i < N; i += stride)</pre>
```

threadIdx.x = 3
blockIdx.x = 0
blockDim.x = 4
gid = 3
Stride = 4 i = 3

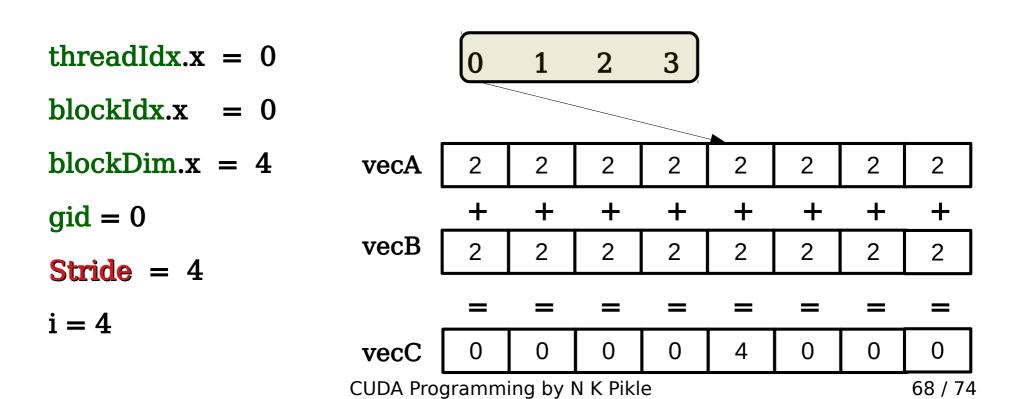


```
int gid = threadIdx.x + blockIdx.x * blockDim.x;
int stride = blockDim.x * gridDim.x;
for(i = gid; i < N; i += stride)</pre>
```

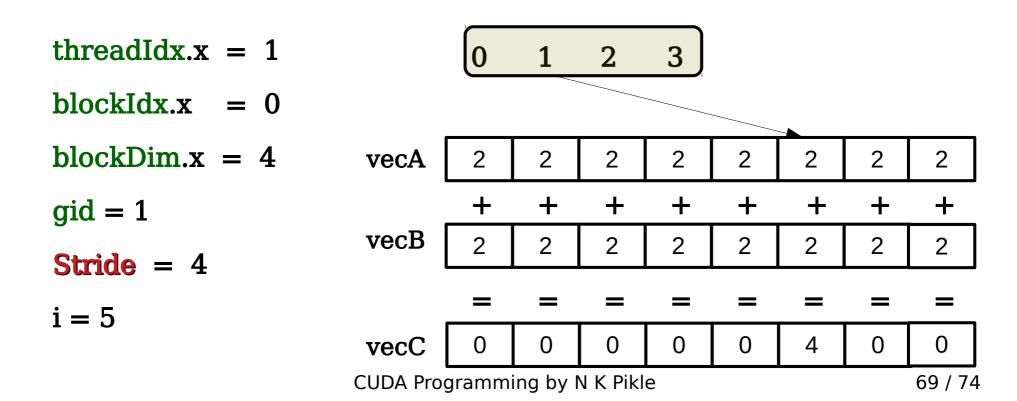
Actually all threads from one block are executed simultaneously



```
int gid = threadIdx.x + blockIdx.x * blockDim.x;
int stride = blockDim.x * gridDim.x;
for(i = gid; i < N; i += stride)</pre>
```



```
int gid = threadIdx.x + blockIdx.x * blockDim.x;
int stride = blockDim.x * gridDim.x;
for(i = gid; i < N; i += stride)</pre>
```



```
int gid = threadIdx.x + blockIdx.x * blockDim.x;
int stride = blockDim.x * gridDim.x;
for(i = gid; i < N; i += stride)</pre>
```

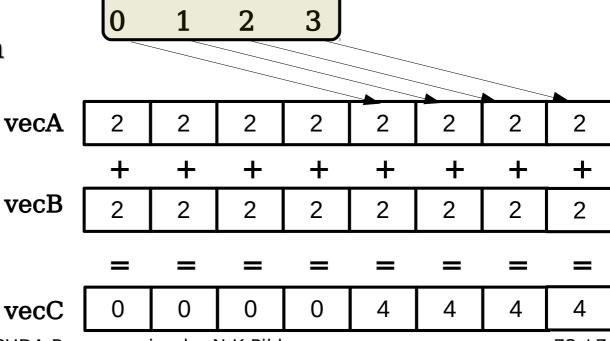
threadIdx.x = 23 1 blockIdx.x = 0blockDim.x = 4vecA 2 2 2 2 2 2 + + gid = 2vecB 2 2 2 Stride = 4i = 6vecC 0 CUDA Programming by N K Pikle 70 / 74

```
int gid = threadIdx.x + blockIdx.x * blockDim.x;
int stride = blockDim.x * gridDim.x;
for(i = gid; i < N; i += stride)</pre>
```

threadIdx.x = 33 1 blockIdx.x = 0blockDim.x = 4vecA 2 2 2 2 2 + + gid = 3vecB 2 2 2 Stride = 4i = 7vecC 0 CUDA Programming by N K Pikle 71 / 74

```
int gid = threadIdx.x + blockIdx.x * blockDim.x;
int stride = blockDim.x * gridDim.x;
for(i = gid; i < N; i += stride)</pre>
```

Actually all threads from one block are executed simultaneously



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Program references

2vecAddUsingManyBlocks(N>threads).cu

Summary

- Thread organization 1 D
- Case study vector addition
 scenario 1 using single thread block
 - 1. N less than or equal to block size
 - 2. N greater than thread block size
- Scenario 2 using multiple thread blocks
 - 1. N less than or equal to total threads
 - 2. N greater than thread total threads