TIME ONE HOURS   **M.M. 30**

Q1. What is Microprocessor?
a) A multipurpose PLD that accepts binary data as input
b) A multipurpose PLD that accepts an integer as input
c) A multipurpose PLD that accepts whole numbers as input
d) A multipurpose PLD that accepts prime numbers as input

Q2.What will the content of A after the execution of the fallowing instructions:
MVI A, 2CH
STA 3000
HLT

a)3000   c)44

b)both c &d are correct   d)2CH

Q3. In 8085 microprocessor which is the first machine cycle of instruction

a) An op-code fetch machine cycle

 b) An memory read/write machine cycle

 c) An IO read/write machine cycle

 d) Any of the above

Q4.The instruction JNC 16 BIT ADDRESS refers to jump to 16 bit address if ?

a) Sign flag is set

b) Carry flag is set

c) Carry flag is reset

d) Zero flag is set

Q5. In 8086 microprocessor , the offset of a particular segment varies from

a) 000H to FFFH   c) 0000H to FFFFH

b) 00H to FFH   d) 00000H to FFFFFH

Q6. In a microprocessor Stack works on:

a) LILO   c) LIFO

 b) FIFO   d) none

Q7. In 8-bit microprocessor, How many opcodes are present?
a)246
b)278
c)250
d) 256

Q8. In an intel 8085A microprocessor, why is READY signal used?

a)To indicate to user that the microprocessor is working and is ready for use.

b) To provide proper WAIT states when the microprocessor is communicating with a slow peripheral device.

c) To slow down a fast peripheral device so as to communicate at the microprocessor's device.

d) none of the above.

Q9.The instruction which is used to store the data into accumulator from a register pair

a)LDAX   c)STAX

b)LDA   d) STA

Q10.The instructions used to transfer 16 bit number of memory location 9000H and 9001H into register pair HL

a)LDA 9000H   c)LHLD 9000 H

b) LDHL 9000H   d) MOV HL,9000 H

Q11.what will be content of reg H after the execution of instruction INR H, if reg H contains FF H

   a) 00H   c)F1 H
   b) 100 H   d) none

Q12. If A>R , then after execution of instruction CMP R what will be the status of flags?

   a)CF=0,Z=1   c) CF=0,Z=0

   b) CF=1, Z=0   d)CF=1,Z=1

Q13. How many times the loop will be executed:

     LXI B,000F H

  LOOP:DCX B

     MOV A,B

     ORA C

     JNZ LOOP

   a) 14 times   c) 15 times
   b) 10 times   d) none

1. **Q14**. The purpose of the microprocessor is to control ___
   A. memory   B. switches   C. processing   D. tasks

Q15. It is an edge triggered input, which causes an interrupt request to the microprocessor.

a) NMA   c)INTR

b) INTA   d) ALE

Q16. What will be content of AL after the execution of following instruction:

MOV AL, [9106H]

if DS contains 1005H , [91060] contains 56 H, [19156] contains 93 H & AL contains 35 H

a)35H   c) 56H

b)93H   d)none

Q17.Which instruction is incorrect:

a)MOV AL, [BP]   c) MOV AL, [BX+12]

b)MOV AL, [BX][SI]   d) MOV AL, [7643H]

Q18. In 8086 microprocessor, during the execution of TEST instruction the operation performed between source and destination   is

   a) OR   c)AND
   b) TEST   d) none

Q19. The instruction used to move data from an input port 12A4 H to register AL is

   a) MOV   c) IN
   b) OUT   d) LXI

Q20. The data lines connected to even memory in 8086 microprocessor are:

   a) D_0-D_{15} c)D_0-D_7
   b) D_8-D_{15} d) none

Q21. If the content of SS=15A1 H & E.A.=ABC0 H. The physical address is

   a) 205D0 H   c)15A10 H
   b) ABC00H   d)2F5D0 H

Q22. In any microprocessor the address bus width of 64 KB of memory is ----------------.

   a) 8 bits   c)32 bits

   b) 16 bits   d)none

Q23. The size of each segment register in 8086 is ? a)64 kb

c) 24 kb b) 1MB   d) 16kb

Q24. In DIV instruction if source is 8 bits it will divide AX by source & after division

a)   The Quotient will be in AL & remainder in AH. b)   The
Quotient will be in AH & remainder in AL. c)   The Quotient will
be in AX & remainder in DX. d)   The Quotient will be in Dx &
remainder in AX.

Q25. In instruction Queue the next byte is fetched from memory when the values of QS1 & QS0 a)00
c)10

b)01   d)11

Q26. Which of the following instruction is incorrect:

a)   MOV DS,1234 H   c)MOV DL, 1234 H[SI]
b)   MOV AL, AH   d) MOV AL,15 H[SI]

Q27. If the direction flag in flag register of 8086 is zero then the value of SI & DI will be:

a)   Increment by 1or2   c)decrement by 1or 2
b)   remains same   d)none

Q28. The interrupt request that is independent of IF flag is

a) NMI   c) TRAP

b) Divide by zero   d) All of the mentioned

Q29. Which of the following are the control flags in a flag register in 8086. a)   DF,TF,IF

c)DF,IF
b)   DF,IF,TF,OF   d)IF,C,OF

Q30. In 8086 microprocessor when reset is sent to logic 1, the content of CS is

a)   0000 H   c)no change
b)   FFFF H   d) none