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Integral windup

Integral windup, also known as integrator windup^[1] or reset windup,^[2] refers to the situation in a <u>PID feedback controller</u> where a large change in setpoint occurs (say a positive change) and the integral terms accumulates a significant error during the rise (windup), thus <u>overshooting</u> and continuing to increase as this accumulated error is unwound (offset by errors in the other direction). The specific problem is the excess overshooting.

Solutions

This problem can be addressed by

- Initialising the controller integral to a desired value, for instance to the value before the problem
- Increasing the setpoint in a suitable ramp
- Disabling the integral function until the to-be-controlled process variable (PV) has entered the controllable region
- Preventing the integral term from accumulating above or below pre-determined bounds
- Back-calculating the integral term to constrain the process output within feasible bounds.^[3]

Occurrence

Integral windup particularly occurs as a limitation of physical systems, compared with ideal systems, due to the ideal output being physically impossible (process <u>saturation</u>: the output of the process being limited at the top or bottom of its scale, making the error constant). For example, the position of a valve cannot be any more open than fully open and also cannot be closed any more than fully closed. In this case, anti-windup can actually involve the integrator being turned off for periods of time until the response falls back into an acceptable range.

This usually occurs when the controller's output can no longer affect the controlled variable, or if the controller is part of a selection scheme and it is selected right.

Integral windup was more of a problem in <u>analog</u> controllers. Within modern <u>Distributed Control Systems</u> and <u>Programmable Logic Controllers</u>, it is much easier to prevent integral windup by either limiting the controller output, or by using external reset feedback, which is a means of feeding back the selected output to the integral circuit of all controllers in the selection scheme so that a closed loop is maintained.

References

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