Animesh Chhotaray
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OVERVIEW

Nationality : Indian
PhD student (Current GPA: 3.79), Cryptography.

APPOINTMENTS

Research Assistant   2017-
FICS Research, University of Florida

Teaching Associate   2013-2015
KIIT University, Bhubaneswar, India

- Research: Develop image encryption techniques using orthonormal and self-invertible matrices.
- Teaching: Programming in C, Computer Security.

Engineer   2011-2013
Samsung Research & Development Institute, NOIDA, India

- Worked for Systems team (FM driver) on feature phones with ARM based SoC ranging from 2G (48xx, 49xx) to 3G (68xx).
- Worked on code optimization, bug fixing and code stabilization through analysis of RAM dumps and traces, and test cases performed for side effects.

EDUCATION

MS Computer Science   2015-2017
University of Florida

- GPA: 3.79/4.0
- Courses : Computer Network Security, Penetration Testing, Introduction to Modern Cryptography, Analysis of Algorithms, Network Algorithms and Data Structures Advanced Data Structures, Programming Language Principles, Computer Architecture, Automated Software and Hardware verification, Mathematics for Intelligent Systems

BTech Computer Science   2007-2011
NIT Rourkela, India

PUBLICATIONS

1. A. Chhotaray, A. Nahiyan, T. Shrimpton, D. Forte, M. Tehranipoor, "Standardizing Bad Cryptographic Practice - A teardown of the P1735 IEEE standard for protecting electronic-design intellectual property", *ACM CCS* , (2017). **7 Common Vulnerabilities and Exposures (CVE) entries in the Vulnerability Notes Database. Featured in The Register, threatpost, and other cybersecurity news publications.**

TEACHING

@ KIIT University, School of Computer Engineering:

- 2013-2015: **Programming in C, Computer Security**

SERVICE

- External reviewer @NDSS'20

- Sub-reviewer @CRYPTO'20, @WiSec'20