



# Software Release Notes USB Power Delivery Software Framework (PSF)

THIS DOCUMENT IS UNCONTROLLED UNLESS OTHERWISE STAMPED. It is the user's responsibility to ensure this is the latest revision prior to using or referencing this document.	Page	Spec. No.	Rev.
© Microchip Technology Inc.	1 of 7	PSF	0.95



Information contained in this publication regarding device applications and the like is provided only for your convenience and can be superseded by updates. It is your responsibility to ensure that your application meets with your specifications. MICROCHIP MAKES NO REPRESENTATIONS OR WARRANTIES OF ANY KIND WHETHER EXPRESS OR IMPLIED, WRITTEN OR ORAL, STATUTORY OR OTHERWISE, RELATED TO THE INFORMATION, INCLUDING BUT NOT LIMITED TO ITS CONDITION, QUALITY, PERFORMANCE, MERCHANTABILITY OR FITNESS FOR PURPOSE.

Microchip disclaims all liability arising from this information and its use. Use of Microchip devices in life support and/or safety applications is entirely at the buyer's risk, and the buyer agrees to defend, indemnify and hold harmless Microchip from any and all damages, claims, suits, or expenses resulting from such use. No licenses are conveyed, implicitly or otherwise, under any Microchip intellectual property rights.

THIS DOCUMENT IS UNCONTROLLED UNLESS OTHERWISE STAMPED. It is the user's responsibility to ensure this is the latest revision prior to using or referencing this document.	Page	Spec. No.	Rev.
© Microchip Technology Inc.	2 of 7	PSF	0.95





Microchip Technology, Incorporated 2355 W. Chandler Boulevard Chandler, Arizona 85224 480/792-7200

REV	DATE	DESCRIPTION OF CHANGE
0.90	Oct 24, 2019	First Web release
0.91	Nov 11,2019	Bug fix release
0.92	Dec 12,2019	Documentation release
0.95	Jan 09, 2020	Release with complete documentation and Bug fix

THIS DOCUMENT IS UNCONTROLLED UNLESS OTHERWISE STAMPED. It is the user's responsibility to ensure this is the latest revision prior to using or referencing this document.	Page	Spec. No.	Rev.
© Microchip Technology Inc.	3 of 7	PSF	0.95



# **Table of Contents**

l	Introdu	ction	
2	Release	e notes	
	2.1 Ver	rsion 0.95	5
	2.1.1	Not implemented / Limited functionality requirements	5
	2.1.2	Bug Fixes	5
	2.1.3	Features added	
	2.1.4	Notes	6
	2.2 Ver	rsion 0.92	6
	2.2.1	Not implemented / Limited functionality requirements	6
	2.2.2	Bug Fixes	6
	2.2.3	Features added	6
	2.2.4	Notes	6
	2.3 Ver	rsion 0.91	6
	2.3.1	Not implemented / Limited functionality requirements	6
	2.3.2	Bug Fixes	7
	2.3.3	Features added	7
	2.3.4	Notes	7
	2.4 Ver	rsion 0.90	7
	2.4.1	Not implemented / Limited functionality requirements	
	2.4.2	Bug Fixes	7
	2.4.3	Features added	7
	2.4.4	Notes	7

THIS DOCUMENT IS UNCONTROLLED UNLESS OTHERWISE STAMPED. It is the user's responsibility to ensure this is the latest revision prior to using or referencing this document.	Page	Spec. No.	Rev.
© Microchip Technology Inc.	4 of 7	PSF	0.95



# 1 Introduction

USB Power Delivery Software Framework (PSF) – a software-based Power Delivery stack along with UPD350 Type-C Port Controller (Maverick) is a USB-PD solution. It is a generic user-friendly that can be ported across various hardware platform.

<b>Component Type</b>	USB Power Delivery Software Framework (PSF)
Target Silicon	SAMD20 + UPD350
Where can an end	Version number appended with filename
user see the version	/PSF/Source/include/ProjectVersion.h
number	

## 2 Release notes

## 2.1 Version 0.95

Release date	09-Jan-2020
Release Type	Beta Release
Pre-requisites (if any)	PSF EVB Rev A
Source Tag	Tag: PSF_STACK_V0.95

## 2.1.1 Not implemented / Limited functionality requirements

- 1. PSF-44: TDA 2.3.1.1 Source Dynamic Load Test, Provider or Provider/Consumer Test Fails when PUT is configured as Source
- 2. PSf-41: SPT.5 Over Current OuadraMAX Test Fails on Both Ports

## 2.1.2 Bug Fixes

- 1. PSF-21: VBUS OCS Power fault handling not working DC\_DC\_EN was toggled twice to reset the fault occurred. Since DC\_DC was set and reset immediately, DC-DC controller did not have enough time to reset itself. PIO Override drive was not reset after a Fault. Both the issues are fixed.
- 2. PSF-7: HiByte has potential error Fixed the warning generated by PC Lint
- 3. PSF-65: PD Source tests in Ellisys compliance fails when MchpPSF\_Run() is called for every 3ms This issue is fixed by setting PE\_PDCONNECTED\_STS\_MASK in ePE\_SRC\_NEGOTIATE\_CAPABILITY state.
- 4. PSF-61: Unable to charge HP-Laptop Possible Bug with VCONN SWAP response (Reject/Not Supported) For source only operation, INCLUDE\_VCONN\_SWAP\_SUPPORT should be defined as '1'. Same is updated in the PSF\_Config.h file description.
- 5. PSF-69: When INCLUDE\_UPD\_PIO\_OVERRIDE\_SUPPORT defined as 0, negotiation is not happening Undervoltage was detected by FW without considering whether under

THIS DOCUMENT IS UNCONTROLLED UNLESS OTHERWISE STAMPED. It is the user's responsibility to ensure this is the latest revision prior to using or referencing this document.	Page	Spec. No.	Rev.
© Microchip Technology Inc.	5 of 7	PSF	0.95



voltage power fault detection was enabled; Which caused undervoltage detection and follow up power shutdown. It is fixed now.

#### 2.1.3 Features added

NA

#### 2.1.4 Notes

- Only 2 Port Source and Sink solution has been tested at 8MHz SPI clock speed.
- System level PD communication between PSF and UPD350 through I2C interface is untested, whereas basic I2C read/writes are tested.

## 2.2 Version 0.92

Release date	12-Dec-2019
Release Type	Documentation Release
Pre-requisites (if any)	PSF EVB Rev A
Source Tag	Tag: PSF_STACK_V0.92

## 2.2.1 Not implemented / Limited functionality requirements

Firmware Bug fixes are not made for this release. All the bugs observed with V0.91 release is applicable for V0.92 release too.

# 2.2.2 Bug Fixes

No Bug fixes made

## 2.2.3 Features added

This release is made to mark the release of following documents

- PSF User Guide
- Getting Started with PSF
- Demo Read me

#### 2.2.4 Notes

None.

## 2.3 Version 0.91

Release date	11-Nov-19
Release Type	Bug fix release
Pre-requisites (if any)	PSF EVB Rev A
Source Tag	Tag: PSF_STACK_V0.91

# 2.3.1 Not implemented / Limited functionality requirements

1. PSF-7 - HiByte has potential error

THIS DOCUMENT IS UNCONTROLLED UNLESS OTHERWISE STAMPED. It is the user's responsibility to ensure this is the latest revision prior to using or referencing this document.	Page	Spec. No.	Rev.
© Microchip Technology Inc.	6 of 7	PSF	0.95



## 2.3.2 Bug Fixes

- 1. PSF-19 -FW has build issue when CONFIG\_PD\_PORT\_COUNT set to 1
- 2. PSF-21 VBUS OCS Power fault handling not working
- 3. PSF-26 Build Fails When INCLUDE\_POWER\_FAULT\_HANDLING Macro is set to 0
- 4. PSF-31 Build Fails When INCLUDE\_PDFU Macro is set to 1
- 5. PSF-32 Configuring CONFIG\_PORT\_n\_SINK\_USB\_COM macro Field is not Effective
- 6. PSF-33 Configuring CONFIG\_PORT\_n\_SINK\_UNCONSTRAINED\_PWR macro Field is not Effective
- 7. PSF-34 Build Fails When INCLUDE\_PD\_3\_0 Macro is set to 0

#### 2.3.3 Features added

NA

#### 2.3.4 Notes

Following JIRA reported marked invalid after developing from developer's side:

- 1. PSF-22 -VCONN OCS handling not working
- 2. PSF 23 PD Negotiation Fails when PDOs are Configured more than 5 with certain values
- 3. PSF-25 The PUT is not Disabled Even After the Under Voltage Count Exceeds the Maximum Fault Count

## 2.4 Version 0.90

Release date	24-Oct-2019
Release Type	Initial Web release
Pre-requisites (if any)	PSF EVB Rev A
Source Tag	Tag: PSF_STACK_V0.91

# 2.4.1 Not implemented / Limited functionality requirements

- 1. PSF-7 HiByte has potential error
- 2. PSF-21 VBUS Power fault handling not working
- 3. PSF-22 VCONN OCS handling not working
- 4. PSF-23 PD Negotiation Fails when PDOs are Configured more than 5 with certain values
- 5. PSF-25 The PUT is not Disabled Even After the Under Voltage Count Exceeds the Maximum Fault Count

## 2.4.2 Bug Fixes

Not Applicable

#### 2.4.3 Features added

Initial revision of PSF for Source only operation.

#### 2.4.4 Notes

Not Applicable

THIS DOCUMENT IS UNCONTROLLED UNLESS OTHERWISE STAMPED. It is the user's responsibility to ensure this is the latest revision prior to using or referencing this document.	Page	Spec. No.	Rev.
© Microchip Technology Inc.	7 of 7	PSF	0.95