

II. *pn* Junction

<u>Symbol</u>	<u>Description</u>	<u>Unit</u>
p_p	Hole concentration in <i>p</i> -type semiconductor (majority carrier)	cm^{-3}
n_p	Electron concentration in <i>p</i> -type semiconductor (minority carrier)	cm^{-3}
p_n	Hole concentration in <i>n</i> -type semiconductor (minority carrier)	cm^{-3}
n_n	Electron concentration in <i>n</i> -type semiconductor (majority carrier)	cm^{-3}
p_{p0}	Hole concentration in <i>p</i> -type semiconductor at thermal equilibrium	cm^{-3}
n_{p0}	Electron concentration in <i>p</i> -type semiconductor at thermal equilibrium	cm^{-3}
p_{n0}	Hole concentration in <i>n</i> -type semiconductor at thermal equilibrium	cm^{-3}
n_{n0}	Electron concentration in <i>n</i> -type semiconductor at thermal equilibrium	cm^{-3}
Δp_n	Excess hole concentration in a <i>n</i> -type semiconductor (minority carrier)	cm^{-3}
Δn_p	Excess electron concentration in a <i>p</i> -type semiconductor (minority carrier)	cm^{-3}
V_o	Built-in voltage of a <i>pn</i> junction	V
W_{dep}	Depletion region width of a <i>pn</i> junction	μm
x_p	Depletion region width on the <i>p</i> -side of a <i>pn</i> junction	μm
x_n	Depletion region width on the <i>n</i> -side of a <i>pn</i> junction	μm
q_j	Charge stored in either side of the depletion region of a <i>pn</i> junction	C
ϵ_o	Permittivity of free space ($\epsilon_o = 8.854 \times 10^{-14}$ F/cm)	F/cm
ϵ_r	Relative permittivity ($\epsilon_r = 11.7$ for Si)	
ϵ_s	Permittivity of semiconductor ($\epsilon_s = \epsilon_r \epsilon_o = 11.7 \times 8.854 \times 10^{-14}$ F/cm for Si)	F/cm
V_{ZK}	Breakdown (knee) voltage of a <i>pn</i> junction	V
V_Z	Breakdown voltage of a Zener diode	V
L_p	Hole diffusion length	μm
L_n	Electron diffusion length	μm
I_S	Saturation current of a <i>pn</i> junction	A
n	Exponential factor in the current-voltage relation of a <i>pn</i> junction*	
C_j	Depletion/junction capacitance of a <i>pn</i> junction	F
C_d	Diffusion capacitance of a <i>pn</i> junction	F
m	Grading coefficient of a <i>pn</i> junction	
i_D	Total current of a <i>pn</i> junction = $I_D + i_d$	A
v_D	Total voltage across a <i>pn</i> junction = $V_D + v_d$	V
I_D	dc current of a <i>pn</i> junction	A
V_D	dc voltage of a <i>pn</i> junction	V
i_d	small-signal ac current of a <i>pn</i> junction	A
v_d	small-signal ac voltage across a <i>pn</i> junction [#]	V
V_{DO}	Turn-on (knee) voltage of the piecewise-linear model of a <i>pn</i> junction	V
r_D	Resistance of the piecewise-linear model of a <i>pn</i> junction	Ω
r_d	Small-signal resistance of a <i>pn</i> junction	Ω
r_s	Series resistance of the neutral regions of a <i>pn</i> junction	Ω

* n is also used to denote electron concentration

[#] v_d is also used to denote the drift velocity of charge carriers