entry:
 %retval = alloca i32, align 4
 %secretBranch = alloca i32, align 4
 %i = alloca i32, align 4
 %result = alloca i32, align 4
 store i32 0, i32* %retval, align 4
 %call = call i64 @time(i64* null) #6, !dbg !257
 %conv = trunc i64 %call to i32, !dbg !257
 call void @srand(i32 %conv) #6, !dbg !258
 call void @llvm.dbg.declare(metadata i32* %secretBranch, metadata !259,
 ... metadata !DIExpression()), !dbg !260
 %call1 = call i32 @rand() #6, !dbg !261
 %rem = srem i32 %call1, 3, !dbg !262
 store i32 %rem, i32* %secretBranch, align 4, !dbg !260
 call void @llvm.dbg.declare(metadata i32* %i, metadata !263, metadata
 ... !DIExpression()), !dbg !265
 store i32 0, i32* %i, align 4, !dbg !265
 br label %for.cond, !dbg !266

for.cond:
 %0 = load i32, i32* %i, align 4, !dbg !267
 %cmp = icmp slt i32 %0, 1000000, !dbg !269
 br i1 %cmp, label %for.body, label %for.end, !dbg !270

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for.body:
 call void @llvm.dbg.declare(metadata i32* %result, metadata !271, metadata
 ... !DIExpression()), !dbg !273
 %1 = load i32, i32* %secretBranch, align 4, !dbg !274
 %2 = load i32, i32* %i, align 4, !dbg !275
 %call2 = call i32 @_Z12targetBranchii(i32 %1, i32 %2), !dbg !276
 store i32 %call2, i32* %result, align 4, !dbg !273
 br label %for.inc, !dbg !277

for.end:
 ret i32 0, !dbg !283

for.inc:
 %3 = load i32, i32* %i, align 4, !dbg !278
 %inc = add nsw i32 %3, 1, !dbg !278
 store i32 %inc, i32* %i, align 4, !dbg !278
 br label %for.cond, !dbg !279, !llvm.loop !280

CFG for 'main' function