Test Case 1:

Input:

Assign_profs(5,10,5,5)

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5

FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC3 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6

FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele6 FDele6 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele6 HDele6

FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6

FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6

FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6 FDCDC1 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC5 HDele1 HDele2 HDele3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele6 HDele6

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6 FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6 FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele3 HDele4 HDele5 HDele6

FDCDC1 FDCDC3 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDCDC5 FDCDC6 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele8 FDELe6 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDELe6 FDELe6 FDELe6 FDELe6 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDELe6 FDELe6 FDELe6 FDELe6 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDELe6 FDELe6 FDELe6 FDELe6 FDELe6 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDELe6 FDCDC6 FDC

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele2 HDele3 HDele4 HDele6 HDele6

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele3 HDele4 HDele5 HDele6

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele4 HDele5 HDele6

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele5 HDele6

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'HDCDC5', 'FDCDC1'], 'Prof 17': ['FDCDC2', 'FDCDC3'], 'Prof 18': ['FDCDC3', 'FDCDC4', 'FDCDC4'], 'Prof 19':
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'HDCDC5', '"], 'Prof 17': ['", 'FDCDC6', 'FDCDC4'], 'Prof 18': ['FDCDC4', 'FDCDC1'], 'Prof 19': ['FDCDC2',
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[", ", 'FDCDC1'], 'Prof 20': ['FDCDC1', 'FDCDC2', 'FDCDC2']}}

Test Case 2:

Input: assign_profs(10,10,10,10)

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5

FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele6 FDele6 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele6 HDele6

FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 F

FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6

FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6

FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6 FDCDC1 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC10 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC5 HDele1 HDele2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6 FDele6 F

FDCDC1 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC10 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele6 HDele6

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6 FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6 FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele2 HDele3 HDele4 HDele6 HDele6

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele3 HDele4 HDele6

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele4 HDele5 HDele6

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele5 HDele6 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele2 HDele3 HDele4 HDele5 HDele6 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6

FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6

FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele2 FDele3 FDele4 FDele5 FDele6 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele2 HDele3 HDele4 HDele5 HDele6 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6

FDCDC1 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC10 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5

FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC10 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele1 HDele2 HDele3 HDele4 HDele5 HDele6

FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDCDC10 FDele2 FDele3 FDele4 FDele5 FDele6 FDele6 FDele7 FDele8 FDele9 FDele10 HDCDC2 HDCDC3 HDCDC4 HDCDC5 HDele2 HDele3 HDele4 HDele5 HDele6 FDCDC1 FDCDC2 FDCDC3 FDCDC4 FDCDC5 FDCDC6 FDCDC7 FDCDC8 FDCDC9 FDele1 FDele2 FDele3 FDele4 FDele5 FDele6 FDele7 FDele8 FDele9 HDCDC1 HDCDC2 HDCDC3 HDCDC4 HDele1 HDele2 HDele3 HDele4

Output

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'Prof 28': ['FDCDC6', 'FDCDC5', 'FDCDC5'], 'Prof 29': ['HDCDC2', 'HDele4', 'HDele4'], 'Prof 30': ['FDele2', 'FDele2',
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'FDele9', 'FDele9'], 'Prof 25': ['FDele2', 'FDele2', 'HDele6'], 'Prof 26': ['FDele7', 'FDele6', 'FDele6'], 'Prof 27':
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{'x1': {'Prof 1': ['FDCDC1'], 'Prof 2': ['FDele1'], 'Prof 3': ['FDele1'], 'Prof 4': ['HDele1'], 'Prof 5': ['HDele1'], 'Prof 6':
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Crash Tests

Crash 1: If the number of courses that the professors can take is less than number of CDS, the matrix can't be formed and the code crashes.

Example:

Input: assign_profs(1,1,1,1)

FDCDC1 FDCDC2 FDCDC3 FDCDC4 HDCDC1 HDCDC2

FDele1 FDele2 FDele3 FDele4 HDele1 HDele2

FDCDC1 FDCDC2 FDCDC3 FDCDC4 HDCDC1 HDCDC2 FDele1 FDele2 FDele3 FDele4 HDele1 HDele2

FDCDC2 FDCDC1 FDCDC3 FDCDC4 HDCDC1 HDCDC2 FDele1 FDele2 FDele3 FDele4 HDele1 HDele2

FDCDC3 FDCDC2 FDCDC1 FDCDC4 HDCDC1 HDCDC2 FDele1 FDele2 FDele3 FDele4 HDele1 HDele2

Error:

if M[i, j] == 0 and not covered_cols[j]: # If value is a zero that is not covered, add to count

IndexError: index 6 is out of bounds for axis 0 with size 6

Crash 2: If the number of professors is greater than the number of courses available, the code assigns some courses to more than two professors.

Example:

Input: assign profs(5,4,5,1)

FDCDC1 FDCDC2 FDCDC3 FDCDC4 HDCDC1 HDCDC2

FDele1 FDele2 FDele3 FDele4 HDele1 HDele2

FDCDC1 FDCDC2 FDCDC3 FDCDC4 HDCDC1 HDCDC2 FDele1 FDele2 FDele3 FDele4 HDele1 HDele2 FDCDC2 FDCDC3 FDCDC4 HDCDC1 HDCDC2 FDele1 FDele2 FDele3 FDele4 HDele1 HDele2 FDCDC1 FDCDC3 FDCDC4 HDCDC1 HDCDC2 FDele1 FDele2 FDele3 FDele4 HDele1 HDele2 FDCDC1 FDCDC2 FDCDC4 HDCDC1 HDCDC2 FDele1 FDele2 FDele3 FDele4 HDele1 HDele2 FDCDC1 FDCDC2 FDCDC3 HDCDC1 HDCDC2 FDele1 FDele2 FDele3 FDele4 HDele1 HDele2 FDCDC1 FDCDC2 FDCDC3 FDCDC4 HDCDC2 FDele1 FDele2 FDele3 FDele4 HDele1 HDele2 FDCDC1 FDCDC2 FDCDC3 FDCDC4 HDCDC1 HDCDC2 FDCDC3 FDCDC4 HDCDC1 HD

{'x1': {'Prof 1': ['FDCDC1'], 'Prof 2': ['HDCDC2'], 'Prof 3': ['FDele1'], 'Prof 4': ['FDele1'], 'Prof 5': ['FDele1']},
'x2': {'Prof 6': ['FDele1', 'HDCDC2'], 'Prof 7': ['FDele2', 'FDele2'], 'Prof 8': ['FDele2', 'FDele2'], 'Prof 9':
['FDele3', 'FDele3']}, 'x3': {'Prof 10': ['FDele4', 'FDele4', 'HDele1'], 'Prof 11': ['HDele1', 'HDele2', 'HDele2'],
'Prof 12': ['FDCDC1', 'FDCDC2', 'FDCDC2'], 'Prof 13': ['FDCDC3', 'FDCDC3', 'FDCDC4'], 'Prof 14': ['FDCDC4',
'HDCDC1', 'HDCDC1']}}

Here, FDele1 gets assigned to four different professors.

Crash 3: If input contains extra blank spaces, they get considered as courses (or preferences) and can lead to crashes or empty assignments.

Example:

Output:

Input:

FDCDC1 FDCDC2 FDCDC3 FDCDC4 HDCDC1 HDCDC2

FDele1 FDele2 FDele3 FDele4 HDele1 HDele2

if M[i, j] == 0 and not covered_cols[j]: # If value is a zero that is not covered, add to count

IndexError: index 12 is out of bounds for axis 0 with size 12

Code crashes as the initial space is considered as a CDC and hence matrix can't be formed.