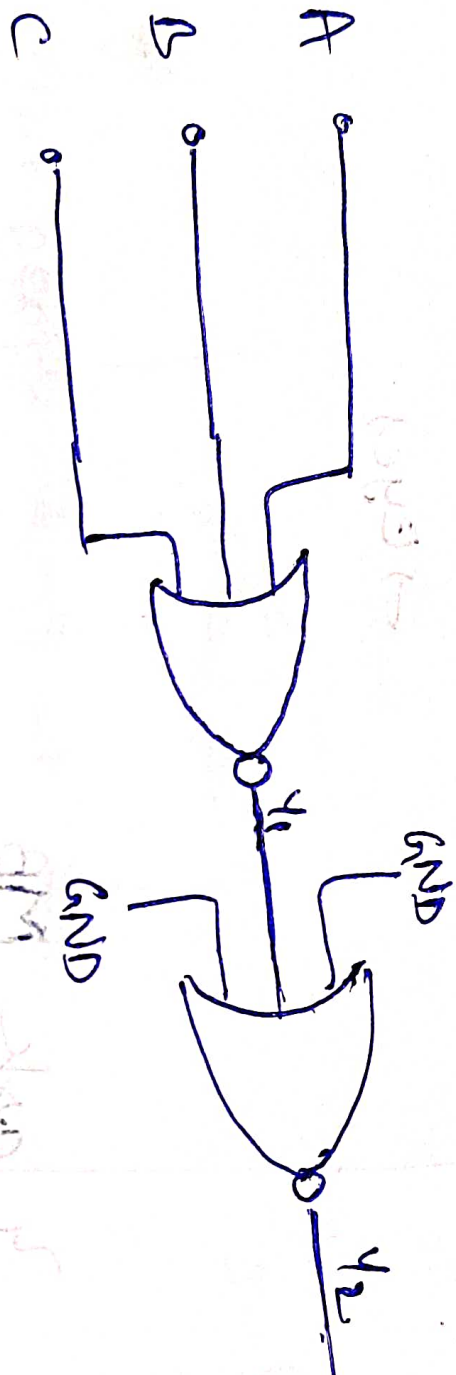


# Logic circuit



$$Y_1 = \overline{A+B+C}$$

$$Y_2 = \overline{Y_1} = \overline{\overline{A+B+C}} = A+B+C$$

Truth table

A	B	C	$\gamma_1$	$\gamma_2$
0	0	0	1	0
0	0	1	0	1
0	1	0	0	1
0	1	1	0	1
1	0	0	0	1
1	0	1	0	1
1	1	0	0	1
1	1	1	0	1