2) # include istdio.bs Hindude creggi. W chardata post\_at\_ 240803; chardala porta at \_ 00 800; charidatace - at -0+30; dolonges for (j-0; j6800; j+1) () niam biou port = 0x80; Shile (1) porta: acc; delay(); acc oxy delay (); porta= acc; delay (); acc = 0x22; posta = acc; delay (); acc = and1; porta = ace; delay();