

PDH \Rightarrow Plesynchronous is a Greek word meaning Almost Synchronous. But not fully Synchronous. In Plesynchronous System every equipment is generating its own clock of synchronization.

PDH is a technology for transporting voice OR data between multiple devices. which are working with clock sources with accepted tolerance level for synchronization. PDH was built for digital transmission of signals.

* Pulse Code Modulation (PCM) is the technique used in P.

← New Doc 2020-01-18 23.45.41



- * Pulse Code Modulation (PCM) is the technique used in PDH Networks. which is the based on TDM.
- * PDH Data was thru twisted pair, co-axial cable and microwave.

p - 3 - 9

Disadvantage of PDH ⇒

- No world standard format
- No Standardized Network Management System
- PDH Allows only Point to Point Configuration

~~- PDH does not support Hubs~~

← New Doc 2020-01-18 23.45.41

Disadvantage of PDH ⇒

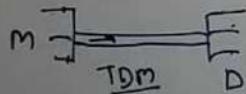
- No World Standard format
- No Standardized Network Management System
- PDH Allows only Point To Point Configuration
- PDH does not Support Hub.
- Multiplexing and Demultiplexing is very complex
- Customer Interface difficult

PDH = Plesynchronous Digital Hierarchy

SONET INTRODUCTION

SONET - Synchronous Optical Network
↳ Widely used in telephone network

- SONET is primarily used in North America
↳ Europe & Japan used modified version Synchronous Digital Hierarchy (SDH)



Synchronous, Asynchronous, Plesiochronous

→ A set of synchronous Signal, the digital transition in signal occur at same rate

SONET - Introduction

Principle of SONET/SDH:-

All signal transitions are fixed with respect to a very accurate atomic clock^(CS), called Primary Reference Clock (PRC).

- Basic Signal of SONET is synchronous transport signal, called STS-1 \Rightarrow 51.84 mbps
- SONET defines a technology for carrying signals of different capacities through a synchronous, flexible, optical hierarchy.

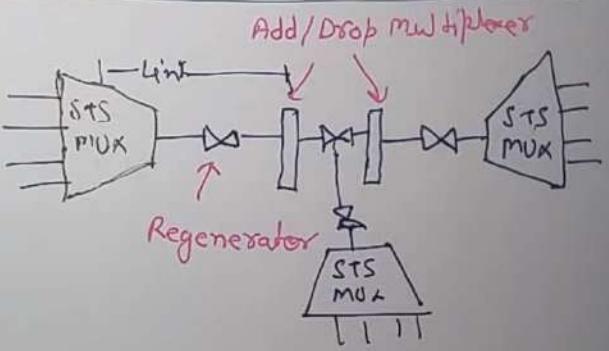
STS-1 → OC-1 → 51.84 mbls
 STS-3 → OC-3 → 155.52 mbls
 STS-12 → OC-12 → 612.08 mbls

SONET: Physical Configuration and Network Element

Three basic devices used in the SONET

- Synchronous Transport Signal (STS) multiplexer/Demultiplexer:
- Regenerator
- Add/drop multiplexer

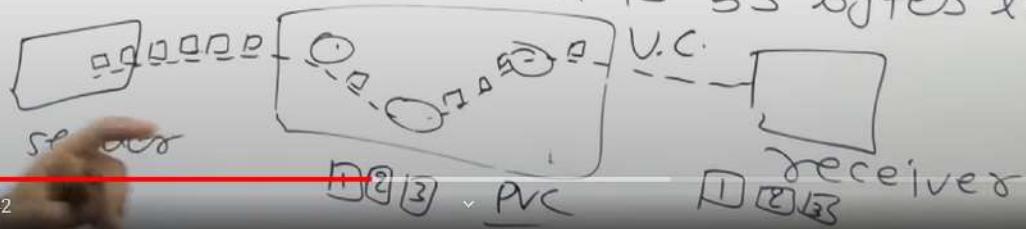
"Can add signals coming from different ~~path~~
sources into a given path or remove a signal"



- A section is optical link connecting two neighboring devices
- A link is portion of network b/w two multiplexer
- A path is end to end portion of network b/w two STS multiplexers

ATM Network [Virtual Circuit]

- ✓ It is a ITU- Telecommunication standard
- ✓ It is connection oriented Network
- ✓ It can transmit all types of data i.e. Video voice text
- ✓ Data is transmitted into fixed size packet called cell which is 53 bytes long



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The diagram illustrates the structure of an ATM cell. On the left, labeled 'sender', there is a rectangular frame containing three small circles. On the right, labeled 'receiver', there is another rectangular frame containing three small circles. Between these two frames is a larger rectangular box labeled 'V.C.' (Virtual Channel). Inside this box, there are three small circles arranged horizontally, representing the payload of the cell. Above the 'V.C.' box, the number '53' is written, indicating the fixed size of the cell.
- ✓ It delivers the packets in order
- ✓ It operates at 155Mbps & 622Mbps

2.4 ATM NETWORK

Asynchronous Transfer Mode (ATM) is a form of data transmission that allows voice, video and data to be sent along the same network. In contrast to ATM, in the past, voice, video and data were transferred using separate networks. For example, voice was transmitted over the phone, video over cable networks and data over an internet work. ATM has its similarities with the frame relay, particularly in the term of data unit size, frame relay used a variable length data unit called frame. On the contrary, ATM used fixed data unit named as "cell", we can say ATM as Cell-Relay in analogy to frame relay.

ATM was emerged as a viable technology for effective transmission of voice, video and data. Some of its features are:

- ATM is a packet network like X.25, frame relay.
- ATM integrates all different types of traffic into one network.
- ATM supports multiplexing of multiple logical connections over single physical channel.
- ATM does not provide flow Control and error control at data link layer.
- ATM can serve as a LAN or WAN backbone without requiring any network replacement.
- ATM can be used in existing physical channels and networks. Because ATM was developed to have such a wide range of compatibility with existing networks, its implementation does not require replacement or over-building of telephone, data or cable networks. It is also compatible with wireless and satellite communications.



As we had already discussed that ATM used a fixed size data unit called cell. As packet size is one of the key issues for protocol design, we would like to discuss the reasons for deciding the cell size. First let's assume a situation of using large packet size.

Large packets are better in a sense that they use less number of headers for data transfer. So, large packets may cause less overhead in a network. Another, important point is if a we are using a large size packet, than sometime the system has to wait till the packet is completely filled before sending any data. Remember the data sending requirement are not same at all time. Just to solve this problem, we can use variable size packet for different type of data. For example, Voice traffic can be sent in small packet and data traffic into large packet. But the variable size packet may increase additional Complexity such that variable packet size can leads to starvation problem for small packets.

The team of ATM designer had discarded the idea of both large packet and variable packets, and agreed for a fixed size data unit of 53 bytes (a 5-byte cell header and 48 bytes of data), which can achieve both higher data rate and less transmission delay. What was so special about '48 bytes'? Some people say that US telecommunication organizations wants 64 bytes Cell but the Europeans and Japanese telecommunication organizations want 32 bytes Cell. So as a compromise, 48 byte was decided.

5 Byte Header	48 bytes Data Unit
---------------	--------------------

Figure 3: ATM Cell

We have various advantages of using fixed size small Cell, like it reduced queuing delay for a high priority cell. This concept simplifies the implementation of switching mechanism in hardware. The fixed cell size ensures that time-critical information such as voice or video is not adversely affected by long data frames or packets. Also, the cell header is organized for efficient switching, virtual-circuit identifiers and header error checks.

ATM cell has two formats for user to network interface and network to network



Figure 3: ATM Cell

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ATM cell has two formats for user to network interface and network to network interface as shown in the Figure 4:

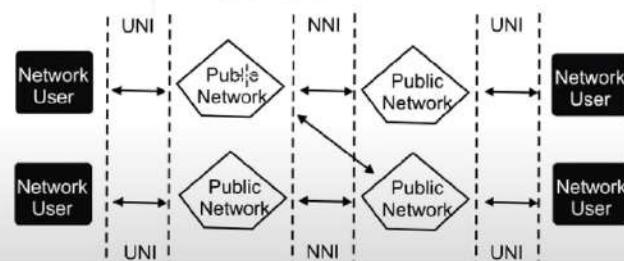


Figure 4: UNI and NNI of ATM

The Header Format

The structure of the header is different in UNI and NNI. In the network-network interface, the virtual path identifier field is expanded from 8 to 12 bits.

ATM Network In Hindi | ATM In Computer Network | ATM Network | Asynchronous Transfer Mode | BCS041

The video player interface shows a man in a yellow shirt speaking. Overlaid on the video are two diagrams of ATM cell headers.

Figure 3: User-network Interface

8	7	6	5	4	3	2	1
Generic Flow Control*				Virtual Path Identifier			
Virtual Path Identifier				Virtual Channel Identifier			
Virtual Channel Identifier							
Virtual Channel Identifier				Payload Type ID		CLP	
Header Error Control							
Hand icon							

Figure 4: Network-network interface

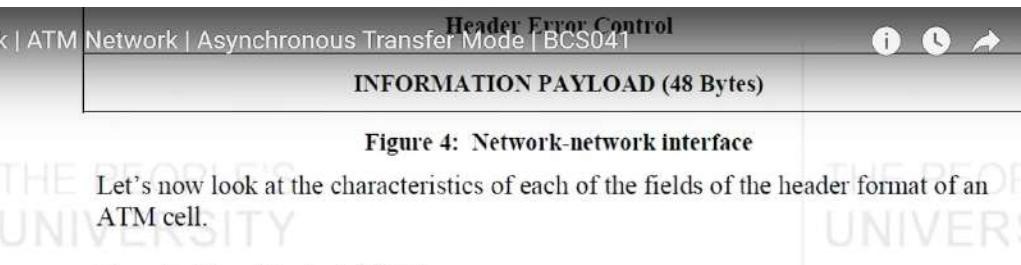
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Virtual Path Identifier							
Virtual Path Identifier				Virtual Channel Identifier			
Virtual Channel Identifier							
Virtual Channel Identifier				Payload Type ID CLP			
Header Error Control							
INFORMATION PAYLOAD (48 Bytes)							

Let's now look at the characteristics of each of the fields of the header format of an ATM cell.

Generic Flow Control (GFC)

The GFC field of the header is only defined across the NNI and does not appear in the UNI.

5:23 / 14:57



Function

- It controls the traffic flow across the UNI.

Virtual Path Identifier (VPI)

The VPI is an 8-bit field for the UNI and a 12-bit field for the NNI.

Function

- It constitutes a routing field for the network and is used to identify virtual paths. In an idle cell, the VPI is set to all 0's.
- Together with the Virtual Channel Identifier, the VPI provides a unique local identification for the transmission.

Virtual Channel Identifier (VCI)

It is a 16-bit field used to identify a virtual channel. For idle cells, the VCI is set to all 0's.

Function

- It functions as a service access point and it is used for routing to and from the end user.
 - Together with the Virtual Path Identifier, the VCI provides a unique local identification for the transmission.

Payload Type Identifier (PTI)

The PTI field indicates the type of information in the information field. The value in each of the three bits of PTI indicate different conditions.

- Bit 1 is set to 1 to identify operation, administration, or maintenance cells (i.e. anything other than data cells).
 - Bit 2 is set to 1 to indicate that congestion was experienced by a data cell in transmission and is only valid when bit 4 is set to 0.

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- Bit 3 is used to convey information between end-users.

Cell Loss Priority (CLP)

The 1-bit CLP field is used for indication of the priority of the cell. It is used to provide guidance to the network in the event of congestion. When set to value 1, it indicates that the cell may be discarded within the network when congestion occurs.

When the CLP value is set to 0, it indicates that the cell is of relatively high priority and should be discarded only in situations where no alternative is available.



- Bit 2 is set to 1 to indicate that congestion was experienced by a data cell in transmission and is only valid when bit 4 is set to 0.
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Header Error Control (HEC)

Each ATM cell includes an 8-bit HEC that is calculated based on the remaining 32 bits of the header.

Function

- It detects all single-bit errors and some multiple-bit errors. As an ATM cell is received at a switch, the HEC of the cell is compared and all cells with HEC discrepancies (errors) are discarded. Cells with single-bit errors may be subject to error correction if supported or discarded. When a cell is passed through the switch and the VPI/VCI values are altered, the HEC is recalculated for the cell prior to being passed out to the port.

ATM Layers

ATM is a connection-oriented protocol. ATM has a layered structure that is similar to



Function

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ATM Layers

ATM is a connection-oriented protocol. ATM has a layered structure that is similar to the 7-layered OSI model. However, ATM only addresses the functionality of the two lowest layers of the OSI model, i.e.

- The physical layer and
- The data link layer.

Apart from these two layers, all other layers of the OSI model are irrelevant in ATM, as these layers are only part of the encapsulated information portion of the cell which is not used by the ATM network. In ATM, three layers handle the functionality of the two lower OSI layers.

35

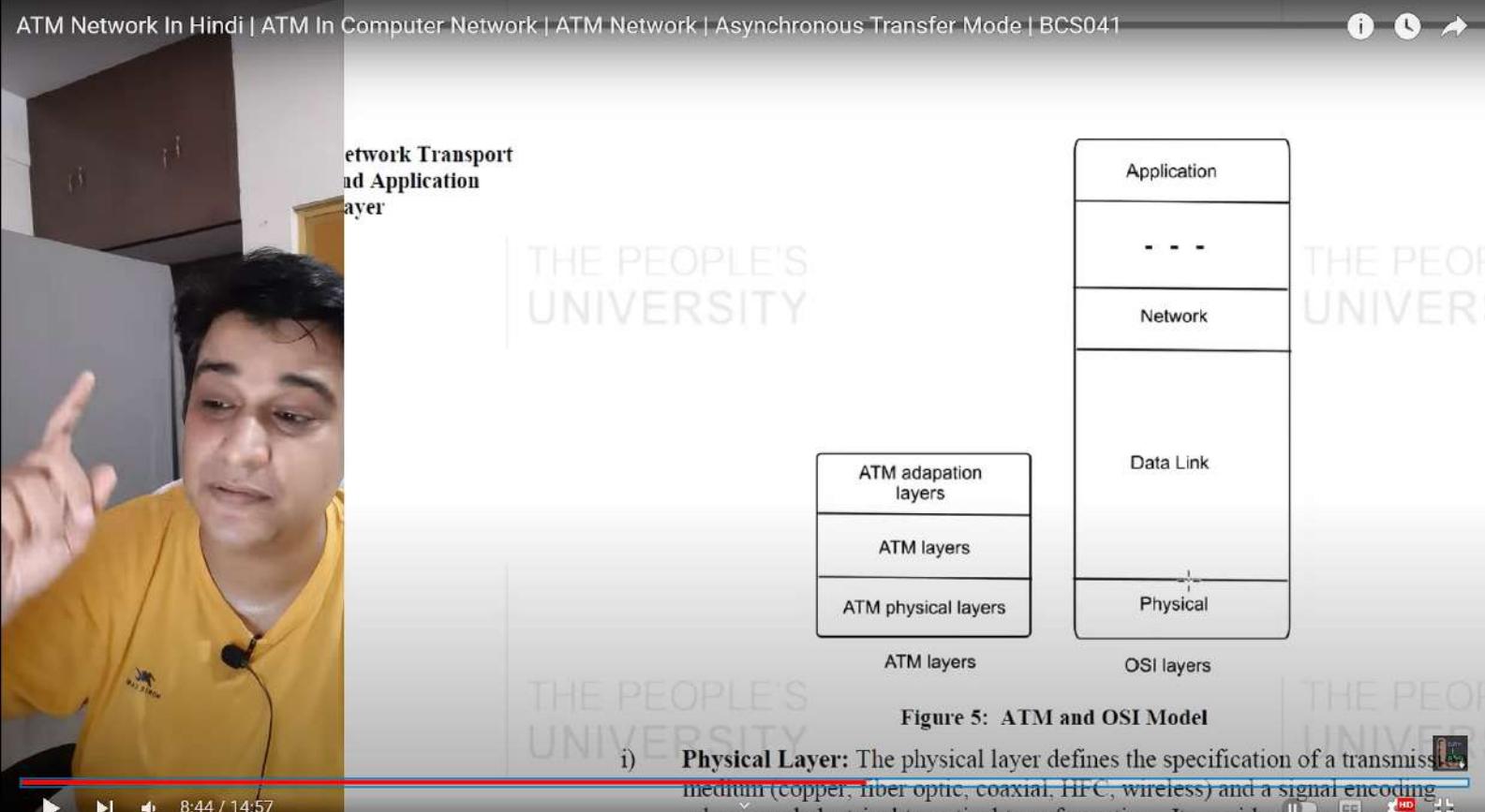


Figure 5: ATM and OSI Model



- i) **Physical Layer:** The physical layer defines the specification of a transmission medium (copper, fiber optic, coaxial, HFC, wireless) and a signal encoding scheme and electrical to optical transformation. It provides convergence with physical transport protocols such as SONET as well as the mechanism for transforming the flow of cells into a flow of bits. The ATM form has left most of the specification for this level to the implementer.
- ii) **The ATM Layer:** The ATM layer deals with cells and cell transport. It defines the layout of a cell and tells what the header fields mean. The size of a cell is 53 bytes (5 bytes of header and 48 bytes of payload). Because each cell is the same size and all are relatively small, delay and other problems with multiplexing different sized packets are avoided.

It also deals with establishment and release of virtual circuits. Congestion control is also located here. It resembles the network layer of the OSI model as it has got the characteristics of the network layer protocol of OSI model like Routing, Switching, End to end virtual circuit set up and Traffic management.

Switches in ATM provide both switching and multiplexing. A Cell format of ATM Layer are distinguished as, UNI (User Network Interface) and NNI (Network-Network Interface)

In both cases the cell consists of a 5 byte header followed by a 48 bytes payload but the two headers are slightly different.

- iii) **ATM Adaptation Layer:** The ATM Adaptation Layer (AAL) maps the higher level traffic to ATM cells. It is located between the ATM layer and the user layer.

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- iii) **ATM Adaptation Layer:** The ATM Adaptation Layer (AAL) maps the higher-level data into ATM cells to be transported over the ATM network, i.e. this layer segments the data and adds appropriate error control information as necessary. It is dependent on the type of services (voice, data etc.) being transported by the higher layer.

ATM is connection oriented and allows the user to specify the resources required on a per-connection basis (per SVC) dynamically. There are the five classes of service defined for ATM (as per ATM Forum UNI 4.0 specification).

ATM Service Classes

Introduction to Network Architecture

Quality of Service Parameter

ATM Service Classes

Service Class	Quality of Service Parameter
Constant bit rate (CBR)	CBR class is used for emulating circuit switching. The cell rate is constant with time. CBR applications are sensitive to cell-delay variation. Examples of applications that can use CBR are telephone traffic (i.e. nx64 kbps), video conferencing and television.
Variable bit rate–non-real time (VBR–NRT)	VBR-NRT class allows users to send traffic at a rate that varies with time depending on the availability of user information. Statistical multiplexing is provided to make optimum use of network resources. Multimedia e-mail is an example of VBR–NRT.
Variable bit rate–real time (VBR–RT)	This class is similar to VBR–NRT but is designed for applications that are sensitive to cell-delay variation. Examples for real-time VBR are voice with speech activity detection (SAD) and interactive compressed video.
Available bit rate (ABR)	ABR class provides rate-based flow control and is aimed at data traffic such as file transfer and e-mail. Although the standard does not require the cell transfer delay and



	Asynchronous Transfer Mode BCS041 sensitive to cell-delay variation. Examples of applications that can use CBR are telephone traffic (i.e. nx64 kbps), video conferencing and television.
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Available bit rate (ABR)	ABR class provides rate-based flow control and is aimed at data traffic such as file transfer and e-mail. Although the standard does not require the cell transfer delay and cell-loss ratio to be guaranteed or minimized, it is desirable for switches to minimize delay and loss as much as possible. Depending upon the state of congestion in the network, the source is required to control its rate. The users are allowed to declare a minimum cell rate, which is guaranteed to the connection by the network.
Unspecified bit rate (CBR)	UBR class is widely used today for TCP/IP.



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Network Transport and Application Layer

Each layer of ATM is further divided into two sublayers

- SAR (Segmentation and Reassembly)
- CS (Convergence Sublayer).

Segmentation & Reassembly: This is the lower part of the AAL. The SAR sublayer breaks packets up into cells on the transmission side and puts them back together again at the destination. It can add headers and trailers to the data units given to it by the CS to form payloads. It is basically concerned with cells.

Convergence Sublayer: The CS sublayer makes it possible to have ATM systems offer different kind of services to different applications. The CS is responsible for accepting bit streams or arbitrary length messages from the application and breaking them into units of 44 or 48 bytes for transmission.

Working of ATM

When a user sends data over the ATM network, the higher-level data unit is passed down to the Convergence Sublayer of the AAL Layer, which prepares the data for the ATM Layer according to the designated AAL protocol. The data is then passed down to the Segmentation and Reassembly Sublayer of the AAL Layer, which divides the data unit into appropriately sized segments.

These segments are then passed down to the ATM Layer, which defines an appropriate cell header for each segment and encapsulates the header and payload segment into a 53-byte ATM cell. The cells are then passed down to the Physical Layer, which then transmits the cells over the appropriate physical transmission medium being used, adding empty cells as needed.

ATM circuit connections are of two types:



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These segments are then passed down to the ATM Layer, which defines an appropriate cell header for each segment and encapsulates the header and payload segment into a 53-byte ATM cell. The cells are then passed down to the Physical Layer, which streams the cells at an appropriate pace for the transmission medium being used, adding empty cells as needed.

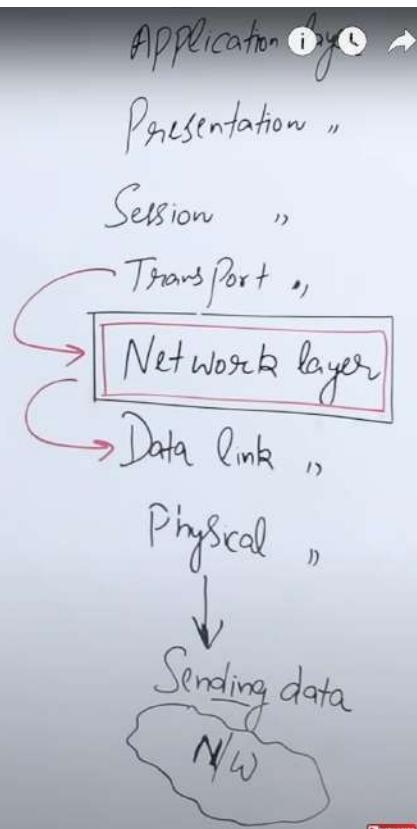
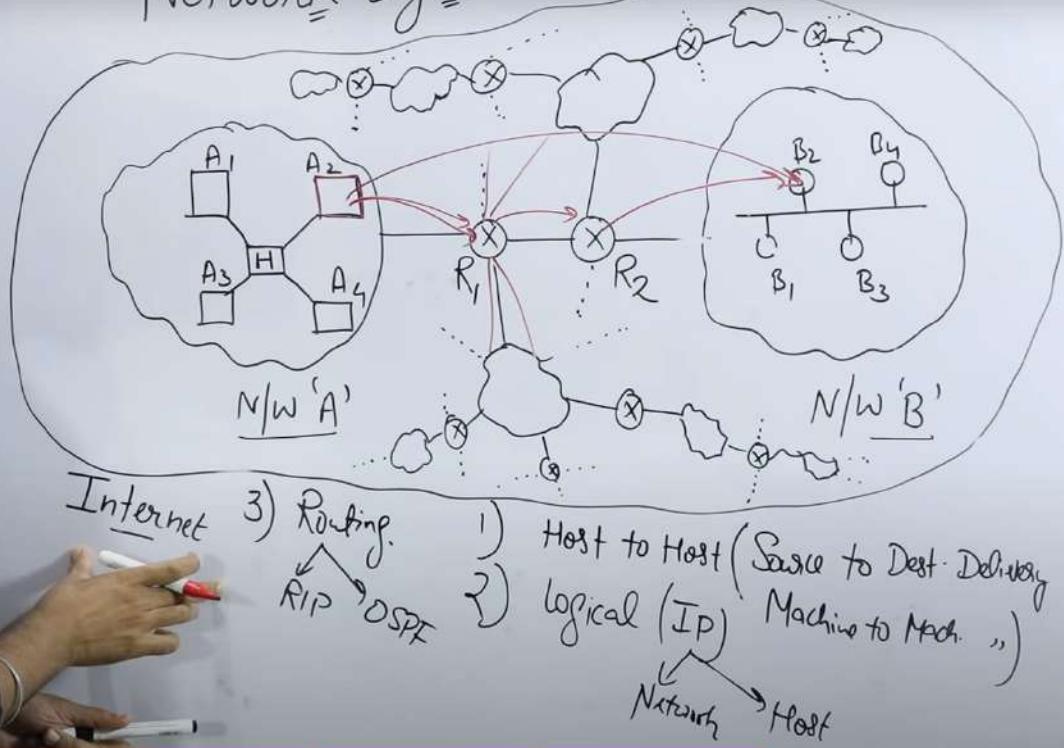
ATM circuit connections are of two types:

1. Virtual Paths and
2. Virtual Channels.

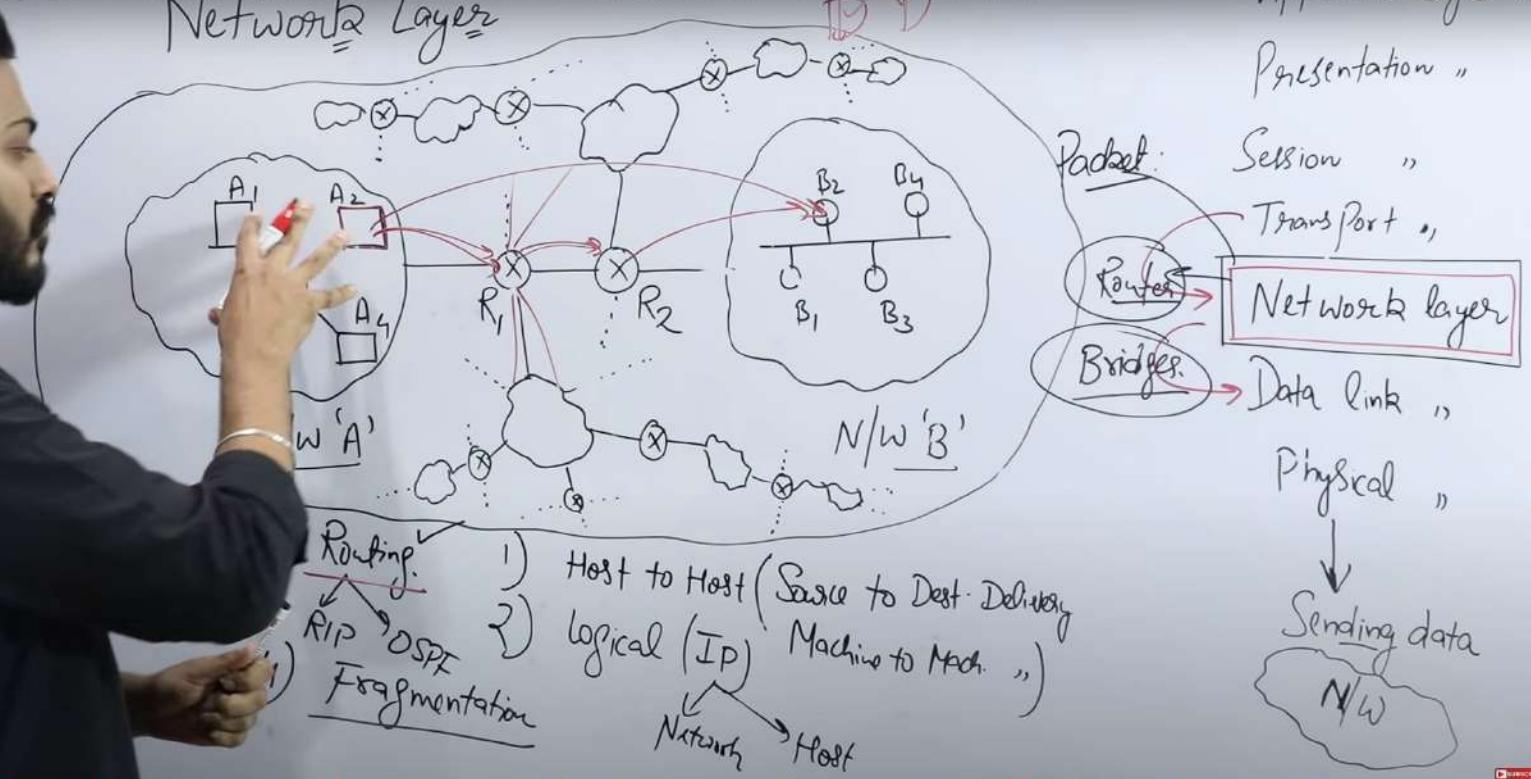
A virtual channel is a unidirectional pipe made up from the concatenation of a sequence of connection elements. A virtual path **consists of a set of these virtual channels**. Each virtual channel and virtual path has an identifier associated with it. Virtual path is identified by Virtual Path Identifiers (VPI) and a virtual channel is identified by a Virtual Channel Identifier (VCI). All channels within a single path **must have distinct channel identifiers but may have the same channel identifier as channels in different virtual paths**.



Network Layer



Network Layer



Application layer
Presentation ..

Session ..

Transport ..

Network layer

Router

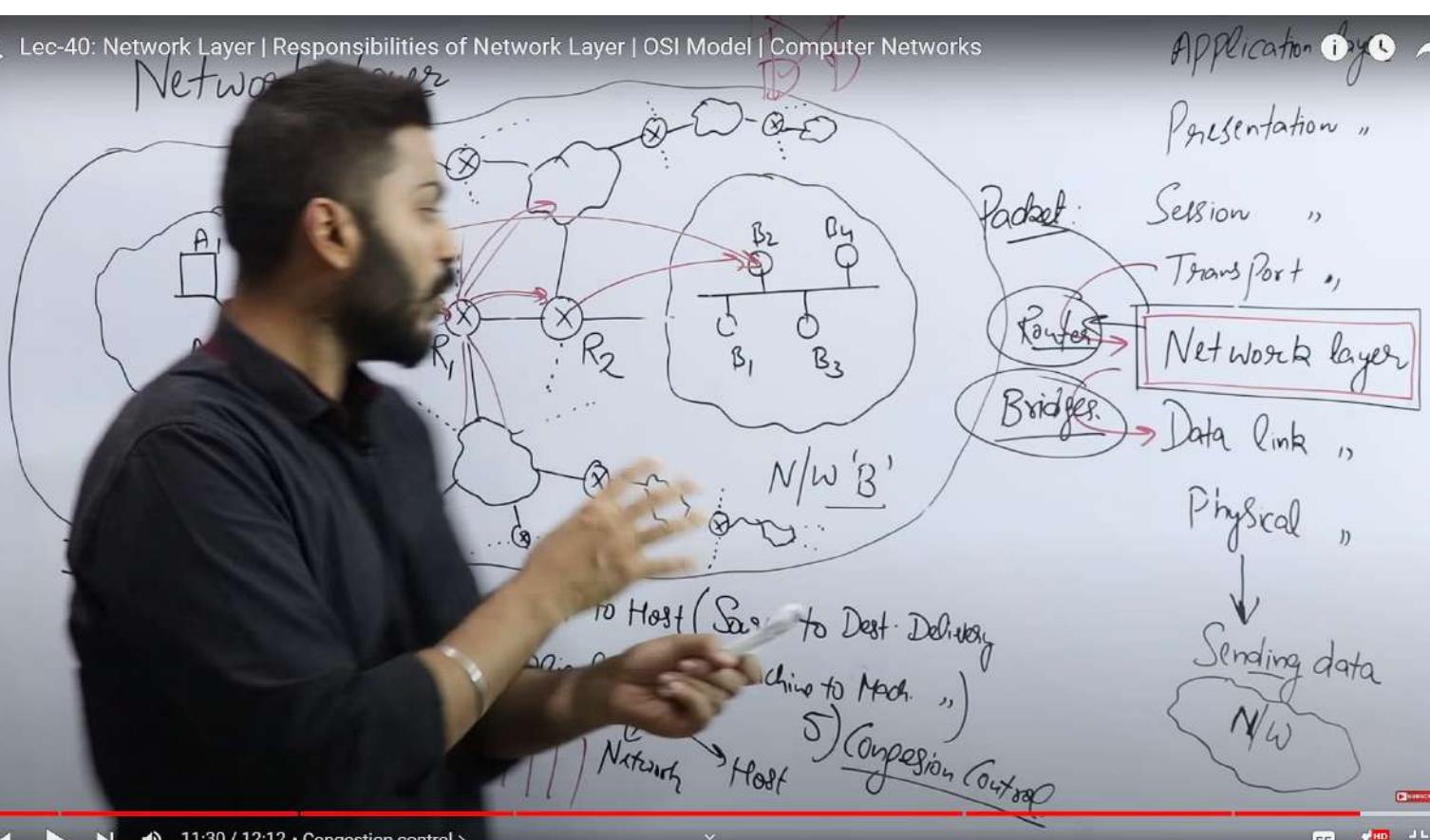
Bridges

Data link ..

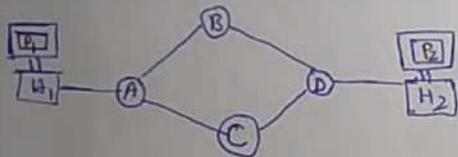
Physical ..

Sending data

N/W



≡ NETWORK LAYER DESIGN ISSUES *issue*



Designer of Network Layer must grapple with

- Service provided to Transport Layer
- Internal design of network

- Store and forward Packet switching

- = Services provided to the Transport Layer
Network Layer provides services to Transport Layer.

The services need to be carefully designed with following goals:

- Services should be independent of router technology
- Transport Layer should be shielded from the number, type and topology of router
- Network addresses made available to transport layer should use uniform numbering plan

Implementation of Connectionless services

- Datagram and Datagram Network

- Implementation of Connection-oriented Services
 - Virtual circuit

Routing Algorithm

⇒ The main function of N/w layer.

* Routing

* Congestion Control

* Internetworking



Routing

It is the process of forwarding a pkt in a n/w so that it reaches its intended destination.

Routing Algorithm

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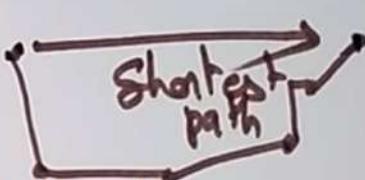
* Congestion Control □

* Internetworking

Routing

It is the process of forwarding of a pkt in a n/w. So that it reaches its intended destination.

Main goal of routing algorithms:

- ① Correctness: routing should be done correctly
- ② Simplicity: routing should be done in a simple manner.

- ③ Robustness: The ability to withstand the sys over a years.
- ④ Stability: routing alg should be stable under all circumstances

→ Adaptive Routing (dynamic)

→ Non Adaptive Routing (static)

changes routes dynamically

* gathers info at runtime

- locally

- from adjacent routers

- from all other routers

* changes routes

- every delta T seconds

- When load changes,

- When topology changes.

→ Adaptive Routing (dynamic)

- * changes routes dynamically
- * gathers info at runtime
 - locally
 - from adjacent routers
 - from all other routers
- * changes routes
 - every delta T seconds
 - when load changes

→ Non Adaptive Routing (static)

choice of route is computed in advance, offline & downloaded to the routers when the n/w is booted.

① Static Routing Algorithm.

- shortest path routing
- Flooding
- Flow based routing



② Dynamic Routing Algorithm.

- Distance Vector routing
- Link State routing

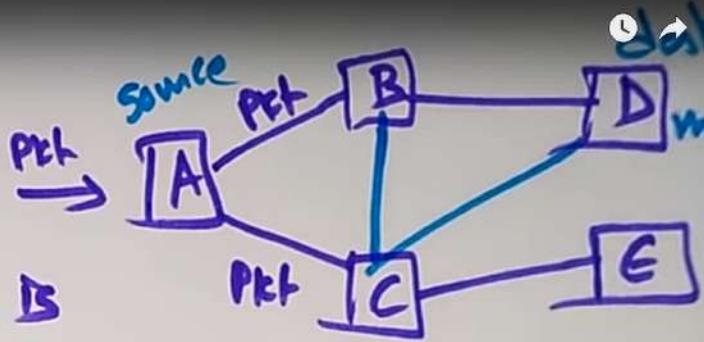
③ Hierarchical routing

⑤ Broadcast routing

④ Routing for mobile Hosts

⑥ Multicast routing

Flooding



Every incoming pkt is

sent out on every outgoing line
except the one it arrived on.

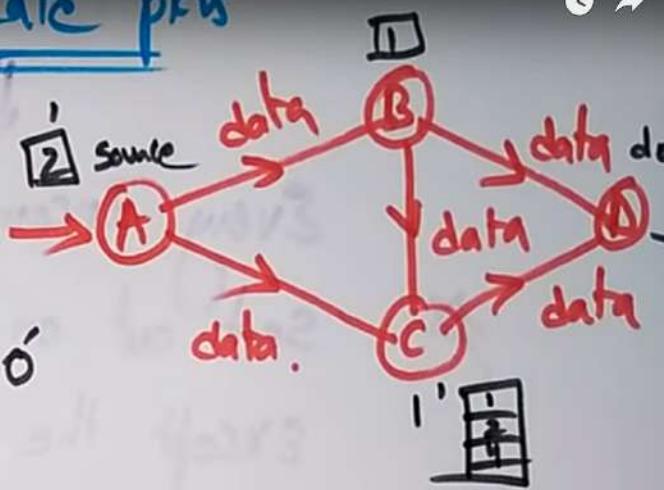


Many no: of duplicate pkts are generated.

→ flooding routing algorithm | data Communication | bhanu priya

① Using a hop counter

- decrement in each counter
 - discard pkt if counter is -

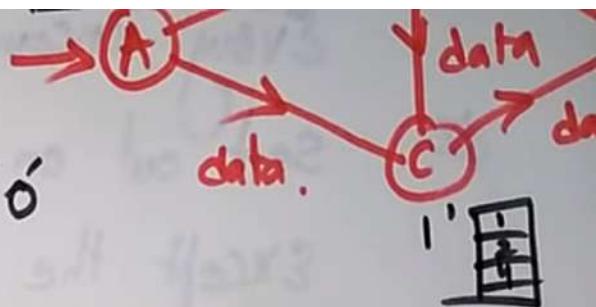


⑦ Sequence no: In pk

- avoid sending the same pkt second time
 - keep in each router per source a list of pkts already seen

① Counting

- decrement in each router
- discard pkt if counter is '0'



② Sequence no: in pkt

- avoid sending the same pkt second time
- keep in each router per source a list of pkts already seen

③ Selective Flooding:

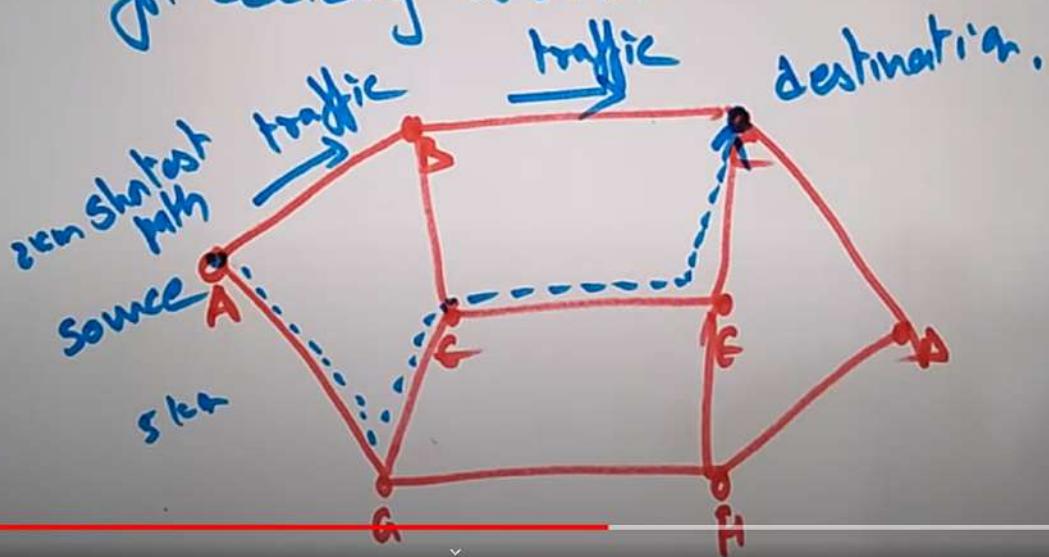
- use only those lines that are going approximately in right direction

Flow Based Routing (Static)



This is a static alg which uses
① topology & load condition (traffic)
in deciding a route.

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① topology & load condition (traffic)
in deciding a route.

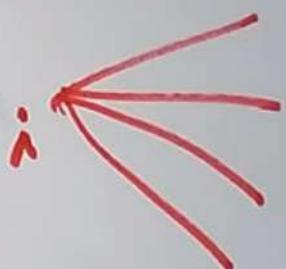


To use the technique of flow based routing
the following info should be known in advance.

1. Subnet topology

2. Traffic matrix

3. Line capacity matrix





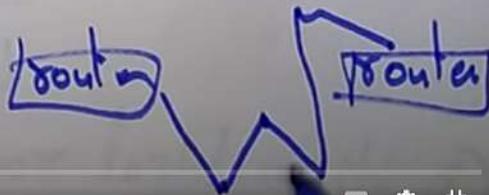
* Shortest path

* Flooding

* Flow based routing

Shortest path routing Algorithm

Finds the shortest path b/w a given pair of routers.



The cost of link may be a function of

- distance
- bandwidth
- avg traffic
- communication cost
- delay etc.,

Lec-63: Transport Layer | Responsibilities of Transport Layer | OSI Model | Computer Networks

Application layer

Presentation ..

Session ..

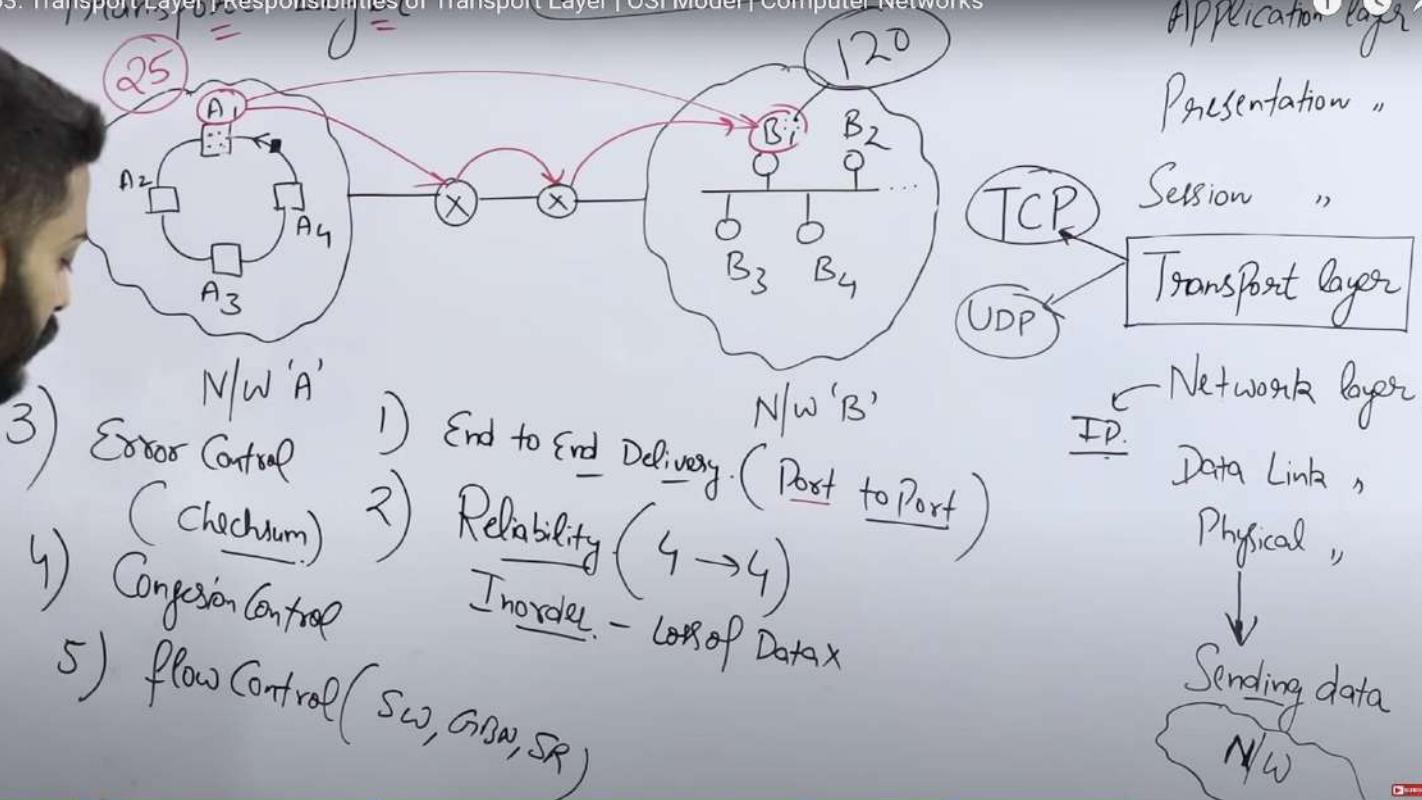
Transport layer

Network layer

Data Link ,
Physical ,

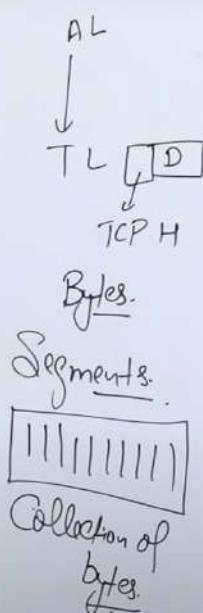
Sending data

N/W



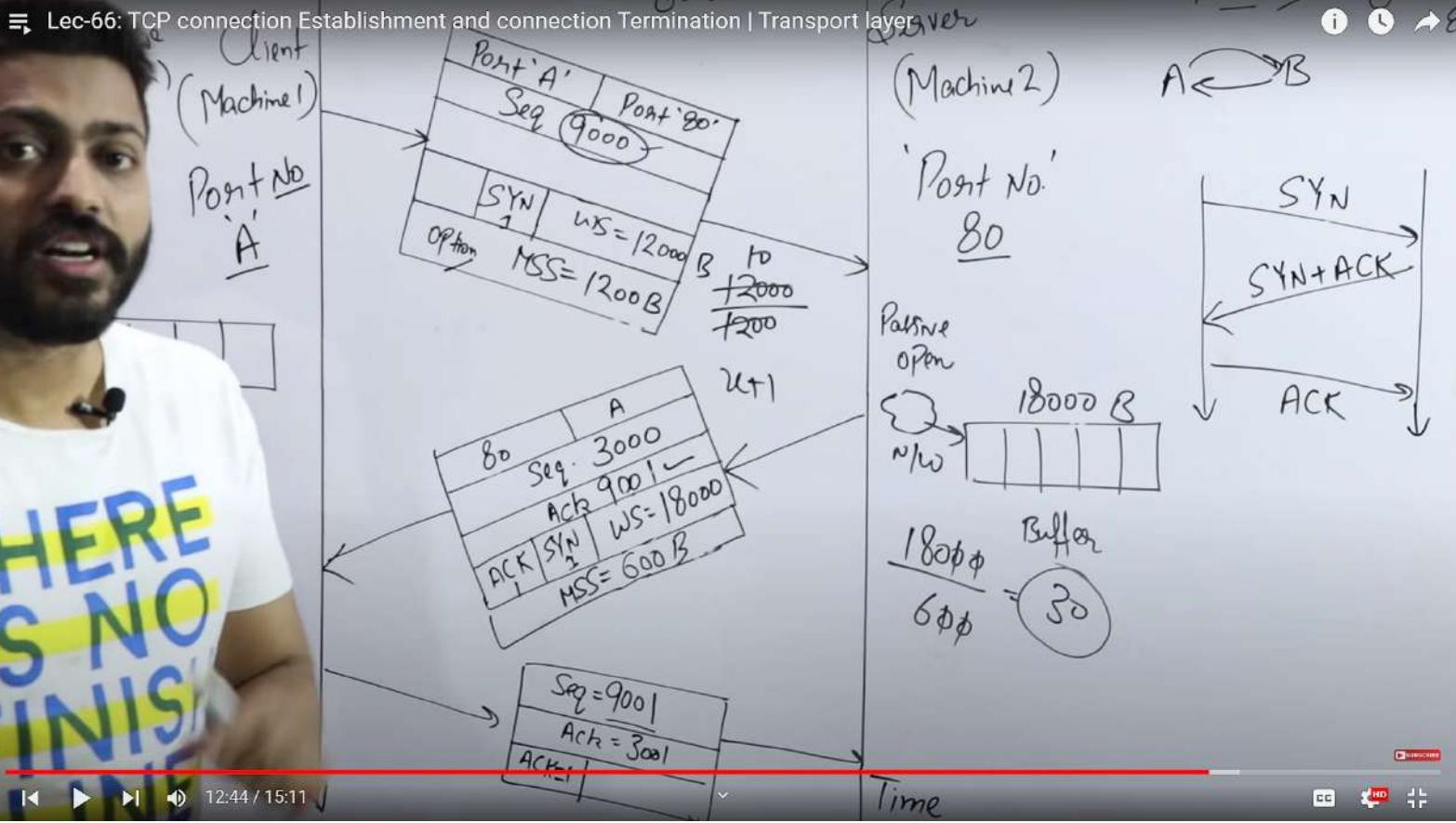
Source Port	16 Bit	Destination Port	16 bit
Sequence Number			32 bit
Acknowledge No.			32 bit
HLEN	4 bit	URG	A P R S F
4 bit	6	R C S S Y I	WINDOW Size 16 bit
G K H T N N			
Checksum	16 bit	16 bit	URGENT Pointer
Options & Padding		40 Bytes	

TCP Header (20-60 B)

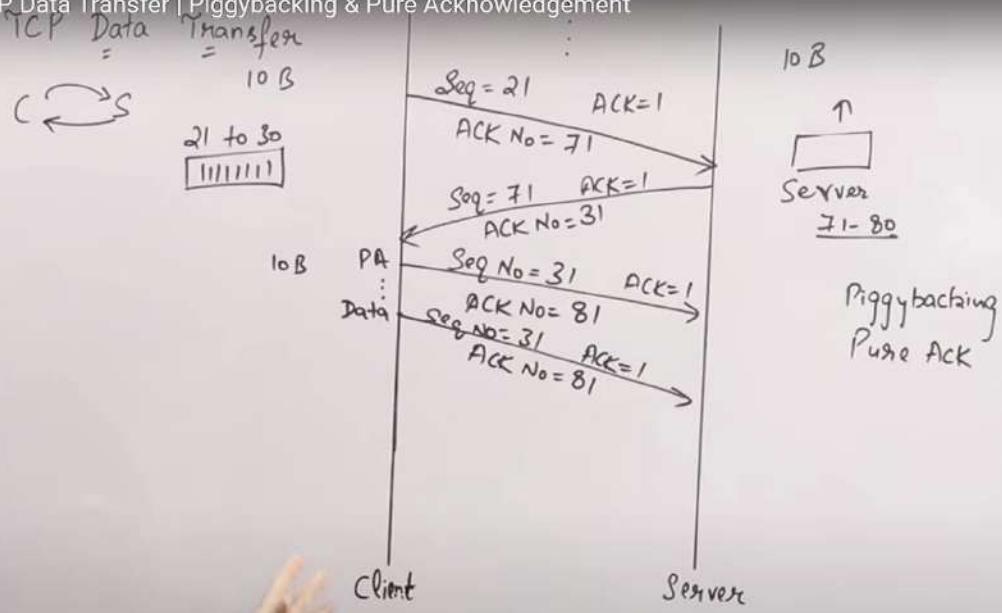


TCP (Transmission Control Protocol)

- Byte Streaming
- Connection oriented — 3 way
- Reliability
- Full Duplex
- Piggybacking
- GBN
- SR.
- Error Control
- Flow Control
- Congestion Control
- N/W

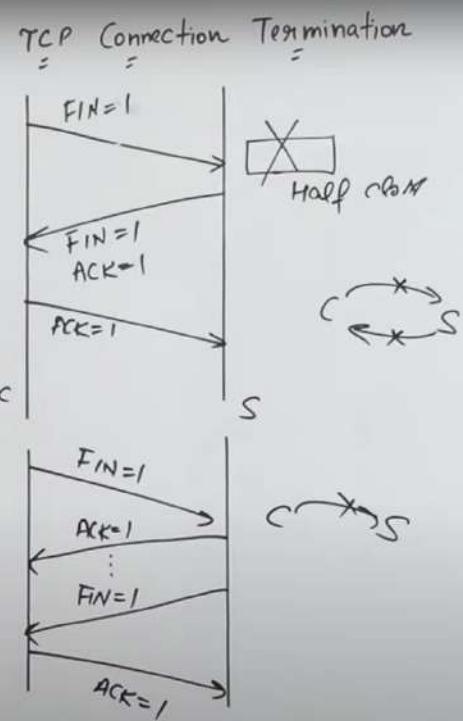
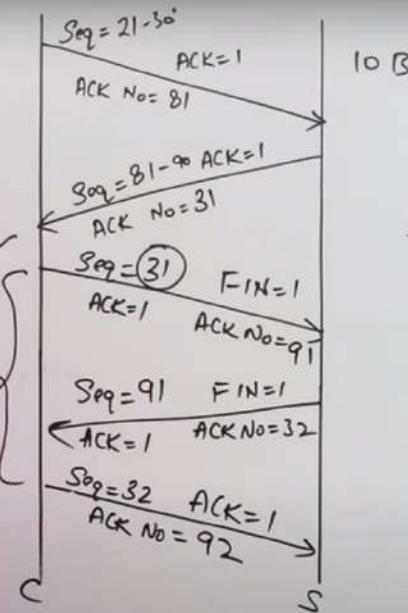
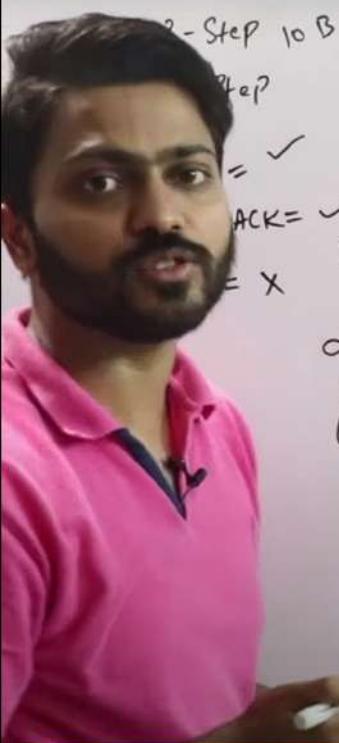


→ Lec-67: TCP Data Transfer | Piggybacking & Pure Acknowledgement





Lec-68: Connection Termination in TCP in Hindi with example



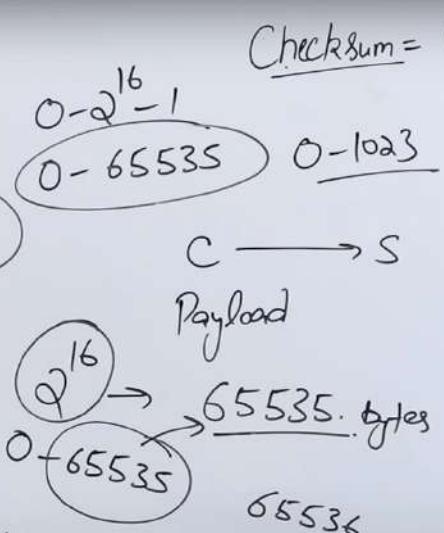
Lec-70: UDP (User Datagram Protocol) header in Computer Networks in Hindi



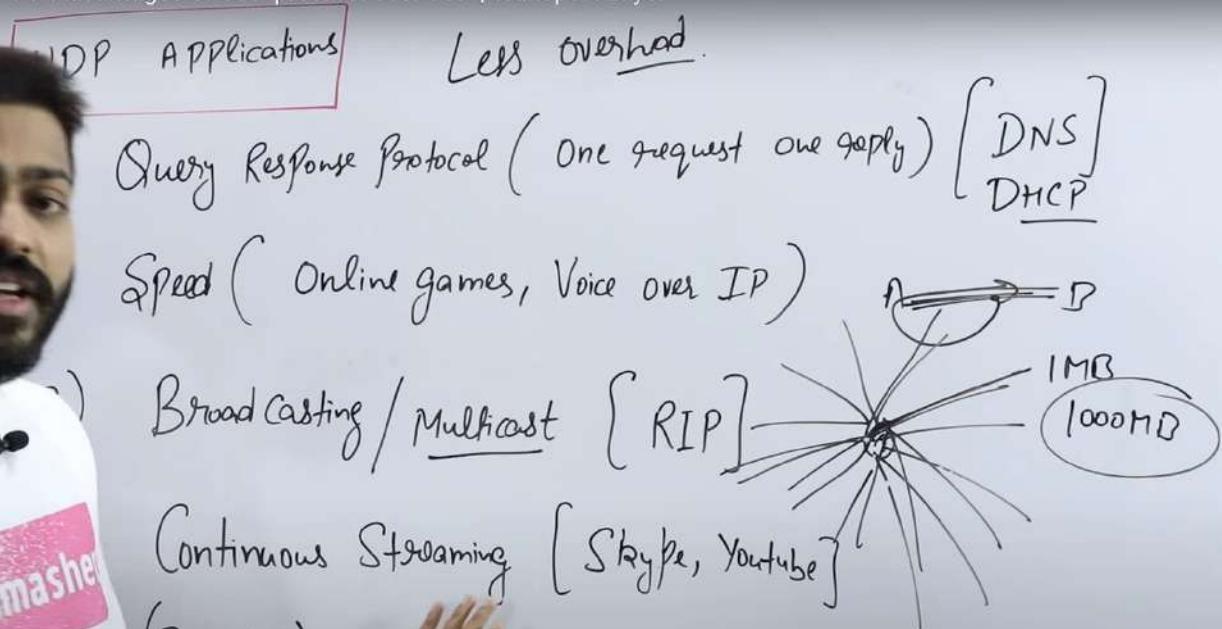
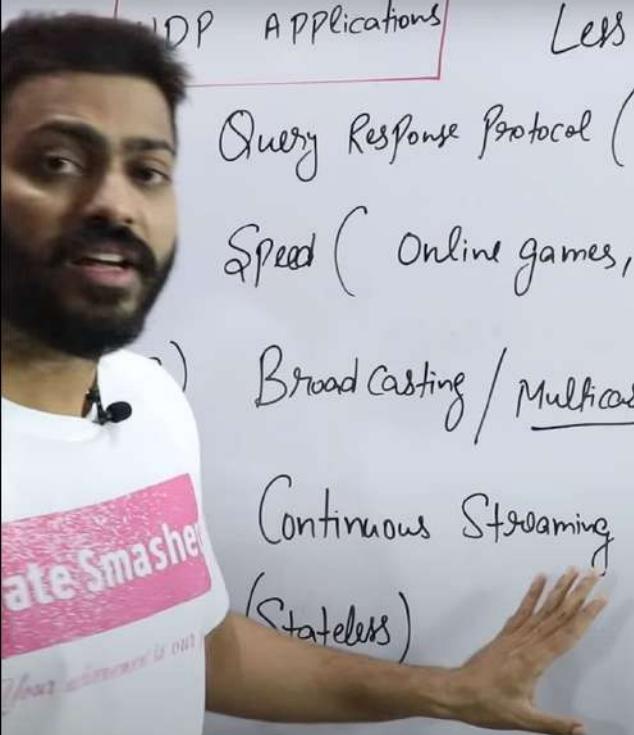
Source Port 16	Destination Port 16
Length 16	Checksum

UDP Header
(8 B fixed)

Connection loss (Unreliable)
→ No ordered



$$4. \quad 2^4 = 16(0-15)$$
$$1111 = 15$$



TCP

Connection oriented
Reliable ordering.

Error Control is mandatory

Low transmission

More overhead ($20-60 B$)
Flow control, Congestion control

Gate Smasher

Your achievement is outstanding

UDP

1) Connection less

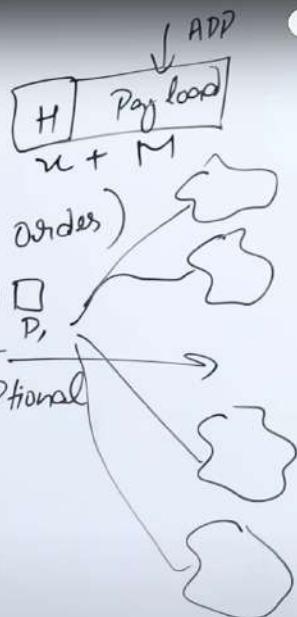
2) Less Reliable

3) Error Control is optional

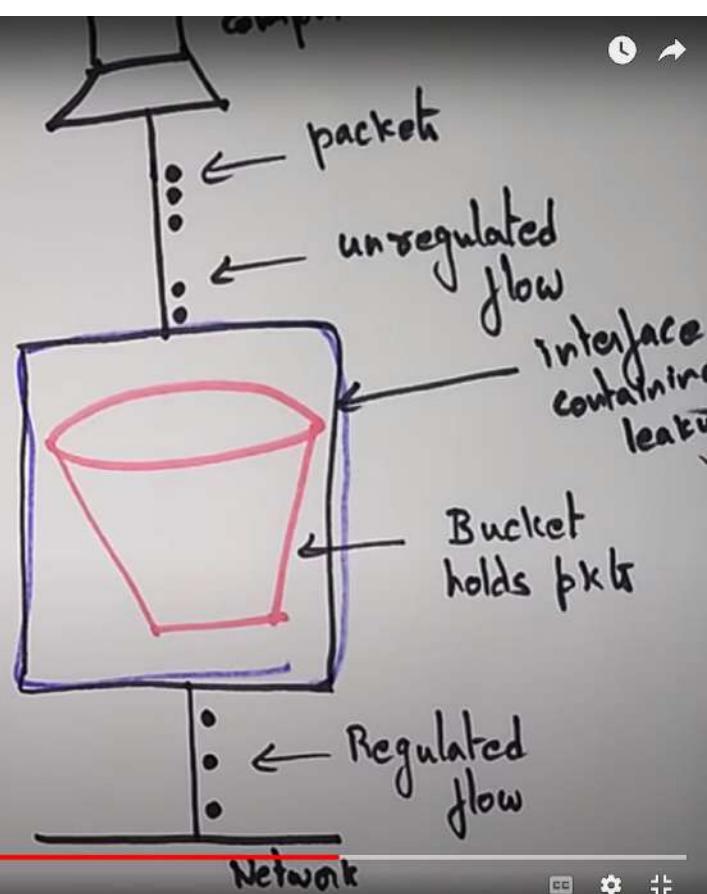
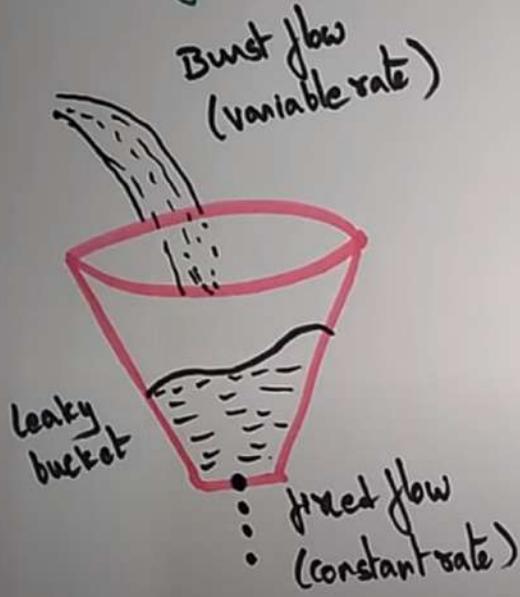
4) Fast transmission

5) Less overhead ($8 B$)

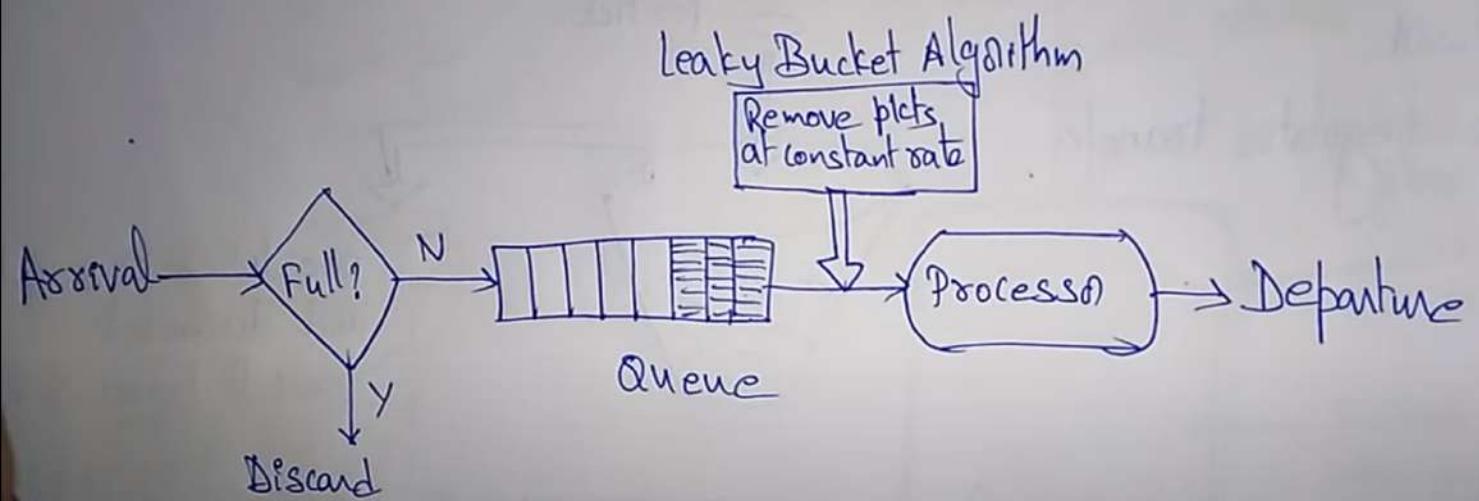
6) No FC, CC



Bucket Algorithm:



Leaky Bucket Implementation



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Quality of Service:- In QoS, we try to create an appropriate environment for the traffic.

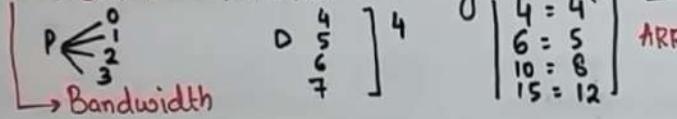
Flow characteristics:-

→ Reliability → Lack of Reliability means losing or acknowledgment.

→ Delay → Source to destination delay

$$S_0 \longrightarrow D_0 [4]$$

→ Jitter → Variation in Delay.

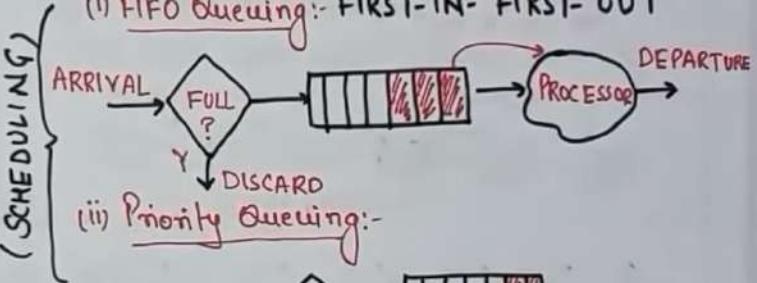


→ Bandwidth

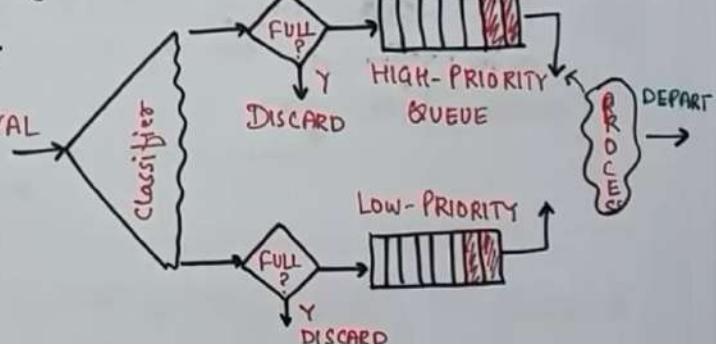
Different Appln Requires diff. Bandwidth.

Techniques to Improve QoS:-

(i) FIFO Queuing:- FIRST-IN- FIRST-OUT



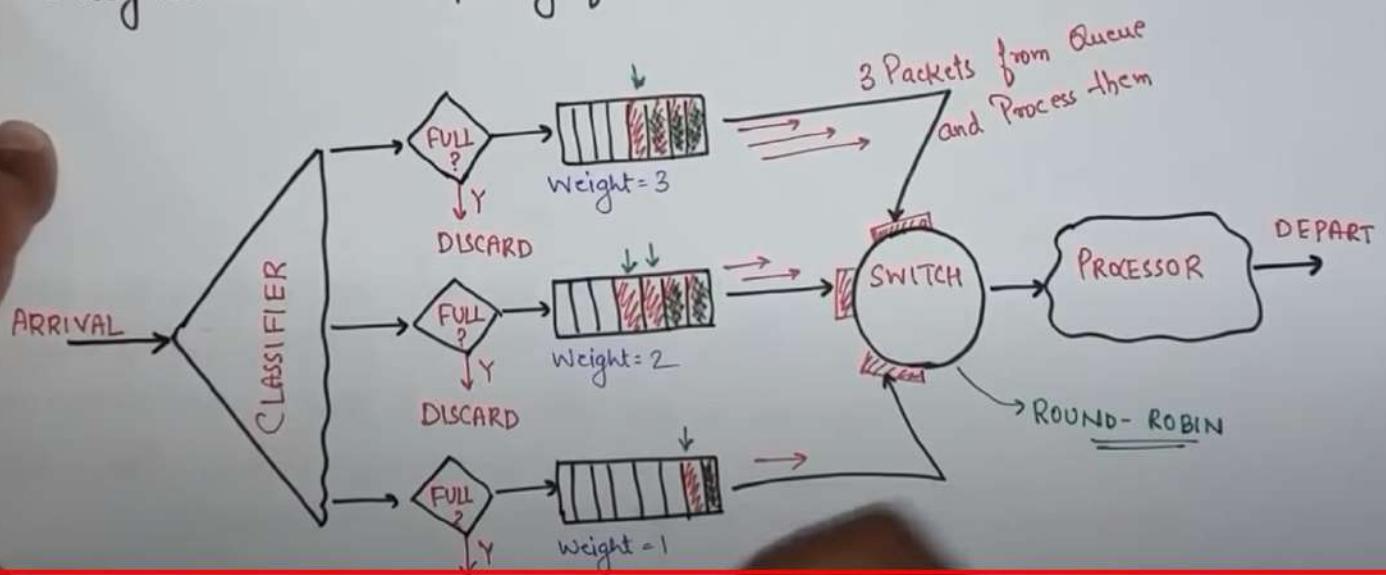
(ii) Priority Queuing:-



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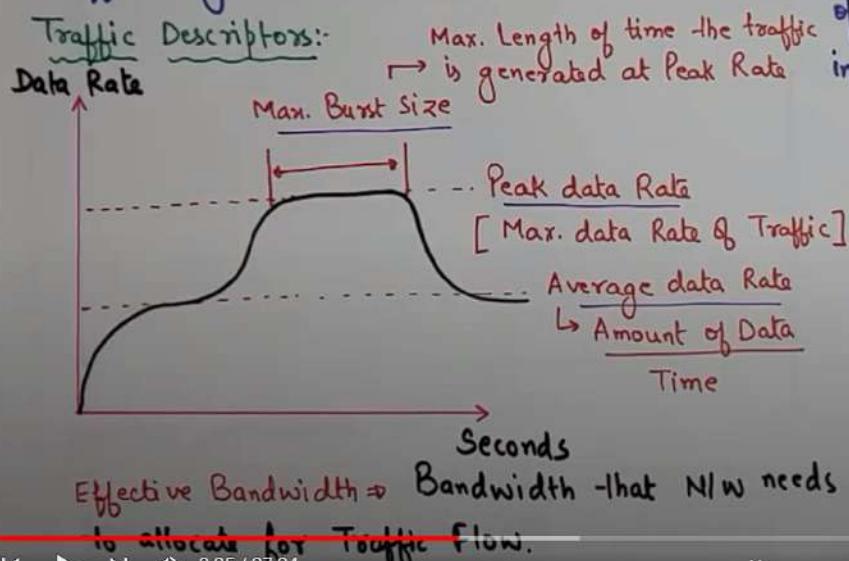
iii) Weighted Fair Queuing: In this, the queues are weighted based on the priority of queue.



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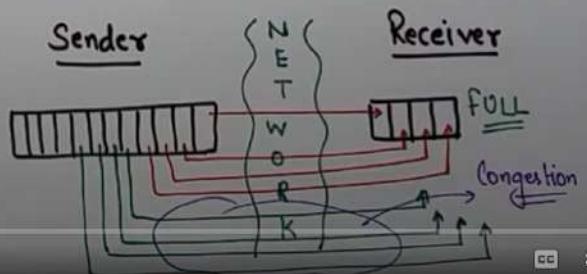
Congestion Control: In this, we try to avoid traffic congestion.



Congestion: It may occur if the load on the N/W is greater than the capacity of the N/W. It occurs due to Queues in Routers and switches.

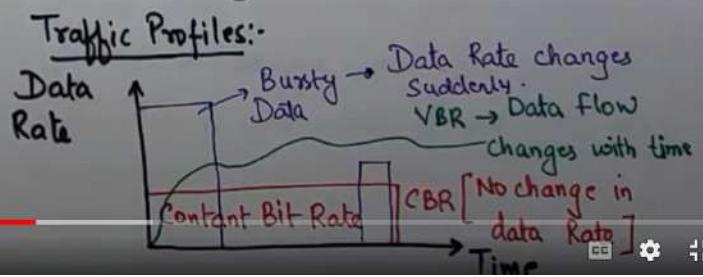
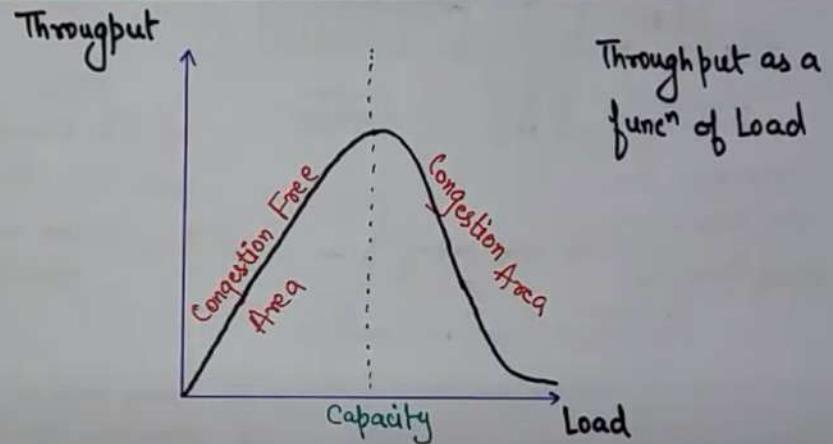
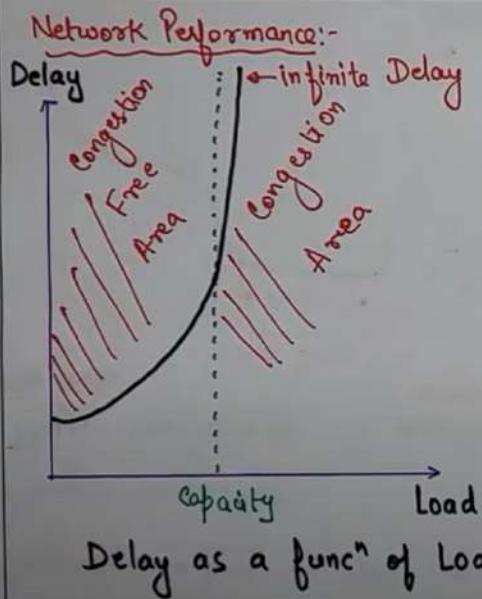
Congestion \rightarrow LOAD $>$ Capacity
 no. of Packets sent to N/W

No. of Packets a N/W can handle.



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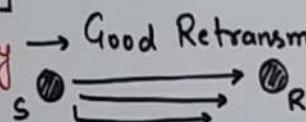
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Congestion Control Categories:

Open-Loop [Prevention]

→ Retransmission Policy (TCP)

→ Window Policy



→ Selective Repeat ARQ is better.

Acknowledgment Policy

Receiver will Send

ACK after a time or N packets.

Discarding Policy

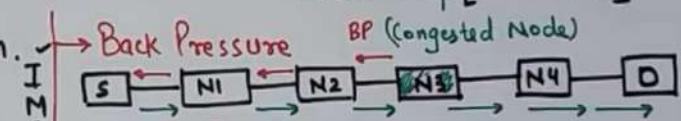
→ discarding less sensitive packets.

Admission Policy

→ Resources are checked first before admitting to the N/W.

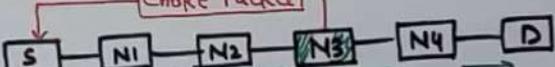
Congestion Control

Closed-Loop [Removal]



BP (Congested Node)

Choke Packet



→ Implicit Signalling → NO COMM b/w Congested Node with other Nodes/Source. Source assumes that Congestion occurred. → ACK is Not Rec. → delay in ACK.

Explicit Signalling

→ Congested node send a Signal to Source/destination. Signal is included in the data packet

= Backward Node to Source

= Forward

(Data Communication and Networking) [QoS-3]

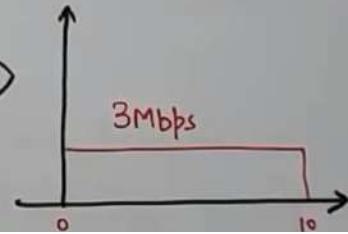
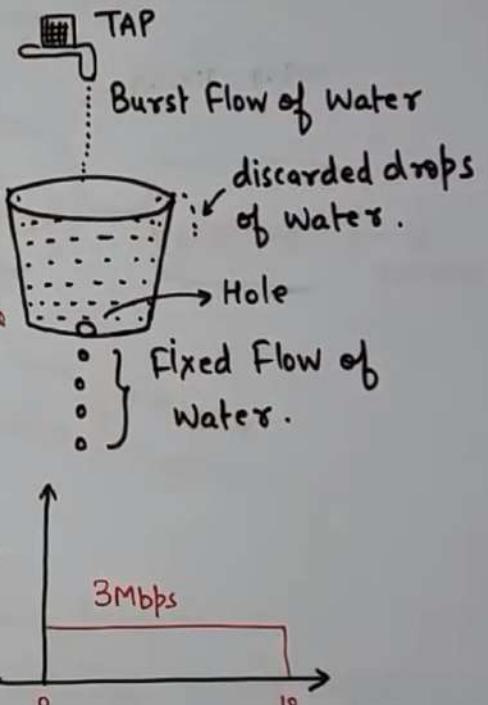
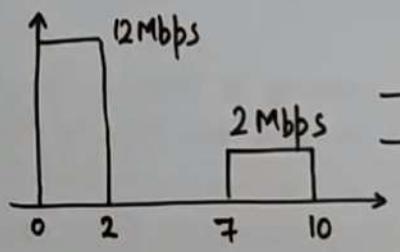
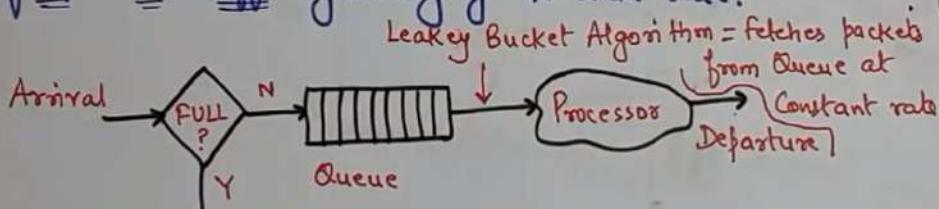
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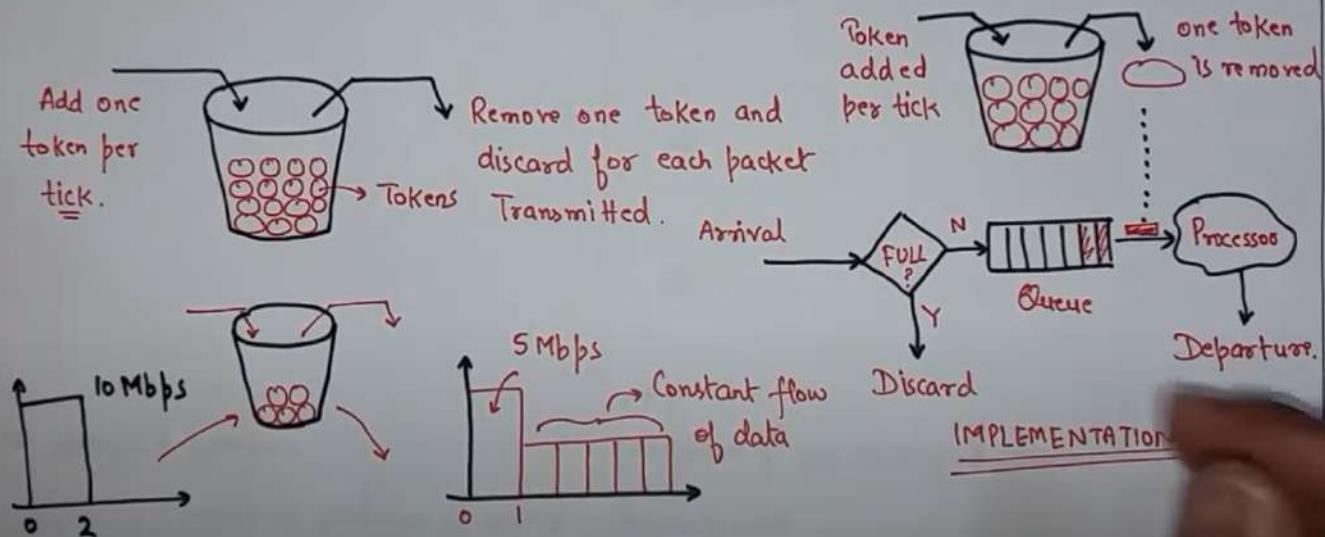
QoS-Traffic Shaping :- It is the mechanism to control

-the amount and -the rate of -the traffic Sent to -the NLW.

(i) Leaky Bucket :- This algorithm shapes Bursty traffic into fixed-rate traffic by averaging -the data rate.



QoS-Token Bucket :- It allows Bursty Traffic at a regulated maximum rate.

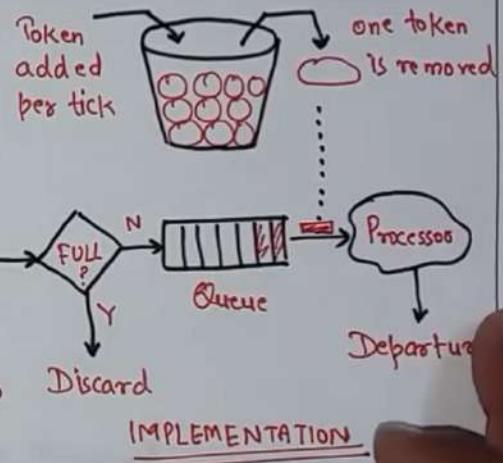
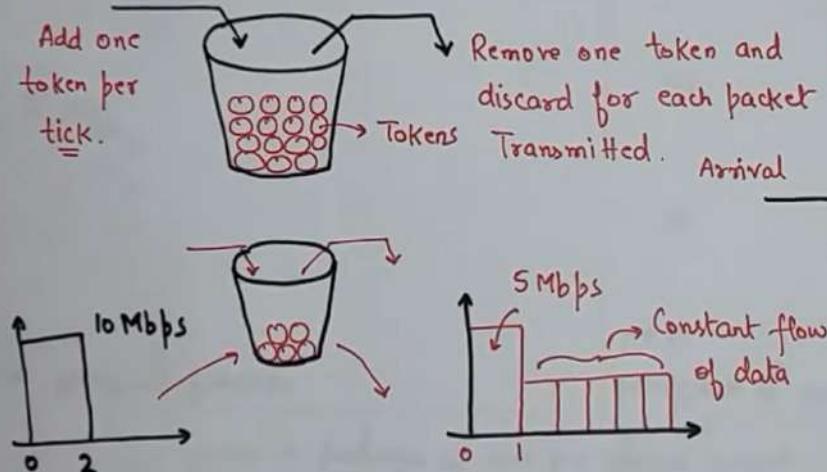


Data Communication and Networking [QoS-4]

QoS - Token Bucket :- It allows Bursty Traffic at a regulated maximum rate.

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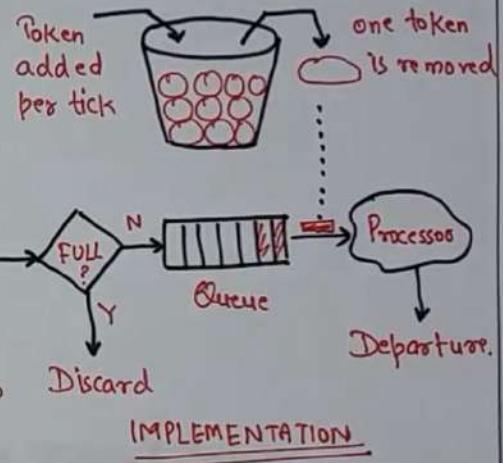
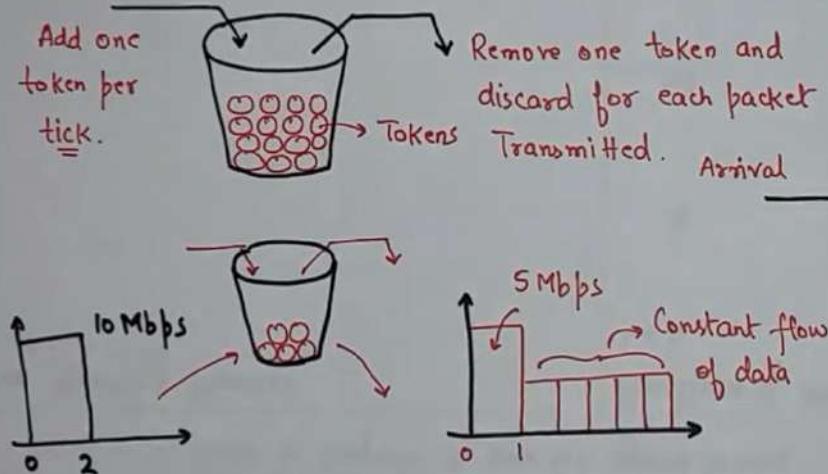


Data Communication and Networking [QoS-4]

QoS - Token Bucket :- It allows Bursty Traffic at a regulated maximum rate.

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L66: Token Bucket – QOS Traffic Shaping | Data Communication and Networking Lectures in Hindi
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QMP: What is the Difference b/w Leaky Bucket and Token Bucket Algorithm.

TOKEN BUCKET

- i) TOKEN Dependent.
- ii) If Bucket is full token is discarded but not the packet.
- iii) Packets can only transmit when there are enough tokens.
- iv) Allows Large bursts to be sent at faster rate.
- v) Saves tokens to send Large bursts.

LEAKY BUCKET

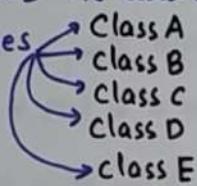
- i) Token Independent.
- ii) If Bucket is full, then packets are discarded.
- iii) Packets are transmitted Continuously.
- iv) Sends the Packet at a Constant Rate.
- v) No concept of Token.



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Classful Addressing: In this -the address space is divided into 5 classes



IMP..

How to Find Class of an Address:-

Binary Notation	
First Byte	
class A	0.....
class B	10....
class C	110....
class D	1110....
class E	1111....

Dotted Decimal First Byte	
Class A	0-127
class B	128- 191
class C	192- 223
class D	224- 239
class E	240- 255

Netid and Hostid: Only class A,B or C is divided into netid and hostid.

Mask: It helps -to find netid and hostid.

DEFAULT MASK FOR CLASSFUL ADDRESSING

(Decimal Dotted)

Ques1.) Find the Class of each Address:-

- a.) 227.12.14.87 \Rightarrow Class D
b.) 193.14.56.22 \Rightarrow class C
c.) 14.23.120.8 \Rightarrow class A
d.) 252.5.15.111 \Rightarrow Class E

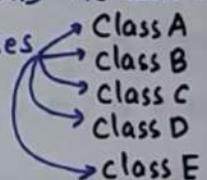
(Data Communication and Networking) (PPV)



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Classful Addressing: In this -the address space is divided into 5 classes



Netid
into n

IMP..

How to find Class of an Address:-

Binary Notation
First Byte

- class A 0....
class B 10....
class C 110....
class D 1110....
class E 1111....

IMP.: Dotted- Decimal
First Byte

- class A 0-127
class B 128- 191
class C 192- 223
class D 224- 239
class E 240- 255

Mask

IDE

L51: IPv4 Addressing | IP Classful Addressing with Notations and Solved Examples | DCN Lectures

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Other Universities, Colleges of India EEC Classes

and Hostid: Only class A, B or C is divided
ntid and hostid.

Binary Notation.

Ques 2.) Find the class of each address:-
Byt1

- a) 00000001 00... → class A
- b) 11000001 100... → class C
- c) 10100111 110... → class B
- d) 11110011 100... → class E

0
10-
110-
1110-
1111'

It helps to understand ntid and hostid.

12:37 / 18:21 ADDRESSING

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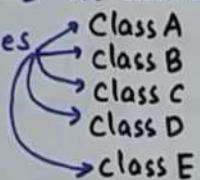
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Classful Addressing: In this -the address space is divided into 5 classes



IMP.: How to Find Class of an Address:-

	Binary Notation First Byte
class A	0.....
class B	10....
class C	110....
class D	1110....
class E	1111....

	Dotted Decimal First Byte
class A	0-127
class B	128-191
class C	192-223
class D	224-239
class E	240-255

Netid and Hostid: Only class A, B or C is divided into netid and hostid.

class	Byte 1	Byte 2	Byte 3	Byte 4
Class A	NetId	HostId	HostId	HostId
Class B	NetId	NetId	HostId	HostId
Class C	NetId	NetId	NetId	HostId

$$\begin{aligned} n \text{ bits} &= 1 \\ (32-n) &= 0 \\ A = n &= 8 \\ B = n &= 16 \\ C = n &= 24 \end{aligned}$$

Mask: It helps to find netid and hostid.

class	Binary (Mask)	Decimal
Class A	11111111 00.....	255.0.0.0
Class B	11111111 11111111 00.....	255.255.0.0
Class C	11111111 11111111 11111111 0...	255.255.255.0

DEFAULT MASK FOR CLASSFUL ADDRESS

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ClassLess Addressing: In this, Variable length blocks are used that belongs to no class.

Restrictions:-

- (i) Addresses in a Block must be contiguous.
 - (ii) No. of Addresses in a Block must be a power of $2 [1, 2, 4, 8, 16, \dots]$
 - (iii) The first address must be evenly divisible by the no. of addresses.

Mask in classless Addressing Can take any Value from 0 to 32. $(n) = 1, (32-n) = 0.$

↳ Slash/CIDR Notation .

x.y.z.t/n, defines the mask.

IMP Points:

- (i) First Address in block can be found by setting the rightmost $(32-n)$ bits to 0s.
 - (ii) Last address can be found by setting the rightmost $(32-n)$ bits to 1s.
 - (iii) No. of Addresses: 2^{32-n}

Example:- One of the Addresses is 205.16.37.39 $\frac{28}{n}$.
Find first, last and total no. of addresses.



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Classless Addressing: In this, Variable length blocks are used that belongs to no class.

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(ii) No. of Addresses in a Block must be a power of $2 [1, 2, 4, 8, 16, \dots]$

(iii) The first address must be evenly divisible by the no. of addresses.

Block → 205.16.37.32 $16 = 2^4$
 First → 205.16.37.33
 ↳ 205.16.37.34
 ↳
 Last → 205.16.37.47

Mask in classless Addressing can take any value from 0 to 32. $(n) = 1, (32-n) = 0$.

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(ii) Last address can be found by setting the rightmost $(32-n)$ bits to 1s.

(iii) No. of Addresses: 2^{32-n}

Example: One of the Addresses is 205.16.37.39/ n .
 Find first, last and total no. of addresses.

F.A.: - $\frac{11001101}{\downarrow} \frac{00010000}{\downarrow} \frac{00100101}{\downarrow} \frac{00100111}{\downarrow} 00100000$



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(iii) The first address must be evenly divisible by the no. of addresses.

Block Fixx → 205.16.37.32 $16 = 2^4$
 ↓ ↓
 205.16.37.33 \vdots
 ↓ ↓
 205.16.37.34 \vdots
 ↓ ↓
 Last 205.16.37.47

Mask in classless Addressing can take any value from 0 to 32. ($n = 1, (32-n) = 0$).

→ Slash/CIDR Notation.

$x.y.z.t/n$, defines the mask.

IMP Points:

(i) First Address in block can be found by setting the rightmost $(32-n)$ bits to 0s.

(ii) Last address can be found by setting the rightmost $(32-n)$ bits to 1s.

(iii) No. of Addresses: 2^{32-n}

Example:- One of the Addresses is 205.16.37.39/ n . Find first, last and total no. of addresses.

F.A.: - $\frac{11001101}{\downarrow} \frac{00010000}{\downarrow} \frac{00100101}{\downarrow} \frac{00100111}{\downarrow} 00100000$
 = 205.16.37.32



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(iii) The first address must be evenly divisible by the no. of addresses.

Block → 205.16.37.32 $16 = 2^4 =$
 Fixx → 205.16.37.33
 ↘ 205.16.37.34
 ↘
 ↘
 Last 2 7.47

Mask in classless Addressing can take any value from 0 to 32. ($n = 1, (32-n) = 0$).

↳ Slash/CIDR Notation.

$x.y.z.t/n$ defines the mask.

IMP Points:

(i) First Address in block can be found by setting the rightmost $(32-n)$ bits to 0s.

(ii) Last address can be found by setting the rightmost $(32-n)$ bits to 1s.

(iii) No. of Addresses: 2^{32-n}

Example:- One of the Addresses is $205.16.37.39/28$.
 Find first, last and total no. of addresses.

F.A.:— $\downarrow \quad \downarrow \quad \downarrow \quad \downarrow$ 00101111
 = $205.16.37.32$ L.A. = $205.16.37.47$



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Classless Addressing: In this, Variable length blocks are used that belongs to no class.

Properties:-

(i) Addresses in a Block must be Contiguous.

(ii) Number of addresses in a Block must be of 2^k [1, 2, 4, 8, 16, ...]

first address must be evenly divisible by the no. of addresses.

$$\begin{array}{l} \text{→ } 205.16.37.32 \\ \text{→ } 205.16.37.33 \\ \text{→ } 205.16.37.34 \\ \text{→ } 205.16.37.47 \end{array}$$

$16 = 2^4$

Mask in classless Addressing can take any value from 0 to 32. ($n = 1, (32-n) = 0$).

↳ Slash/CIDR Notation.

$x.y.z.t/n$ defines the mask.

IMP Points:

(i) First Address in block can be found by setting the rightmost $(32-n)$ bits to 0s.

(ii) Last address can be found by setting the rightmost $(32-n)$ bits to 1s.

(iii) No. of Addresses: 2^{32-n}

Example: One of the Addresses is $205.16.37.39/28$. Find first, last and total no. of addresses.

$$\begin{array}{ccccccc} & & & & & & \rightarrow 00101111 \\ & \downarrow & \downarrow & \downarrow & \downarrow & \downarrow & \\ 11001101 & 00010000 & 00100101 & 00100111 & & & N.A. = 2^4 \\ F.A.: - & & & & 00100000 & & \\ = & 205.16.37.32 & & & L.A. = 205.16.37.47 & = 16 & \end{array}$$



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One More Way of Extracting Block Info:-

- ↳ (i) No. of Addresses, $N = 2^{32-n}$.
- (ii) First Address = (Address) AND (MASK)
- (iii) Last Address = (Address) OR (COMPLEMENT OF MASK)

Example:- For a Address 205.16.37.39 / n, find

- i) First Address
 - ii) Last Address
 - iii) No. of Addresses. = 2^{32-n}
- $$= 2^4$$
- $$= 16$$

Address:- 11001101 00010000 00100101 00100111

Mask:- 11111111 11111111 11111111 11110000

First Add. 11001101 00010000 00100101 00100000

AND

$1+1=1$

Mask
(Comp.)

00000000 00000000 00000000 00001111

11001101 00010000 00100101 00101111



(Data Communication and Networking) (IPv4-7)

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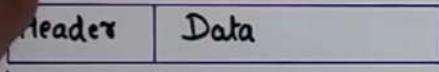
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IPv4 Protocol: IP Packet/Datagram Format

IMP Points:-

Used by TCP/IP protocols at N/W Layer.

iii) Reliable and Connectionless



Header Length				(Header + data)	
VER (4)	HLEN (4)	Service (8)	Total Length (16)		
		Identification (16)	Flags (3)	Frag. offset → Error	
	Time to live (8)	Protocol (8)	Header Checksum (16)		
Source IP address (32)					
Destination IP address (32)					
option (32)					

(Header) [32 bits]

Max. time for
which IP data
gram is alive

Higher Level
TCP UDP
ICMP --

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2128

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IPv6 Addresses: It is of 128 bits or 16 bytes.

↳ Length is 4-times the length address of IPv4.

Notations:

(i) Dotted Decimal: It is used for IPv4 compatibility. $221.14.65.11.105.45.170.34.12.234.18.0.14.0.115.255$](16)

(ii) Colon Hexadecimal: It is used to make the address more readable. In this notation, the 128 bits are divided into 8 sections, each of 2 bytes in length. [Two bytes in Hexadecimal req. 4 Hexadecimal digits].

FDEC: BA98: 7654: 3210: ADBF: BBFF:

2922: FFFF

Abbreviation: It is a technique to reduce the length of IPv6 address. It is done by omitting / removing the leading zeros of a section.

[NOTE: Only the leading zeros can be dropped]
[Zero-Compression]

FDEC: 0074: 0000: 0000: 0000: BOFF: 0000: FFF0

↓
omitting these Zeros

FDEC: 74: 0: 0: 0: BOFF: 0: FFF0

} Abbreviated Address

FDEC: 74: : BOFF: 0: FFF0

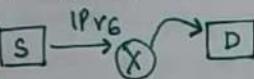
↓
GAP.

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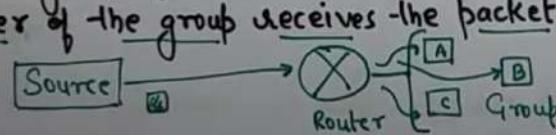
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Types of Address Space in IPv6:-

(i) Unicast Addresses:- It defines single interface or computer. The packet sent to a unicast address will be routed to the intended PC or recipient.



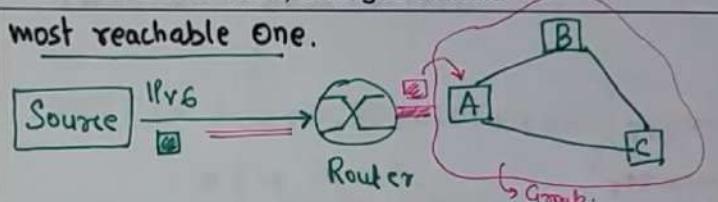
(ii) Multicast Addresses:- These are used to define a group of computers/hosts. In this, each member of the group receives the packet.



(iii) Anycast Addresses:- Defines group of nodes or computers that all share a single address.

A packet with unicast address is delivered to

most reachable One.

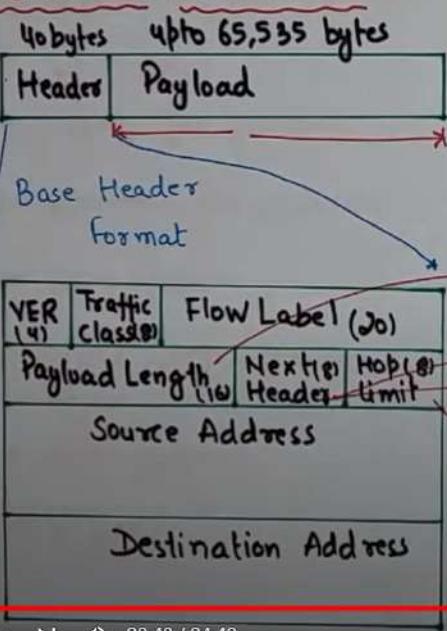


(iv) Broadcasting and Multicasting: IPv6 does not define broadcasting and considered it as a special case of multicasting.

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IPv6 Protocol:- Packet Format



VER :- IPv6 = ⑥

Traffic class :- distinguishing diff. Payload

Flow Label :- provide special handling for particular data flow.

Length of IP datagram - Base headers

2 ① = ICMP

6 = TCP, 17 = UDP -----

TTL

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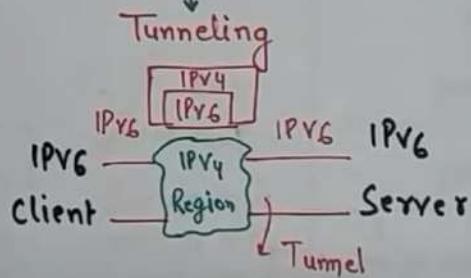
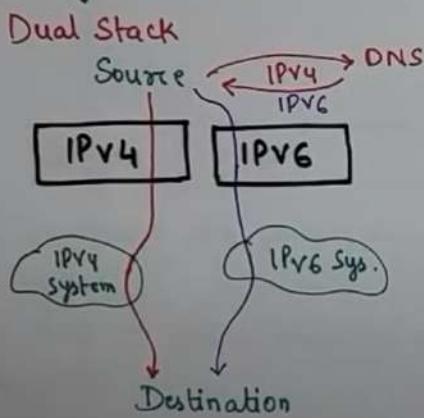
Changes in IPv6:-

- i) Larger address $\Rightarrow 2^{128} - 2^{32} \Rightarrow 2^{96}$ time more address than IPv4.
Space
 - ii) Better Header format \Rightarrow options are separated from Base Header.
 - iii) New options \Rightarrow Additional functionalities.
 - iv) Allowance for Extension \Rightarrow Extension of Protocol
 - v) Support for Resource Allocation \Rightarrow Traffic class
 \downarrow
Flow Label
 - vi) Support for More Security \Rightarrow Encryption
- Traffic class }
Flow Label }
Special Handling of
-the packet

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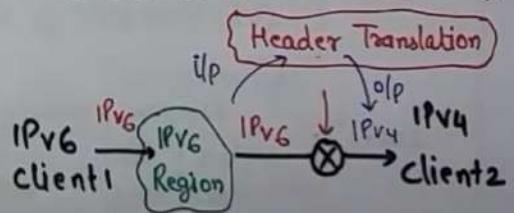
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Transition from IPv4 to IPv6: There are three transition strategies.



IPv6 Packet is encapsulated in an IPv4 Packet when it enters the IPv4 Region.

Header Translation
When most of system are on IPv6 but some still uses IPv4



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Difference Between IPv4 AND IPv6

IPv4

- 1.) Length of Address = 32 Bit
- 2.) Represent in Decimal notation
- 3.) IPsec Support = Optional
- 4.) Packet Flow Indication = NONE
- 5.) checksum field = YES
- 6.) option field = YES
- 7.) Address(IP) to MAC = (ARP)
- 8.) Broadcast Message = YES
- 9.) Total No. of Addresses = 2^{32}

IPv6

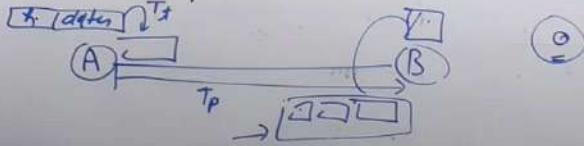
- i) Length of Address = 128 Bit
- ii) Represented in Hexadecimal notation
- 3.) IPsec Support = Inbuilt
- 4.) Packet Flow = YES → Flow Label field
- 5.) checksum field = NONE
- 6.) options field = NONE, IPv6 Extension Header
- 7.) Replaced By = Neighbour Discovery (NDP) Protocol
- 8.) Broadcast Message = Special type of Multicast Address
- 9.) Total No. of Addresses = 2^{128}

Delays in CN

The delay of network specifies how long it takes a bit of data to travel across the network from one node to another.

there are four different type of delay:

3. Queuing Delay: Time the packet spend in routing queuing. The queuing delay depends upon number of packet arrived earlier in the queue.



④ Processing Delay: A packet consist of head and data field. The head contain address of source and destination. The time require to examine header is called processing delay.

2. Propagation Delay: Time for a bit reaches destination. Time taken by first bit travel from source to destination

$$\text{Propagation delay } T_p = \frac{\text{Distance}(d)}{\text{Transmission speed}(V)}$$

$$d = 2.1 \text{ km}$$

$$V = 2.1 \times 10^8 \text{ m/s}$$

$$T_p = \frac{2.1 \times 10^3}{2.1 \times 10^8} = 10^{-5} \text{ sec}$$

light = $3 \times 10^8 \text{ m/s}$
 speed of light
 $= 3 \times 10^8 \times \frac{1}{10^8}$
 $= 2.1 \times 10^8 \text{ m/s}$