Gate Question

SECTION A

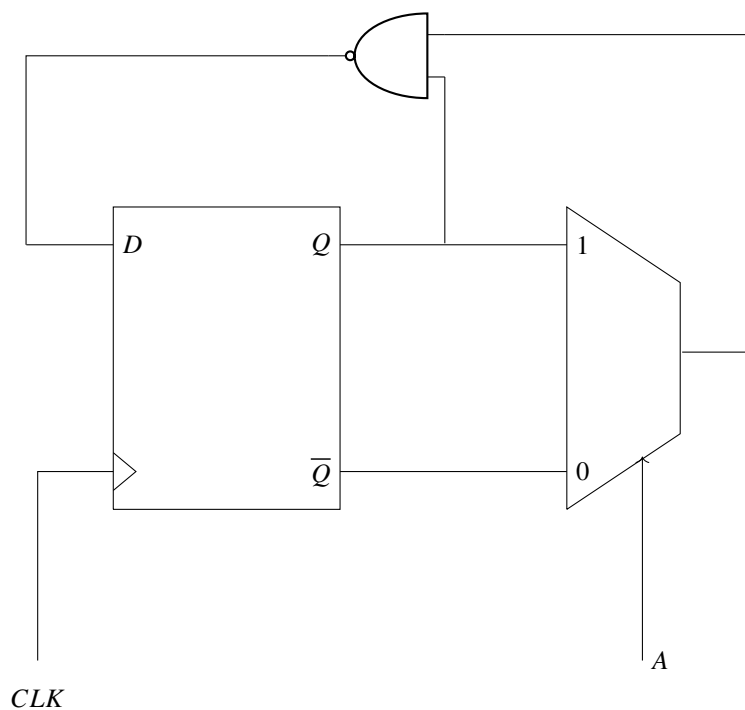## February 5, 2024

1. The state transition diagram for the circuit shown is   (GATE-IN2019,39)
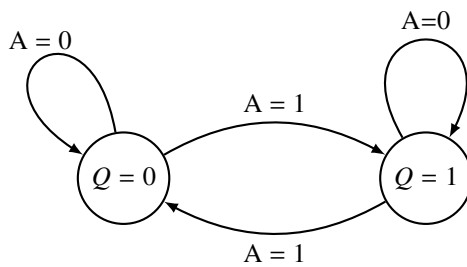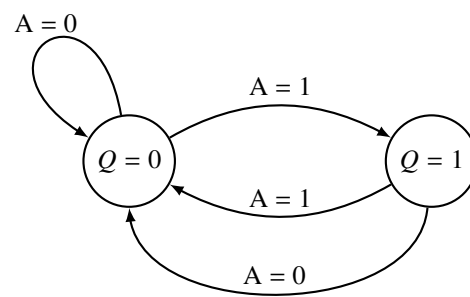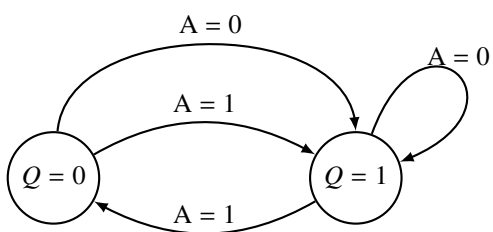

Figure 1: Circuit Diagram

(a)

A = 0

A = 1

$Q = 0$

$Q = 1$

A = 1

A = 0

(b)

A = 0

A = 0

A = 1

$Q = 0$

$Q = 1$

A = 1

(c)

A=0

A = 1

A = 0

$Q = 0$

$Q = 1$

A = 1

(d)