- 1. [2 pts x 5 = 10 pts] True or False. No explanations are necessary.
  - a) The internal resistance R of a practical current source is in parallel with the source.
  - b) For a well-designed circuit with a practical current source, the internal resistance *R* of the current source should be much larger than the load resistance.
  - c) The input resistance of an ammeter is very small.
  - d) When we calculate the Thevenin Equivalent circuit, the dependent sources must be turned off.
  - e) The input resistance of an operational amplifier is very small.

	a)	b)	c)	d)	e)
True (T) or False (F)	T	T	T	F	F

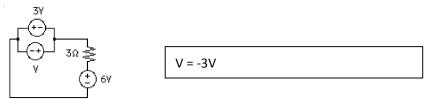
- 2. [28 pts] Problems with short answers.
  - a) [2 pts] You have a copper trace on a Printed Circuit Board that is 10cm long and 1mm wide. The thickness of the copper is 0.1mm. The resistivity of copper is  $1.7x10^{-8}\Omega \cdot m$ . Calculate the resistance of the copper wire.

R = 17 m 
$$\Omega$$

b) [4 pts] An electric car has a rechargeable battery with a capacity of 60kWh. A standard J1772 charger supplies 30A at 200V. What is the output power of the charger (use the correct units)? If the battery is completely empty, how long does it take to fully charge the battery using the J1772 charger?

Power = 6KW		Time = 10h
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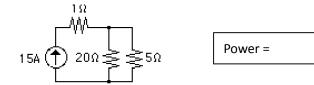
c) [2 pts] Find the voltage V of the voltage source in parallel with the 3V voltage source in the circuit below.



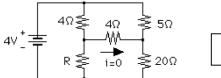
d) [4 pts] You have two resistors  $R_1$  and  $R_2$ . Using these two resistors in various combinations you can get resistances of 4, 6, 12 and 18 $\Omega$ . Find  $R_1$  and  $R_2$ .

$R_1 = 6 \Omega$ $R_2$	2 = 12 Ω
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e) [4 pts] Calculate the power dissipated in the  $5\Omega$  resistor.



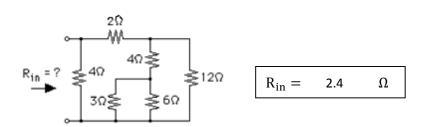
f) [4 pts] Find R in the circuit below.



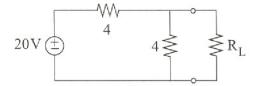
720

W

g) [4 pts] Find R<sub>in</sub> in the circuit below.



h) [4 pts] In the circuit below (resistance is in ohms), for what value of  $R_L$  is power in  $R_L$  maximized? What's the maximum power dissipated in  $R_L$ ?

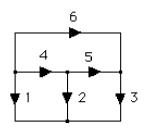


$$R_L = 2$$
  $\Omega$ 

3. [6 pts] Apply KCL to show that

$$I_1 + I_4 - I_5 + I_3 = 0$$

in the circuit below. Here  $I_k$  is the current of branch k ( $k=1,2,\cdots,6$ ) and its direction is shown in the circuit.

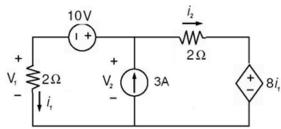


$$-1_{6} = 1_{1} + 1_{4}$$

$$1_{6} = -1_{5} + 1_{3}$$

$$0 = 1_{1} + 1_{4} - 1_{5} + 1_{3}$$

4. [16 pts] In the circuit shown below



(a) [2 pts] Express  $i_1$  in terms of  $V_1$  and constants given in this problem.

$$V_1 = 2 i_1$$

(b) [4 pts] Express  $i_2$  in terms of  $V_1$ ,  $V_2$  and/or constants given in this problem.

$$i_2 = 3 - V_1/2$$

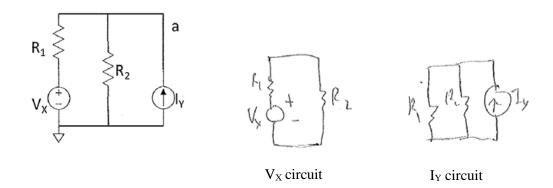
(c) [6 pts] Write two equations in  $V_1$  and  $V_2$  that can be used to solve the circuit.

$$10+V_1-V_2=0$$
  
  $2x(3-V_1/2)+8x(V_1/2)-V_2=0$ 

(d) [4 pts] Solve for  $V_1$ ,  $V_2$ ,  $i_1$  and  $i_2$ .

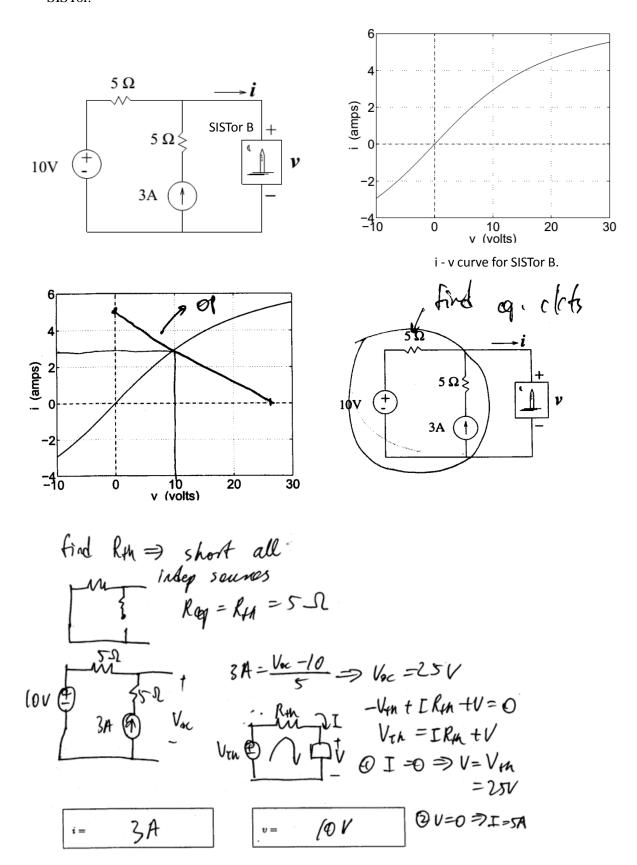
$$V_1 = 2 V$$
  
 $V_1 = 12 V$   
 $i_1 = 1 A$   
 $i_2 = 2 A$ 

- 5. [12 pts] Use superposition to calculate the voltage at node a in the figure below by
  - a) Drawing the two simplified circuits corresponding to the individual independent sources.
  - b) Solving for the effect of each source and writing the equations for the output as a function of the independent sources.

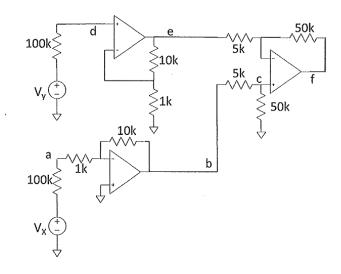


$$V_{a}(V_{X}, I_{Y}) = I_{y} \frac{R_{1}R_{1}}{R_{1}R_{2}} + V_{x} \frac{R_{2}}{R_{1}R_{2}}$$

6. [16 pts] The resistive network shown below is connected to a SISTor B. The SISTor is a nonlinear device with *i* - *v* characteristic shown below. Find the current i drawn by the SISTor and the voltage v across the SISTor.



7. [12 pts] In the circuit below, find the gain from  $V_x$  to a, from a to b, from b to c, and so on. Assume that the operational amplifiers are ideal.



Gain from $V_X$ to a $(=V_a/V_X)$	1/101
Gain from $V_a$ to $V_b$ $(=V_b/V_a)$	-10
Gain from $V_b$ to $V_c$ $(=V_c/V_b)$	10/11
Gain from $V_y$ to $V_d$ $(=V_d/V_y)$	1
Gain from $V_d$ to $V_e$ $(=V_e/V_d)$	11
Gain from $(V_b, V_e)$ to $V_f$ $(=V_f/(V_b-V_e))$	10