Circuits and Transforms

EE3900: Linear Systems and Signal Processing Indian Institute of Technology Hyderabad

Ankit Saha AI21BTECH11004

8 Oct 2022

1. Definitions

1.1 The unit step function is defined as

$$u(t) = \begin{cases} 1 & t > 0 \\ \frac{1}{2} & t = 0 \\ 0 & t < 0 \end{cases}$$
 (1.1)

1.2 The Laplace transform of g(t) is defined as

$$G(s) = \int_{-\infty}^{\infty} g(t)e^{-st} dt$$
 (1.2)

2. Laplace Transform

- 2.1. In the circuit, the switch S is connected to position P for a long time so that the charge on the capacitor becomes q_1 μ C. Then S is switched to position Q. After a long time, the charge on the capacitor is q_2 μ C
- 2.2. Draw the circuit using latex-tikz **Solution:**

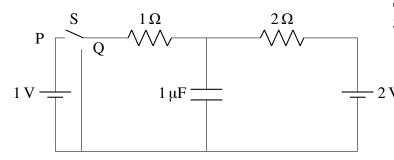
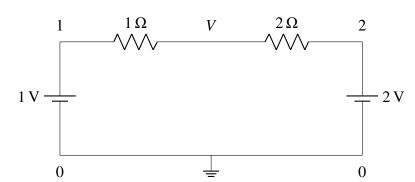


Fig. 2.2. Circuit diagram of the circuit in question

2.3. Find q_1

Solution: After a long time, when steady state is achieved, a capacitor behaves like an open circuit, i.e., current passing through it is zero



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By Kirchoff's junction law, we get

$$\frac{V-1}{1} + \frac{V-2}{2} = 0 \tag{2.1}$$

$$\implies V = \frac{4}{3} V \tag{2.2}$$

$$\implies q_1 = CV = \frac{4}{3} \,\mu\text{C} \tag{2.3}$$

2.4. Show that the Laplace transform of u(t) is $\frac{1}{s}$ and find the ROC

Solution: The Laplace transform of u(t) is given by

$$\mathcal{L}\left\{u(t)\right\} = \int_{-\infty}^{\infty} u(t)e^{-st} \, \mathrm{d}t \qquad (2.4)$$

$$= \int_0^\infty e^{-st} \, \mathrm{d}t \tag{2.5}$$

$$=\lim_{R\to\infty}\frac{1-e^{-sR}}{s}\tag{2.6}$$

This limit is finite only if $\Re(s) > 0$, which is going to be its ROC

Therefore

$$u(t) \stackrel{\mathcal{L}}{\longleftrightarrow} \frac{1}{s} \qquad \Re(s) > 0 \qquad (2.7)$$

2.5. Show that

$$e^{-at}u(t) \stackrel{\mathcal{L}}{\longleftrightarrow} \frac{1}{s+a} \qquad a > 0$$
 (2.8)

and find the ROC

Solution: The Laplace transform of $e^{-at}u(t)$ for a > 0 is given by

$$\mathcal{L}\{u(t)\} = \int_{-\infty}^{\infty} e^{-at} u(t) e^{-st} dt$$
 (2.9)
=
$$\int_{0}^{\infty} e^{-(s+a)t} dt$$
 (2.10)
=
$$\lim_{R \to \infty} \frac{1 - e^{-(s+a)R}}{s+a}$$
 (2.11)

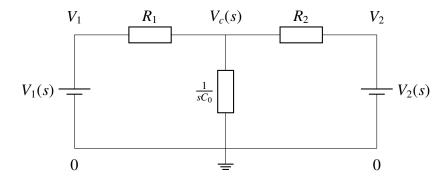
This limit is finite only if $\Re(s+a) > 0$, which is going to be its ROC

Therefore

$$e^{-at}u(t) \stackrel{\mathcal{L}}{\longleftrightarrow} \frac{1}{s+a} \qquad \Re(s) > -a \qquad (2.12)$$

since a is real

2.6. Now consider the following resistive circuit transformed from Fig. 2.2



where

$$u(t) \stackrel{\mathcal{L}}{\longleftrightarrow} V_1(s)$$
 (2.13)

$$2u(t) \stackrel{\mathcal{L}}{\longleftrightarrow} V_2(s)$$
 (2.14)

Find the voltage across the capacitor $V_{C_0}(s)$ **Solution:**

$$V_2(s) = \frac{2}{s}$$
 $\Re(s) > 0$ (2.16)

By Kirchoff's junction law, we get

$$\frac{V_c - V_1}{R_1} + \frac{V_c - V_2}{R_2} + \frac{V_c - 0}{\frac{1}{sC_0}} = 0 \quad (2.17)$$

$$\Longrightarrow V_c \left(\frac{1}{R_1} + \frac{1}{R_2} + sC_0 \right) = \frac{V_1}{R_1} + \frac{V_2}{R_2} \quad (2.18)$$

$$\Longrightarrow V_c(s) = \frac{\frac{1}{sR_1} + \frac{2}{sR_2}}{\frac{1}{R_1} + \frac{1}{R_2} + sC_0}$$
 (2.19)

$$= \frac{\frac{1}{R_1 C_0} + \frac{2}{R_2 C_0}}{s \left(s + \frac{1}{R_1 C_0} + \frac{1}{R_2 C_0}\right)}$$
(2.20)