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Course: MCA 'D'

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Subject:

Computer Organization & Architecture

(mid term Exam)

Q1. Discuss the working of NAND gate with the help of Diagram & truth table.

NAND Gate

The NAND gate is a special type of logic gate in the digital logic circuit. The NAND gate is the universal gate. it means all the basic gates such as AND, OR and NOT gate can be constructed using a NAND gate. The NAND gate is the combination of the NOT-AND gate.

The output state of the NAND gate will be low only when all the inputs are high.

The NAND gate Boolean Expression is given by,

$$A = (X \cdot Y)'$$

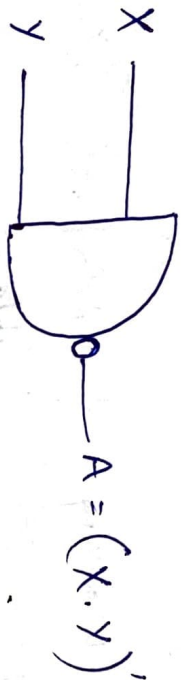
X and Y are the inputs and A is the output

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NAND logic gate. Can be achieved by multiplying all the inputs and then complementing the multiplied result.

The NAND gate Symbol is Shown.



* NAND gate means "not AND gate", hence the output of this gate is just reverse of that of a similar AND gate.

Truth table of 2 input NAND gate

Input		output
A	B	$X = A \cdot B$
0	0	1
0	1	1
1	0	1
1	1	0

NAND gate type

There are basically three types of NAND logic gates depending on the number of inputs.

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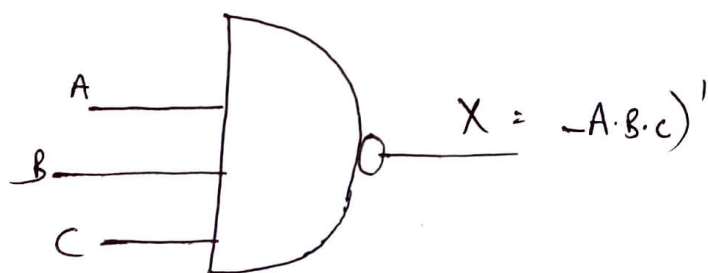
2-input NAND, 3-input NAND

3-input NAND

The gate accepts three inputs and gives single output
the total possible combinations of inputs are 8

The truth table of 3-input NAND

A	B	C	X
0	0	0	1
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	0



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