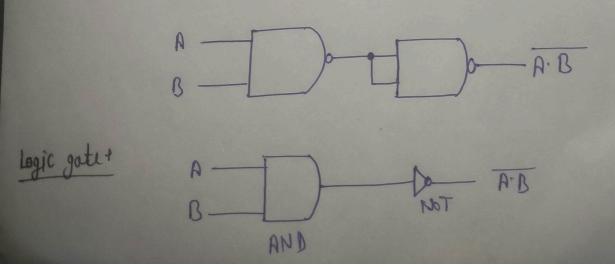
NAME-UTKARSH CHAVHAN UNIV. ROll- 10841 SUBJECT+ Computer organization & COURSE- mca, 1.8 STUDENT. ID- 1391035 CODE: Architecturu TMC 102

Semester- 1xt Datet 22-man-2022 Class Roll. No. 60 Page- 1

Not gote. They are connected in carcade form it is also called Negated And gote. The NAND gote Provider the falls or low output only when their autputs is high or true. The NAND gote by when their autputs is high or true. The NAND gote is essential because different types of a boolean function are implemented by using it.

The NAND gote has the Brokerty of Eunctional completeness. means any type of gote can be implemented by Using NAND gotel.

Circuit diagrame



Subject + COA MAINE - UTKARSH CHRUHAN UNIV. ROII- 210141 COURSE- MCA 1,B STUDENTID- 41391035 COde+ TMC 102 Sumuster - 1st Dotter 22- man - 2022 Clan Roll no: 60 Page No .- 2 Touth tablet NAND gote Output A B 7 0 0 1 0 0