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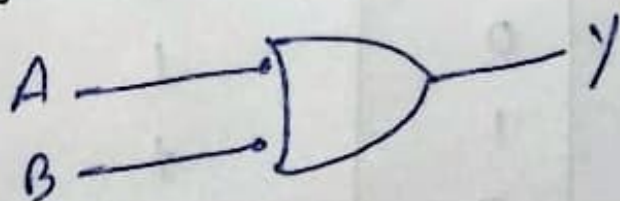
Course - MCA Sem - I<sup>st</sup>

## Computer organisation Mid term

Q1. NAND Gate: A NAND gate is a logic gate that performs the reverse operation of an AND logic gate. It is a blend of AND and NOT gates and it is a commonly used logic gate.

The NAND gate has an output that is normally at logic high and only goes to logic low when all of its inputs are at logic high. The logic NAND gate is the reverse or complementary design of the AND gate.

NAND gate symbol:

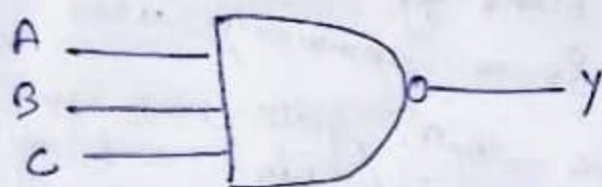


Anjali

NAND Gate Truth table:

Input		output
A	B	Y
0	0	1
0	1	1
1	0	1
1	1	0

3 Input NAND Gate



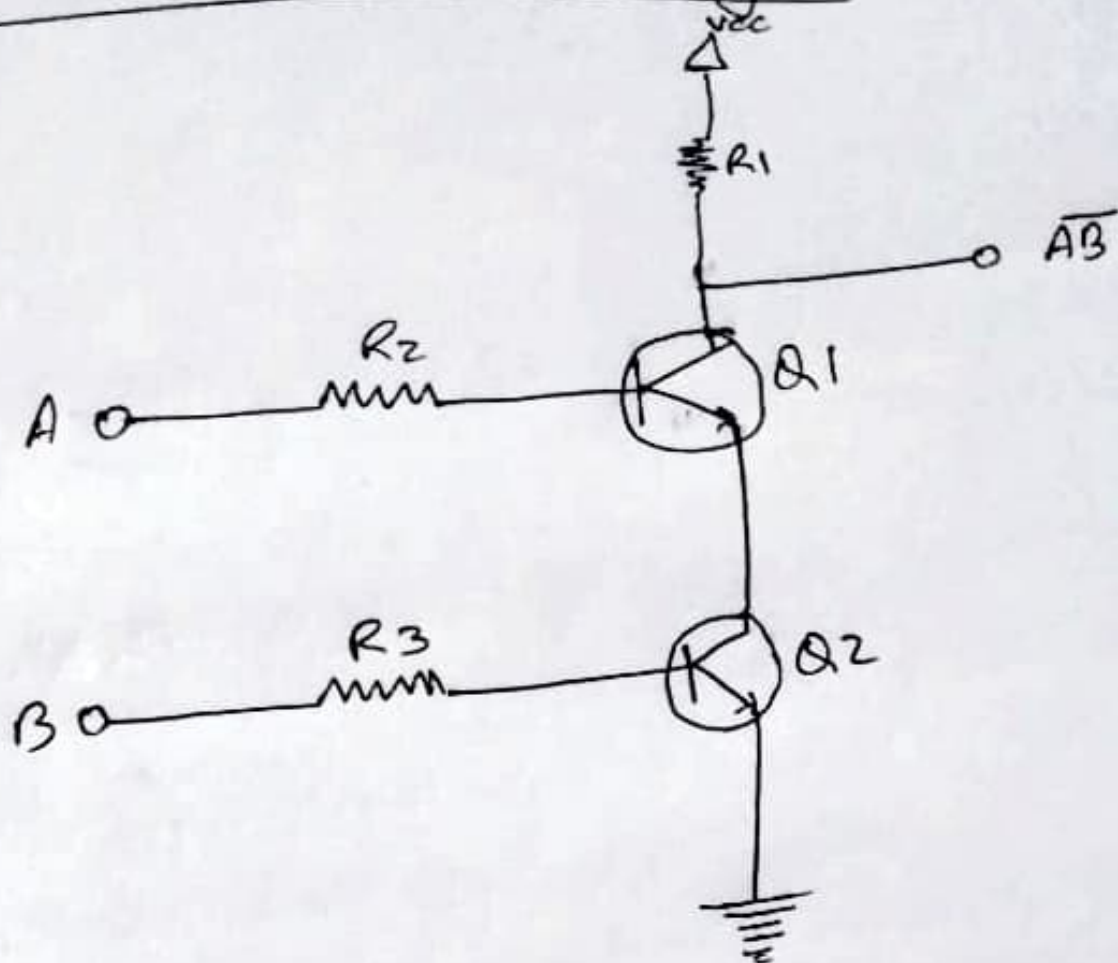
Truth table

Inputs			output
A	B	C	Y
0	0	0	1
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	0

Ans

Q

# NAND Gate Circuit + Diagram



Arshi

A	B	Q1	Q2	output
0	0	off	off	1
0	1	off	on	1
1	0	on	off	1
1	1	on	on	0