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Section :- B

Ans 1 ⇒ A "NAND" gate ("not AND gate") is a logic gate that produces a low output (0) only if all its inputs are true, and high output (1) otherwise.

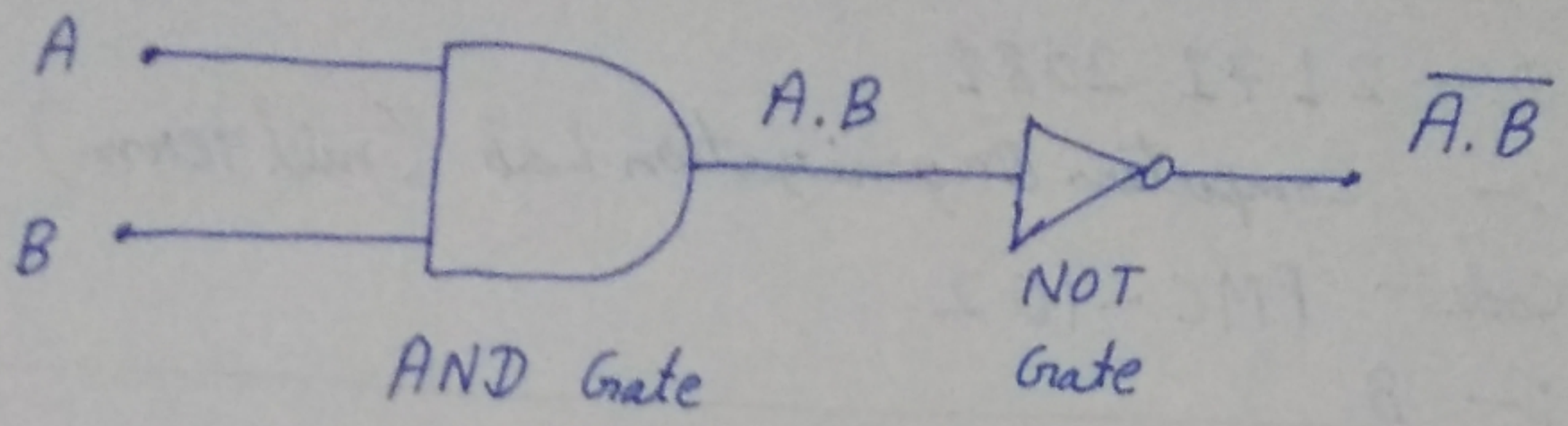
Hence the NAND gate is the inverse of an "AND" Gate, and its circuit is produced by connecting an "AND" gate to a "NOT" Gate.

Just like "AND" Gate, "NAND" gate may have any number of inputs but only one output.

The NAND gate performs the logical "NAND" operation. NAND gates are known as "Universal Gates", which means they are a type of logic gates which can implement any "Boolean function" without the need to use any other gate type

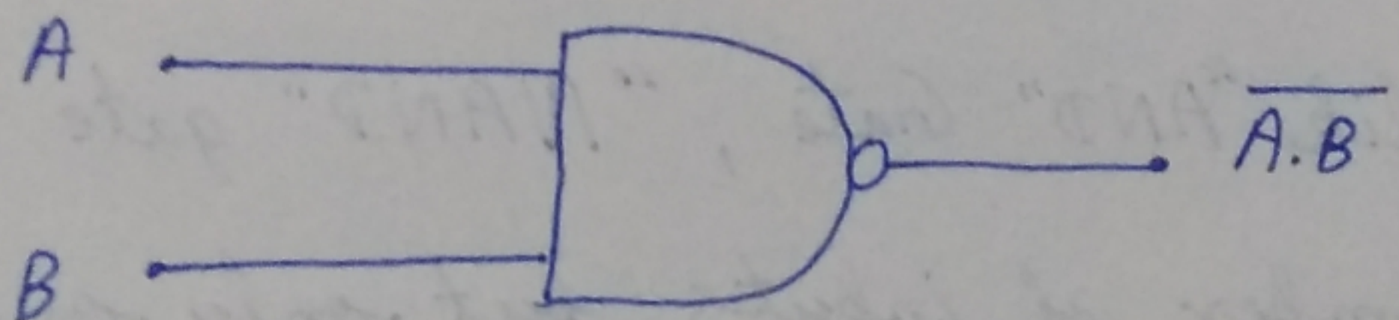
Sarika

Basic Construction of the NAND gate is shown below:-



Now, the symbol of a NAND gate is similar to the AND gate, but a bubble is drawn at the output point of the AND Gate.

The symbol of the NAND Gate is shown below:-



NAND Gate

Truth Table

A	B	output $\overline{A.B}$
0	0	1
0	1	1
1	0	1
1	1	0

Sorita