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COURSE: MCA

SECTION: 'A' SEMESTER: 1st

SUBJECT: CO Practical Exam.

Ques:- Discuss the working of NAND gate with the help of circuit diagram & truth table?

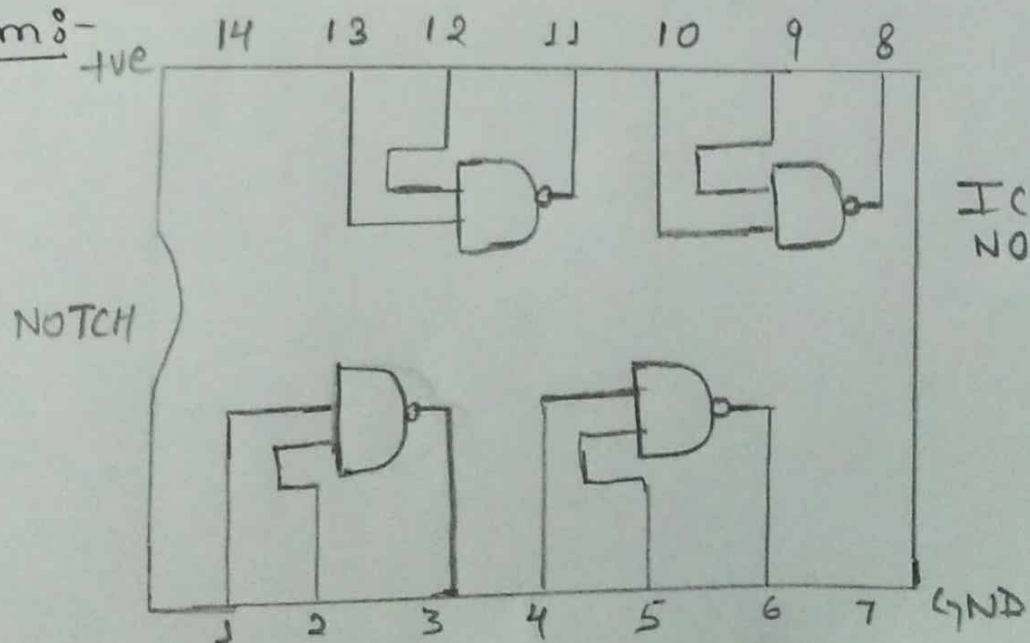
Ans:- Aim:- To study of logic NAND gates

Apparatus Required:- NAND gate IC, Power supply, Hook up wires and bread board etc

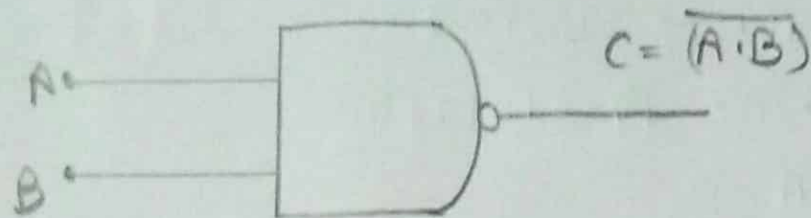
Theory:- The NAND gate is a special type of logic gate in digital logic circuit. The NAND gate is the universal gate. It means all the basic gates such as AND OR & NOT gate can be constructed using a NAND gate.

The NAND gate is the combination of the NOT-AND gate. The output state of the NAND gate will be low only when all the inputs are high.

IC diagram:-



Logic diagram:



TRUTH TABLE:

Input		Output
A	B	$C = \overline{A \cdot B}$
0	0	1
0	1	1
1	0	1
1	1	0

Procedure - connect the vcc & ground to the pins specified by the data sheet

* Do all the connection as per the IC diagram

* Switch on the Power supply

Result: The result of the experiment has been verified successfully.