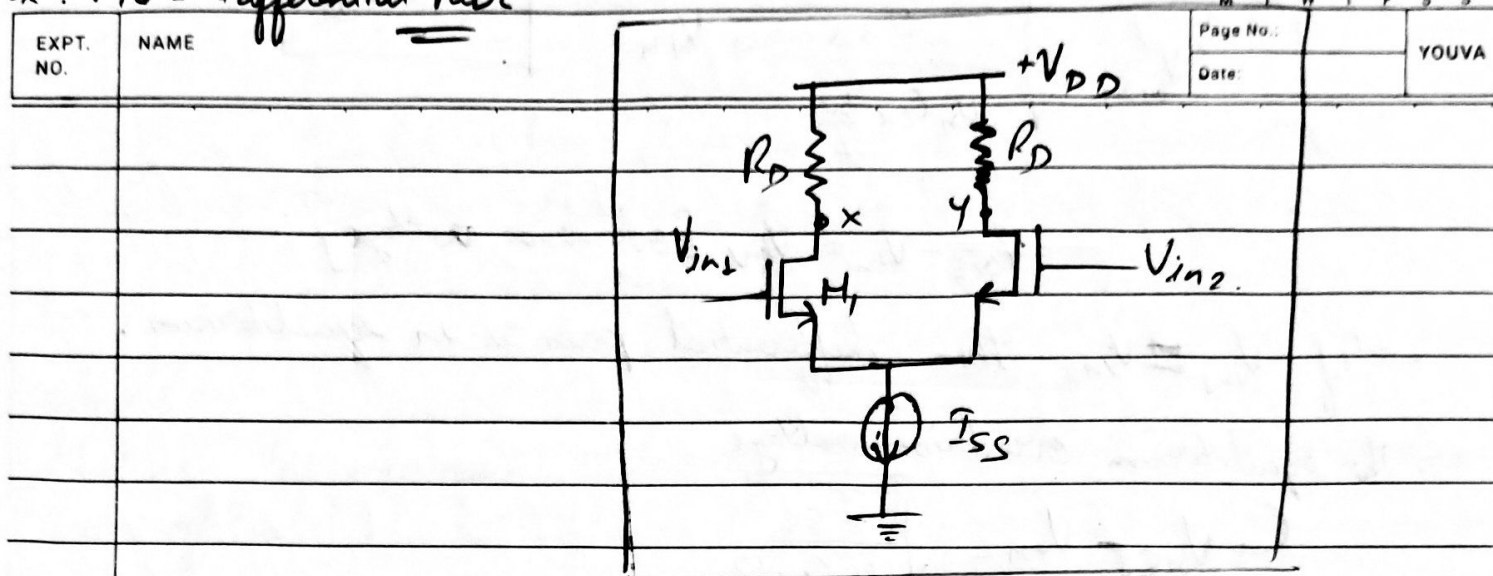


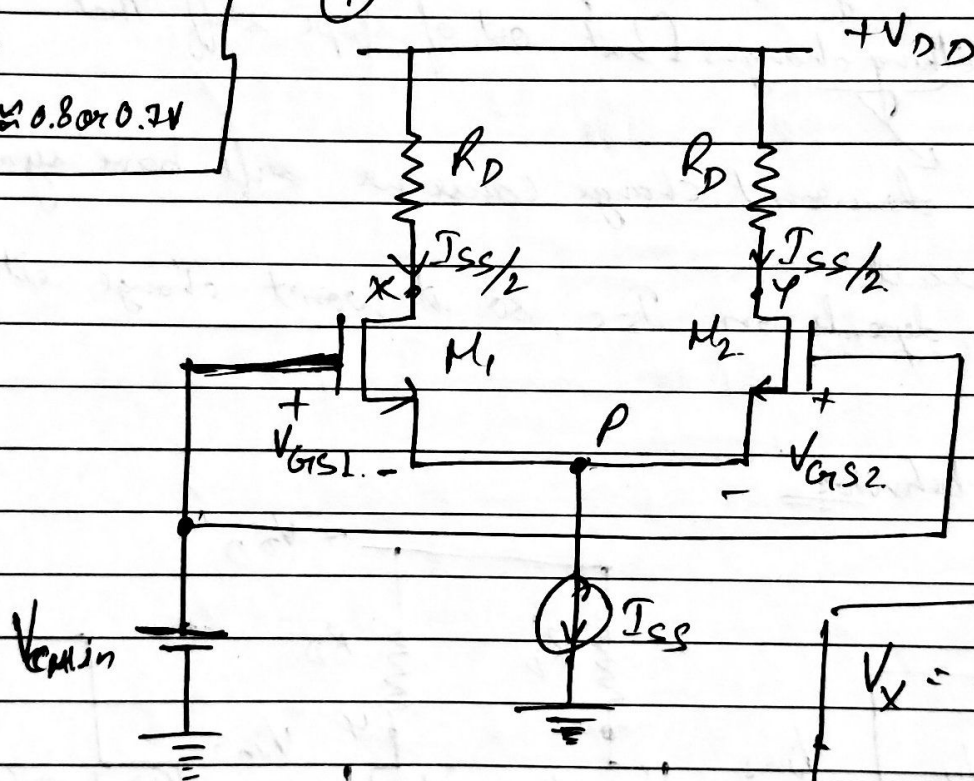
# \* MOS Differential Pair



## \* General Properties

Let  $V_{DD} = 1V$

then  $V_{min} \approx 0.8 \text{ or } 0.7V$



$$V_X = V_Y = V_{DD} - R_D \frac{I_{SS}}{2}$$

$$V_{GS1} = V_{GS2}$$

Since Gate & Source voltages are same.

$$I_D = \frac{1}{2} \mu_n C_{ox} \left( \frac{W}{L} \right) \left( V_{GS} - V_{th} \right)^2 \quad \text{Set } [M_1]$$

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Now,  $V_{GS1} = \sqrt{\frac{I_{SS}}{\mu_n C_{ox} \frac{W}{L}}} + V_{tn}$   $I_{SS} = I_D$

$V_{GS} - V_{tn} = V_{ov}$  [overdrive voltage]

→ If  $V_{in1} \approx V_{in2}$  then differential pair is in equilibrium,

→ The equilibrium overdrive voltage.

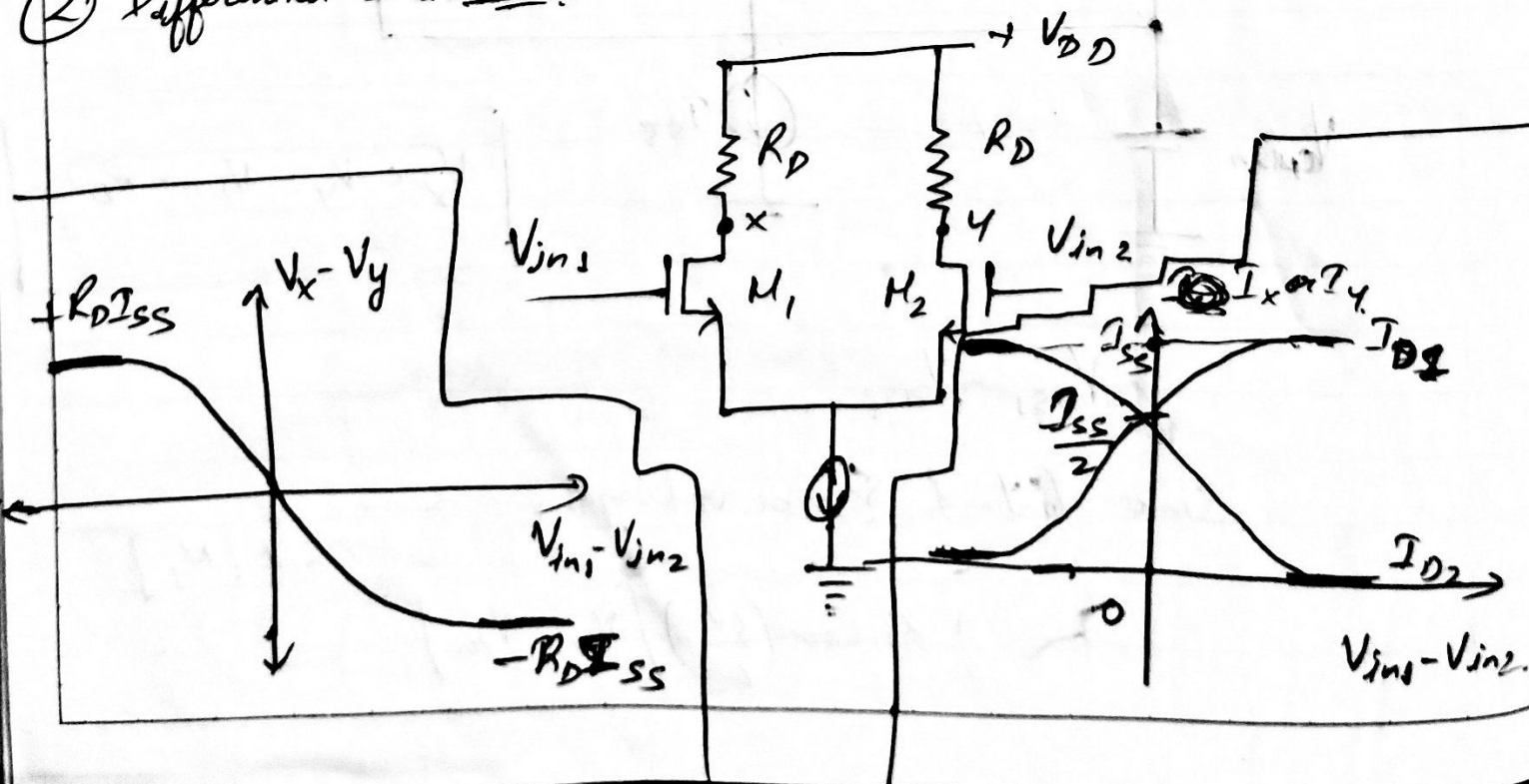
→  $V_{GS} - V_{tn} = \sqrt{\frac{I_{SS}}{\mu_n C_{ox} \frac{W}{L}}}$

① If  $V_{cm}$  changes, what quantities in the circuit change  
Ans → Nothing changes (but out of scope stuff that  $V_p$  changes)

→  $I_{SS}$  won't change cause we still have symmetry in the circuit

→  $V_{GS1}$  depends on  $I_{SS}$ , so it won't change either.

② Differential behavior.



$$I_D = \frac{1}{2} \mu_n C_{ox} \left( \frac{W}{L} \right) [V_{GS} - V_{th}]^2$$

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Condition for satisfying saturation equation:-

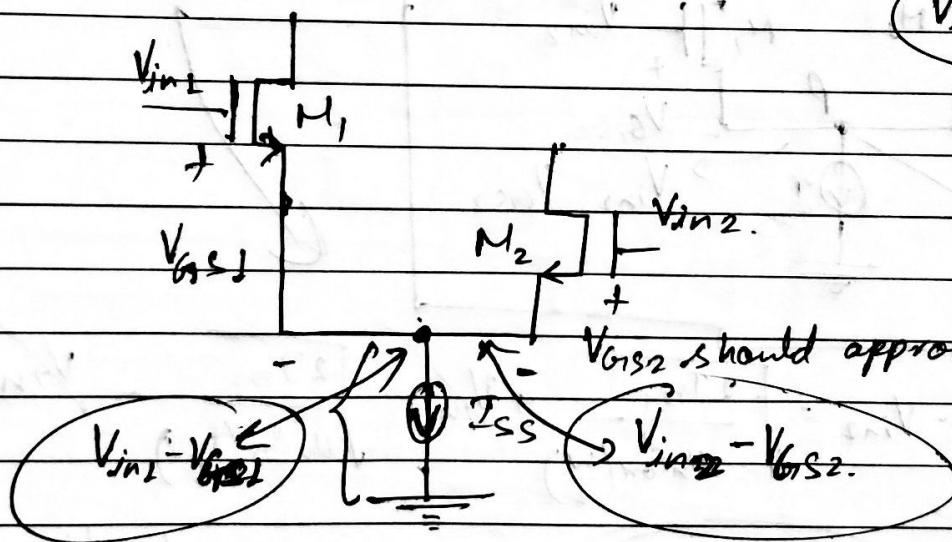
① The device should be on line  $V_{GS} \geq V_{th}$

②  $V_{DS} > V_{GS}$

Example:-

What is the minimum value of  $V_{in1} - V_{in2}$  at which one transistor turns off?

$V_{in2}$  is decreasing



$V_{GS2}$  should approach  $V_{th}$  so,  $I_D$  is approaching zero.

$$V_{in1} - V_{GS1}$$

$$V_{in2} - V_{GS2}$$

$$V_{GS1} = \sqrt{\frac{2 I_{SS}}{\mu_n C_{ox} \left( \frac{W}{L} \right)}} + V_{th} + V_{th}$$

$$\frac{I_{SS}}{2} \rightarrow I_{SS}$$

Since  $I_D$  for  $M_2$  is  $\frac{I_{SS}}{2} \rightarrow 0$ .

$$V_{in1} - V_{in2} = V_{GS1} - V_{GS2}$$

$$\text{So, } V_{in1} - V_{in2} = \sqrt{\frac{2 I_{SS}}{\mu_n C_{ox} \left( \frac{W}{L} \right)}} + V_{th} - V_{th}$$

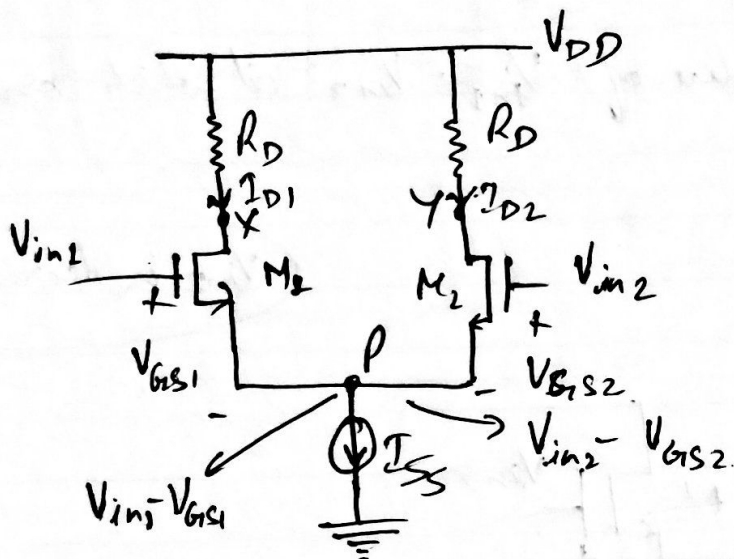
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So,

$$V_{in1} - V_{in2} = \sqrt{\frac{2I_{SS}}{\mu_n C_{ox} \left(\frac{W}{L}\right)}}$$

\* Large-Signal Analysis [Equation of  $V_x - V_y$  in terms of  $V_{in1} - V_{in2}$ ]  $\Rightarrow$  Objective.



Always  $[ < V_L ]$ :-  
 $V_{in1} - V_{in2} = V_{GS1} - V_{GS2}$

$$V_{in1} - V_{in2} = \sqrt{\frac{2I_{D1}}{\mu_n C_{ox} \left(\frac{W}{L}\right)}} + V_{th} - \sqrt{\frac{2I_{D2}}{\mu_n C_{ox} \left(\frac{W}{L}\right)}} - V_{th} \rightarrow (1)$$

$I_{D1}, I_{D2} \neq 0 \rightarrow M_1, M_2$  are not off.

We know that from Bipolar analysis  $< V_L$   
 $V_x - V_y = -R_D I$

$$V_x = V_{DD} - R_D I_{D1}$$

$$V_y = V_{DD} - R_D I_{D2}$$

$$V_x - V_y = -R_D [I_{D1} - I_{D2}] \rightarrow (2)$$

Express  $I_{D1} - I_{D2}$  in terms of  $V_{in1} - V_{in2}$   $\rightarrow$  Objective.

Since we know,  $I_{D1} + I_{D2} = I_{SS} \rightarrow (3)$

So,

Squaring Eq<sup>n</sup> (1)

$$[V_{in1} - V_{in2}]^2 = \frac{2I_{D1}}{\mu_n C_{ox}(\frac{W}{L})} + \frac{2I_{D2}}{\mu_n C_{ox}(\frac{W}{L})} - 2 \frac{4I_{D1}I_{D2}}{[\mu_n C_{ox}(\frac{W}{L})]^2}$$

$$[V_{in1} - V_{in2}]^2 = \frac{2}{\mu_n C_{ox}(\frac{W}{L})} \left[ \frac{I_{D1} + I_{D2}}{1} - 2\sqrt{I_{D1}I_{D2}} \right]$$

Now,

Using Eq<sup>n</sup> (3)

$$\text{So, } [V_{in1} - V_{in2}]^2 = \frac{2}{\mu_n C_{ox}(\frac{W}{L})} \left[ I_{SS} - 2\sqrt{I_{D1}(I_{SS} - I_{D1})} \right]$$

~~Now, square again Eq<sup>n</sup> (4) to find  $I_{D1}$  in terms of  $V_{in1} - V_{in2}$~~

~~$$\text{So, } [V_{in1} - V_{in2}]^4 = \frac{4}{[\mu_n C_{ox}(\frac{W}{L})]^2} \left[ I_{SS}^2 + 4I_{D1}(I_{SS} - I_{D1}) - 4I_{SS} \right]$$~~

$$\frac{[V_{in1} - V_{in2}]^2}{\ln \coth \left( \frac{W}{L} \right)} = - \frac{4 \sqrt{I_{D1} (I_{SS} - I_{D1})}}{\ln \coth \left( \frac{W}{L} \right)} \rightarrow (4)$$

Now, again square both sides to get value of  $I_{D1}$  in terms of  $(V_{in1} - V_{in2})$ .

$$\begin{aligned} [V_{in1} - V_{in2}]^4 + \frac{4 I_{SS}^2}{\left( \ln \coth \frac{W}{L} \right)^2} &= \frac{4 I_{SS} (V_{in1} - V_{in2})^2}{\ln \coth \frac{W}{L}} \\ &= \frac{16 [I_{D1} (I_{SS} - I_{D1})]}{\left( \ln \coth \frac{W}{L} \right)^2} \end{aligned}$$

After the math  
we'll get

$$I_{D1} - I_{D2} = \frac{1}{2} \ln \coth \left( \frac{W}{L} \right) [V_{in1} - V_{in2}] \sqrt{\frac{4 I_{SS}}{\ln \coth \frac{W}{L}} - \frac{(V_{in1} - V_{in2})^2}{\ln \coth \frac{W}{L}}}$$

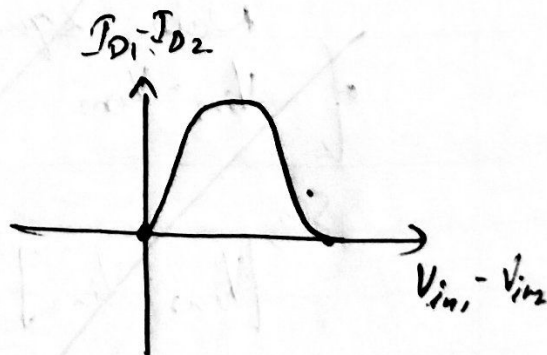
Large Signal  
behaviour for  
Differential Pair.

Now,

$$V_x - V_y = -R_D (I_{D1} - I_{D2})$$

⇒ Observations

- ①  $I_{D1} - I_{D2} = 0$  if  $V_{in1} = V_{in2}$
- ② The equation is not applicable for large value of  $V_{in1} - V_{in2}$ . Since one of the transistor will turn off if  $V_{in1} - V_{in2}$  becomes large.



Now continuing the point ② that what should be the max of  $V_{in1} - V_{in2}$ .

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So,

$$V_{in1} - V_{in2} \geq V_{G1} - V_{G2} = \sqrt{\frac{2I_{SS}}{\mu_n C_{ox} \frac{W}{L}}}$$

→ As previously shown.

③ So, Equation (A) is valid only if  $M_1$  &  $M_2$  are ON or at the edge of turning OFF, up to

$$|V_{in1} - V_{in2}| = \sqrt{\frac{2I_{SS}}{\mu_n C_{ox} \frac{W}{L}}}$$