

CS 110 : Assembly Language and Programming Semester - 4

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Introduction to Assembly Language

Overview

Assembly Language is a low-level programming language. In computers, there is an assembler that helps in converting the assembly code into machine code executable. Assembly language is designed to understand the instruction and provide it to machine language for further processing. It mainly depends on the architecture of the system, whether it is the operating system or computer architecture.

Assembly Language mainly consists of mnemonic processor instructions or data and other statements or instructions. Assembly Language helps in fine-tuning the program.

What is Language?

Language is a mode of communication that is used to share ideas, opinions with each other. For example, if we want to teach someone, we need a language that is understandable by both communicators.



What is a Programming Language?

A programming language is a computer language that is used by programmers (developers) to communicate with computers. It is a set of instructions written in any specific language (C, C++, Java, Python) to perform a specific task.

Types of Programming Languages

Machine Language

Considered a native language as it can be directly understood by a (CPU). It's not so easy to understand, as the language uses the binary system in which the commands are written in 1 and 0 form which is not easy to interpret.

Assembly Language

Low-level programming language in which there is a very strong correspondence between the instructions in the language and the architecture's machine code instructions.

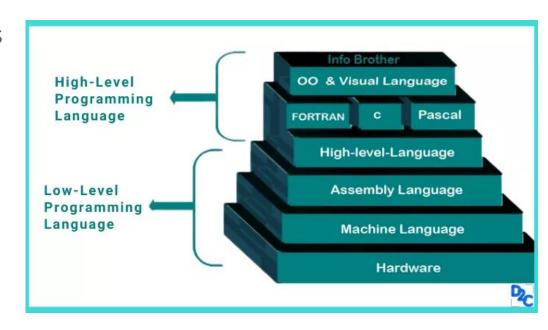
High Level Language

Easy to understand and the code can be written easily as the programs written are user-friendly. The other advantage of code written is independent of a computer system The high-level of language uses the concept of abstraction

High-Level Language

Most programming now-a-days is done using so-called "high-level" languages (such as

FORTRAN, BASIC, COBOL, PASCAL, C, C++, JAVA, SCHEME, Lisp, ADA, etc.)



High-Level Language

"High-level language" refers to the higher level of abstraction from machine language. Rather than dealing with registers, memory addresses, and call stacks, high-level languages deal with variables, arrays, objects, complex arithmetic or boolean expressions, subroutines and functions, loops, threads, locks, and other abstract computer science concepts.

with strong abstraction from the details of the computer. it may use natural language elements, be easier to use, the amount of abstraction provided defines how "high-level" a programming language is



Programmer writes code in high level language Code is turned into machine instructions by compiler or interpreter

Computer runs machine instructions

0010101011101001 0101101010101100 0101101101001 01011010101011001 01011010101011100111

Compiler

A compiler is a special program that processes statements written in a particular programming language and turns them into machine language or "code" that a computer's processor uses. Typically, a programmer writes language statements in a language such as Pascal or C



Low-level programming language

Low-level language is machine-dependent (0s and 1s) programming language. The processor runs low-level programs directly without the need of a compiler or interpreter, so the programs written in low-level language can be

run very fast

Low-level language is further divided into two parts -

- i. Machine Language
- ii. Assembly Language

Machine code

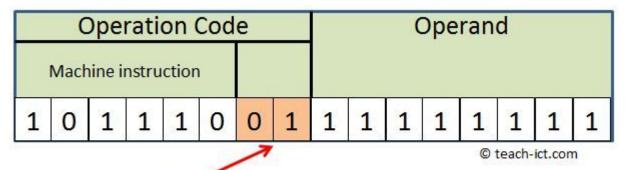
Machine code, also known as machine language, is the elemental language of computers. It is read by the computer's central processing unit (CPU), is composed of digital binary numbers and looks like a very long sequence of zeros and ones.

Each CPU has its own specific machine language. The processor reads and handles instructions, which tell the CPU to perform a simple task. Instructions are comprised of a certain number of bits

If instructions for a particular processor are 8 bits, for example, the first 4 bits part (the opcode) tells the computer what to do and the second 4 bits (the operand) tells the computer what data to use.

Example of 32 bit instructions 01001000 00110101 01101100 01101100

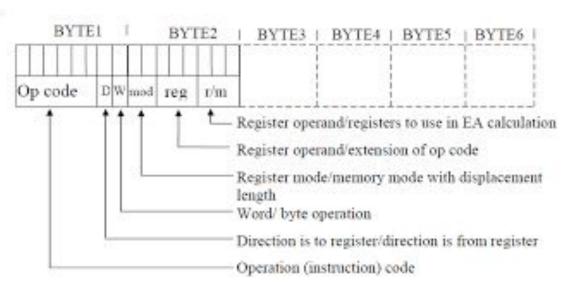
The 32-bit instruction may have six fields: cond, op, funct, Rn, Rd, and Src2. The operation the instruction performs is encoded in the field: op (also called the opcode or operation code) and funct or function code; the cond field encodes conditional execution based on flags



Address Mode

Depending upon the processor, a computer's instruction sets may all be the same length, or they may vary, depending upon the specific instruction

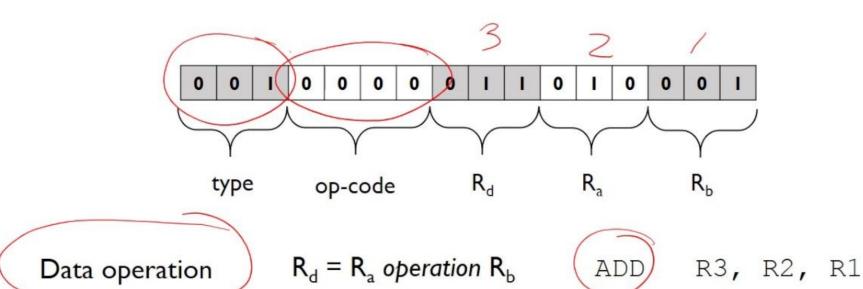
The architecture of the particular processor determines how instructions are patterned



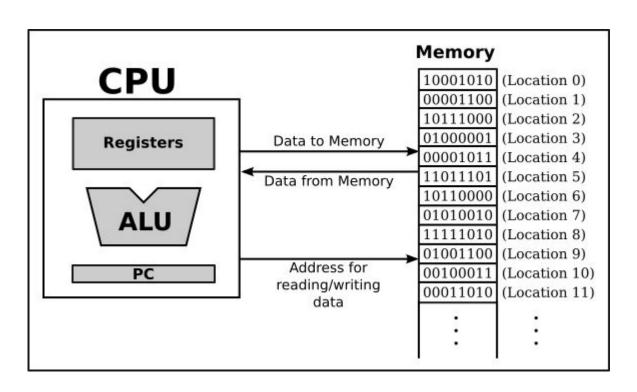
Instruction format

- The fictitious example below shows a 16-bit instruction
- It has a 3-address format

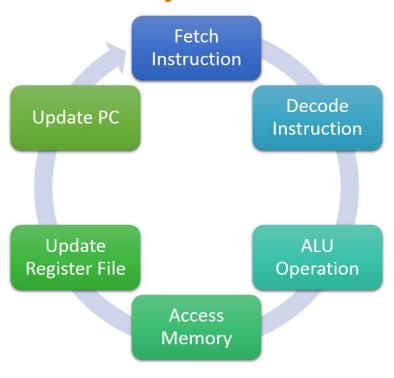
R2 + R,



How Does CPU execute these?



The Fetch and Execute Cycle



Disadvantages of Machine language

- It is machine dependent i.e. it differs from computer to computer.
- It is difficult to program and write.
- It is prone to errors
- It is difficult to modify.

Assembly language

It is a low level programming language that allows a user to write a program using alphanumeric mnemonic of instructions. It requires a translator as assembler to convert language into machine language so that it can be understood by the computer.

It is easier to remember and write than machine language.

Assembly vs Machine Code

```
Machine code bytes
                   Assembly language statements
                   foot
B8 22 11 00 FF
                   mov1 $0xFF001122, %eax
01 CA
                   addl %ecx, %edx
31 F6
                   xorl %esi, %esi
53
                  pushl %ebx
                  movl 4(%esp), %ebx
8B 5C 24 04
8D 34 48
                   leal (%eax, %ecx, 2), %esi
39 C3
                   cmpl %eax, %ebx
72 EB
                   jnae foo
C3.
                   retl
Instruction stream
B8 22 11 00 FF 01 CA 31 F6 53 8B 5C 24
04 8D 34 48 39 C3 72 EB C3
```

What's new

Using alphanumeric mnemonic codes instead of numeric codes for the instruction in the instruction set e.g. using ADD instead of 1110 (binary) or 14 (decimal) for instruction to add.

Allowing storage location to be represented in form of alphanumeric address instead of numeric address e.g. representing memory locations 1000, 1001 etc.



Advantage(s)

Advantage(s) of using assembly language rather than

machine language is/are:

A. It is mnemonic and easy to read

B. Addresses any symbolic, not absolute

C. Introduction of data to program is easier

D. easy to locate and correct errors.



Key Ideas

There are two key ideas:

- -- mnemonic opcodes: we use abbreviations of English language words to denote operations
- -- symbolic addresses: we invent "meaningful" names for memory storage locations we need

These make machine-language understandable to humans – if they know their machine's design

Let's see our example-program, rewritten using actual "assembly language" for Intel's Pentium

32-bit Instructions

- Instructions are represented in memory by a series of "opcode bytes."
- A variance in instruction size means that disassembly is position specific.
- Most instructions take zero, one, or two arguments:

instruction destination, source

For example: add eax, ebx is equivalent to the expression eax = eax + ebx

Assembly Programming

Assembly Language instruction consist of four fields

[label:] mnemonic [operands] [;comment]

- Labels
 - See rules
- mnemonic, operands
 - MOV AX, 6764
- comment
 - ; this is a sample program

General Format

mnemonic

operand(s)

;comments

MOV

destination, source

;copy source operand to destination

Example:

MOV DX,CX

Example 2:

MOV CL,55H MOV DL,CL MOV AH,DL MOV AL,AH MOV BH,CL MOV CH,BH

AH	AL
ВН	BL
СН	CL
DH	DL

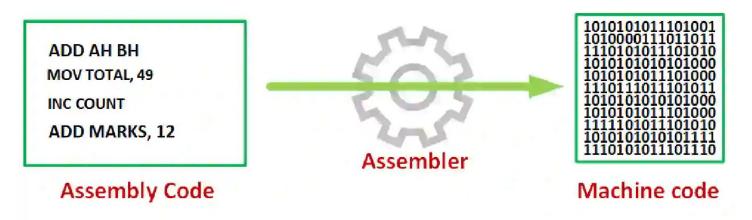
Instructions

Instructions

- * Each instruction does something relatively simple.
 - move some bits around
 - treat some bits as base 2 numbers and apply arithmetic operations.
 - send/read some bits to/from I/O devices.
 - select the group of bits that will make up the next instruction

Assembler

It is a computer programme which converts or translate assembly language into machine language. It assembles the machine language program in the main memory of the computer and makes it ready for execution.



Opcode

In computing, an opcode (abbreviated from operation code, also known as instruction machine code, instruction code, instruction syllable, instruction parcel or opstring) is the portion of a machine language instruction that specifies the operation to be performed.

DATA TRANSFER	ARITHMETIC	LOGICAL	BOOLEAN	PROGRAM BRANCHING
MOV	ADD	ANL	CLR	LJMP
MOVC	ADDC	ORL	SETB	AJMP
MOVX	SUBB	XRL	MOV	SJMP
PUSH	INC	CLR	JC	JZ
POP	DEC	CPL	JNC	JNZ
XCH	MUL	RL	JВ	CJNE
XCHD	DIV	RLC	JNB	DJNZ
	DA A	RR	JBC	NOP
		RRC	ANL FLES	LCALL
		SWAP	ORL	ACALL
			CPL	RET
				RETI
				JMP

Functional Group	Example Mnemonics		
Move Instructions	MOV		
Math Instructions	MUL DIV ADD SUB		
Logic Instructions	AND IOR XOR NEG		
Rotate/Shift Instructions	ASR LSR SL		
Bit Instructions	BSET BCLR BTG BTST		
Compare/Skip/Branch	BTSC BTSS CPBEQ CPBGT		
Flow Control Instructions	BRA CALL RCALL REPEAT		
Shadow/Stack Instructions	LNK POP PUSH ULNK		
Control Instructions	NOP CLRWDT PWRSAV RESET		
DSP Instructions	MAC LAC SAC SFTAC		

Labels

A label can be placed at the beginning of a statement. During assembly, the label is assigned the current value of the active location counter and serves as an instruction operand.

Acts as a place marker

There are two types of lables: symbolic and numeric

Assembler Directives

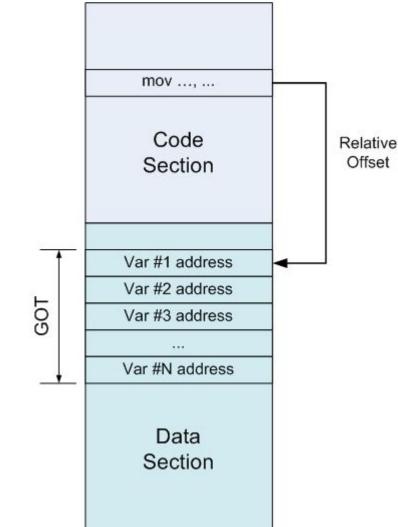
Assembler directives are the instructions used by the assembler at the time of assembling a source program.

More specifically, we can say, assembler directives are the commands or instructions that control the operation of the assemble

Basic Assembly Program

An assembly program can be divided into three sections –

- The data section,
- The stack section, and
- The text section.



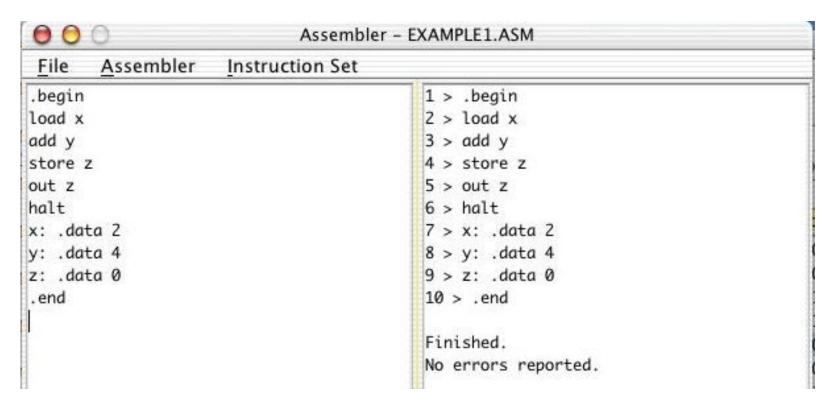
Comments

In computer programming, a comment is a programmer-readable explanation or annotation in the source code of a computer program. They are added with the purpose of making the source code easier for humans to understand, and are generally ignored by compilers and interpreters

Assembly translates to Machine Code

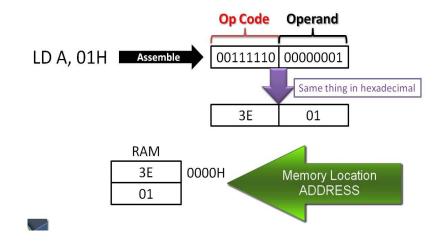
Machine code	Assembly code	Description
001 1 000010	LOAD #2	Load the value 2 into the Accumulator
010 0 001101	STORE 13	Store the value of the Accumulator in memory location 13
001 1 000101	LOAD #5	Load the value 5 into the Accumulator
010 0 001110	STORE 14	Store the value of the Accumulator in memory location 14
001 0 001101	LOAD 13	Load the value of memory location 13 into the Accumulator
011 0 001110	ADD 14	Add the value of memory location 14 to the Accumulator
010 0 001111	STORE 15	Store the value of the Accumulator in memory location 15
111 0 000000	HALT	Stop execution

Example assembly program



Interpreting an Assembly Instruction

To interpret machine language, one must decipher the fields of each instruction word. Suppose there is a 32 bit instruction, different instructions use different formats, but all formats start with a 6-bit opcode field. Thus, the best place to begin is to look at the opcode. If it is 0, the instruction is R-type; otherwise it is l-type or J-type.



Translating Memory Instructions into Machine Language

Example, Translate the following assembly language statement into machine language.

STR R11, [R5], #-26

STR is a memory instruction, so it has an op of 012. According to Table 6.11, L=0 and B=0 for STR. The instruction uses post-indexing, so according to Table 6.10, P=0 and W=0. The immediate offset is subtracted from the base, so I=1 and U=0. shows each field and the machine code. Hence, the machine language instruction is 0xE405B01A.

When do we use Assembly

Assembly-level programming is still written, and fairly often, as well.

The most traditional groups of people who write assembly are compiler and OS programmers, but it's also used in a wide swath of other applications

It's used in:

- Certain parts of video games
- Certain parts of virtual machines
- Most automobile software
- A lot of embedded systems



Disadvantages of Assembly Language

Disadvantages Assembly Language:

- 1. One of the major disadvantages is that assembly language is machine dependent. A program written for one computer might not run in other computers with different hardware configuration.
- 2. Long programs written in such languages cannot be executed on small sized computers.
- 3. It takes lot of time to code or write the program, as it is more complex in nature.

Conclusion : Assembly Language

An assembly language is a type of programming language that translates somewhat english like mnemonics and addresses directly into machine language.

It is a necessary bridge between software programs and their underlying hardware platforms.

Today, assemble languages are rarely written directly, although they are still used in some niche applications such as when performance requirements are particularly high.

Thank You