

Ankit Kumar Biswal

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OBJECTIVE:

Pursuing B. Tech Electronics and Communication currently final year, proficient in C++ ,JAVA, Verilog seeking an opportunity in field of software and hardware to work alongside industry professionals, enhance my technical expertise, and gain hands on experience in a dynamic and collaborative environment while contributing to the growth and success of the organization.

EDUCATION:

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|---|----------------------------------|
| • Vellore Institute of Technology(VIT) | CGPA: 8.19 (2021-Present) |
| • ODM Public School(Class 12th) | Percentage:90% (2020) |
| • Vagdevi Vilas School (Class 10th) | Percentage:84% (2018) |

EXPERIENCE:

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|---|-------------------------------------|
| • Intern at Ericsson | August,2023 - September,2023 |
| Experienced Java developer skilled in XML transformation, validation, parsing, marshalling and unmarshalling with Spring Boot for building robust and scalable application. | |
| • Intern at NLC India Limited | October,2023 - November,2023 |
| Worked on Optical fibre testing and maintenance, Switching Techniques, WIFI protocols, VoIP and error detection . | |

PROJECTS:

- **Antennas for vehicle mount anti-drone system- with Beam steering (Two set of arrays: 2.4-2.5 GHz and 5-6 GHz):** (*Ansys HFSS, Antenna*)
This paper presents the design and fabrication of an antipodal Vivaldi antenna for a vehicle-mounted anti-drone system with beam steering capabilities. Operating in the frequency ranges of 2.4-2.5 GHz and 5-6 GHz, the antenna achieves a gain of 11 dB at 5 GHz.
- **Brain Tumour Detection using Resnet50:** (*Jupiter Notebook, Flask, python, Deep Learning ,MRI*)
Detecting the tumour part of the MRI brain image using the neural network resnet50 which is a pretrained model and deploying it with the help of flask .
- **Patient Assistance Nurse Calling System:** (*Arduino ,Node-Red, HTML,CSS*)
The objective of the innovative nurse call system is to improve communication and response time between patients and healthcare providers have made use of Arduino for emergency calling, displaying and Node-Red for the Dashboard application receiving message.
- **16 bit Multiply Accumulate Unit(MAC Unit):** (*Modelsim, Verilog, Digital system Design*)
Developed a 16-bit Mac unit using row-by-passing method and the Modified Brent-kung adder, implemented and verified in Modelsim for optimized performance and efficiency.
- **Memristor Inverter Circuit Design:** (*Cadence Virtuoso, VLSI*)
CMOS-Memristor Inverter Circuit Design and Analysis using Cadence Virtuoso.

CERTIFICATIONS:

- Completed course on semiconductor design at Vellore Institute of Technology.
- Completed Course on NPTEL Google Cloud Computing.
- Completed CPP Course from IIT Bombay Spoken Tutorial.
- Completed C Course from IIT Bombay Spoken Tutorial.
- Completed JAVA Course from IIT Bombay Spoken Tutorial.

TECHNICAL SKILLS:

LANGUAGE: C++,JAVA,HTML , CSS, Python, Verilog, VHDL, Assembly language, SQL,XML.

TOOLS: VS CODE, Eclipse, Modelsim, MATLAB, Netsim, Cadence, NODE-RED, Jupiter Notebook, Ansys, Google Colab ,Simulink, Silvaco Tcad.

TECHNOLOGICAL FRAMEWORK: GitHub

HACKATHONS AND COMPETITIONS:

- Finalist in smart city hackathon conducted by IET and Nokia.
- Participated in Smart India Hackathon.

INTERESTS:

- Participating in Hackathons and working on social needs-oriented projects .
- Exploring new tools and frameworks.

LANGUAGE:

Proficient: English, Hindi, Oriya

Beginner: Japanese , Kannada, Telegu

DECLARATION:

I hereby declare that the above furnished information is true to best of my knowledge.

ANKIT KUMAR BISWAL