

Development of Virtual lab :Round 2 (R2)-Storyboard - Template (Worksheet)

Name of Faculty: Mr.Dhananjay Bisen

Institute: Rajkiya Engineering College Banda

Email ID (as submitted in the registration form): bisen.it2007@gmail.com

Discipline to which the Lab belongs: computer information technology

Name of the Lab: WELCOME TO LOGIC DESIGN AND COMPUTER ORGANISATION LAB

Name of experiment : carry look ahead adder

(only one Experiment per worksheet):

Kindly Refer these documents before filling the worksheet

Coursework (MOOC) on Pedagogy , Storyboard , Lab Manual : <http://bit.ly/Vlabs-MOOC>

Additional Documentation booklet for reference. <http://vlabs.iitb.ac.in/vlabs-dev/document.php>

Sample Git Repository. :<http://github.com/ankitkunta18/virtual-lab.git>

Round 2

1. Story Outline:

Choose the input A and B. select the last digit of A and B which will be denoted as A_i and B_i . choose another input $C_i(0,1)$. then with the help of logic gates P_i is calculated. now the carry input will be generated as G_i . now the value of sum and carry will be displayed as P_i and C_i through Exclusive OR gate.

2. Story:

2.1 Set the Visual Stage Description:

Inputs are given visually to the gates.Both the inputs entered passes through the gates and perform the operation of sum and carry.Then the respective outputs are displayed visually to the user so that the user can understand the experiment properly.

2.2 Set User Objectives & Goals:

The main objective of this experiment is to perform the experiment of carry look ahead adder virtually using gates.

The goal of this experiment is to make the user understand the concepts of carry look ahead adder virtually.

2.3 Set the Pathway Activities:

Input the four digit value in a and B .select the respective A_i and B_i . enter the value of C_i and hence the operation is performed .now the sum and carry is displayed.

2.4 Set Challenges and Questions/Complexity/Variations in Questions:

Q1. Which one of the following is better way to compute sum in less time?

Q2.Give the correct formula of P_i and G_i ?

2.5 Allow pitfalls:

1.Wrong implementation of procedure will generate wrong value.

2.Knowledge of logic gates is required.

2.6 Conclusion:

The addition of two binary numbers in less time is displayed.

2.7 Equations/formulas:

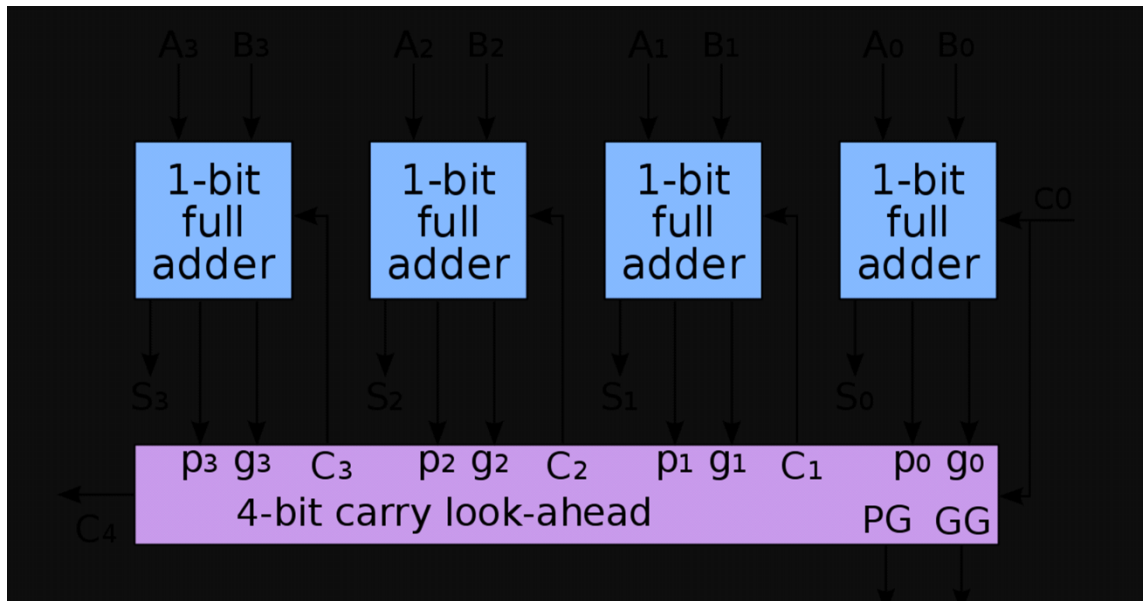
$$P_i = A_i * B_i + A_i \sim * B_i$$

$$G_i = A_i * B_i$$

$$S_i = P_i * C_i + P_i \sim * C_i$$

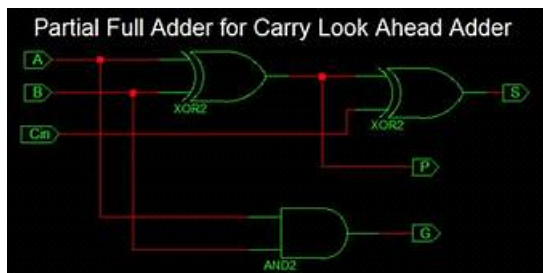
$$C_{(i+1)} = G_i + P_i * C_i$$

3. Flowchart



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4. Mindmap



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5. Storyboard

Choose the input A and B. select the last digit of A and B which will be denoted as A_i and B_i . choose another input $C_i(0,1)$. then with the help of logic gates P_i is calculated. now the array input will be generated as G_i . now the value of sum and carry will be displayed as P_i and C_i through Exclusive OR gate.

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