



TO WHOMSOEVER IT MAY CONCERN

This is to certify that Ankit Raj, a student of B.Tech ECE, 2024 from Vellore Institute of Technology, Vellore, Tamil Nadu has successfully completed his VLSI Design Internship Program from 15/05/2023 to 07/07/2023

During his internship program with us, he worked on the below mentioned project. AHB2APB bridge design using Verilog HDL.

We wish him all the best in his future endeavors.

We found him punctual, hardworking and inquisitive. He was very sincere during the tenure of his internship

For Maven Silicon Softech Ltd.

Sweety Dharamdasani Head - Learning & Development

Place: Bangalore Date: 24/07/2023

Email: hr@maven-silicon.com