

# Report Generated by Test Manager

**Title:** WDGBrakingLogic REQ-Based Tests

**Author:**






















**Date:** 22-Sep-2021 19:07:22

## Test Environment

Platform: PCWIN64

MATLAB: (R2022a)


## Summary

Name	Outcome	Duration (Seconds)
<a href="#">Results: 2021-Sep-22 19:07:11</a>	1  3 	7.192
 <a href="#">WDGBrakingLogic Test</a>	1  3 	7.193
 <a href="#">requirements based tests</a>		7.193
 <a href="#">TC1</a>		7.194
 <a href="#">interface tests</a>		0
 <a href="#">TC1</a>		0
 <a href="#">fault injection tests</a>		0
 <a href="#">TC1</a>		0.00099993
 <a href="#">resource usage tests</a>		0
 <a href="#">TC1</a>		0

## Results: 2021-Sep-22 19:07:11

Result Type: Result Set  
Parent: None  
Start Time: 22-Sep-2021 19:07:12  
End Time: 22-Sep-2021 19:07:19  
Outcome: Total: 4, Passed: 1, Disabled: 3

### Aggregated Coverage Results

Analyzed Model	Sim Mode	Complexity	Decision	Condition	MCDC	Execution	Relational Boundary
 <a href="#">WDGBrakingLogic</a>	Normal	21	70%	54%	27%	100%	4%

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## WDGBrakingLogic\_Test

### Test Result Information

Result Type: Test File Result  
Parent: [Results: 2021-Sep-22 19:07:11](#)  
Start Time: 22-Sep-2021 19:07:12  
End Time: 22-Sep-2021 19:07:19  
Outcome: Total: 4, Passed: 1, Disabled: 3  
Description:

Model Version: 4.0

Model Last Modified On: 22-Sep-2021 18:13:35

Checksum when Compiled as Referenced Model: 4047763966 1651424035  
440184956 1533764249

### Test Suite Information

Name: WDGBrakingLogic\_Test

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## requirements\_based\_tests

### Test Result Information

Result Type: Test Suite Result  
Parent: [WDGBrakingLogic Test](#)  
Start Time: 22-Sep-2021 19:07:12  
End Time: 22-Sep-2021 19:07:19  
Outcome: Total: 1, **Passed: 1**

### Test Suite Information

Name: requirements\_based\_tests  
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## TC1

### Test Result Information

Result Type: Test Case Result  
Parent: [requirements based tests](#)  
Start Time: 22-Sep-2021 19:07:12  
End Time: 22-Sep-2021 19:07:19  
Outcome: **Passed**

### Test Case Information

Name: TC1  
Type: Baseline Test  
Baseline Name: ReqBaseline.mat  
Baseline File: C:\Users\mabualqu\hlf2\ISO\_06\_09\_SwUVer\WPs\ISO\_6\_9\_5\_1\_SwVerSpec\WDGBrakingLogic\ReqBaseline.mat

### Test Case Requirements

Description: SW\_HLF#7.2.4.2 Partial Braking Level 1  
Document: SSRS.slreqx

Description: SW\_HLF#7.2.4.3 Partial Braking Level 2  
Document: SSRS.slreqx

Description: SW\_HLF#7.2.4.4 Full Braking

Document: SSRS.slreqx

Description: SW\_HLF#7.2.4.5 Idle state

Document: SSRS.slreqx

## Baseline Comparison

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✓ WDGBrakingLogic:1	0	0	0	0	0	BrStatus		0.1	BrStatus		0.1	zoh	union
✓ WDGBrakingLogic:2	0	0	0	0	0	double	m/s^2	0.1	double	m/s^2	0.1	zoh	union

## Simulation

### System Under Test Information

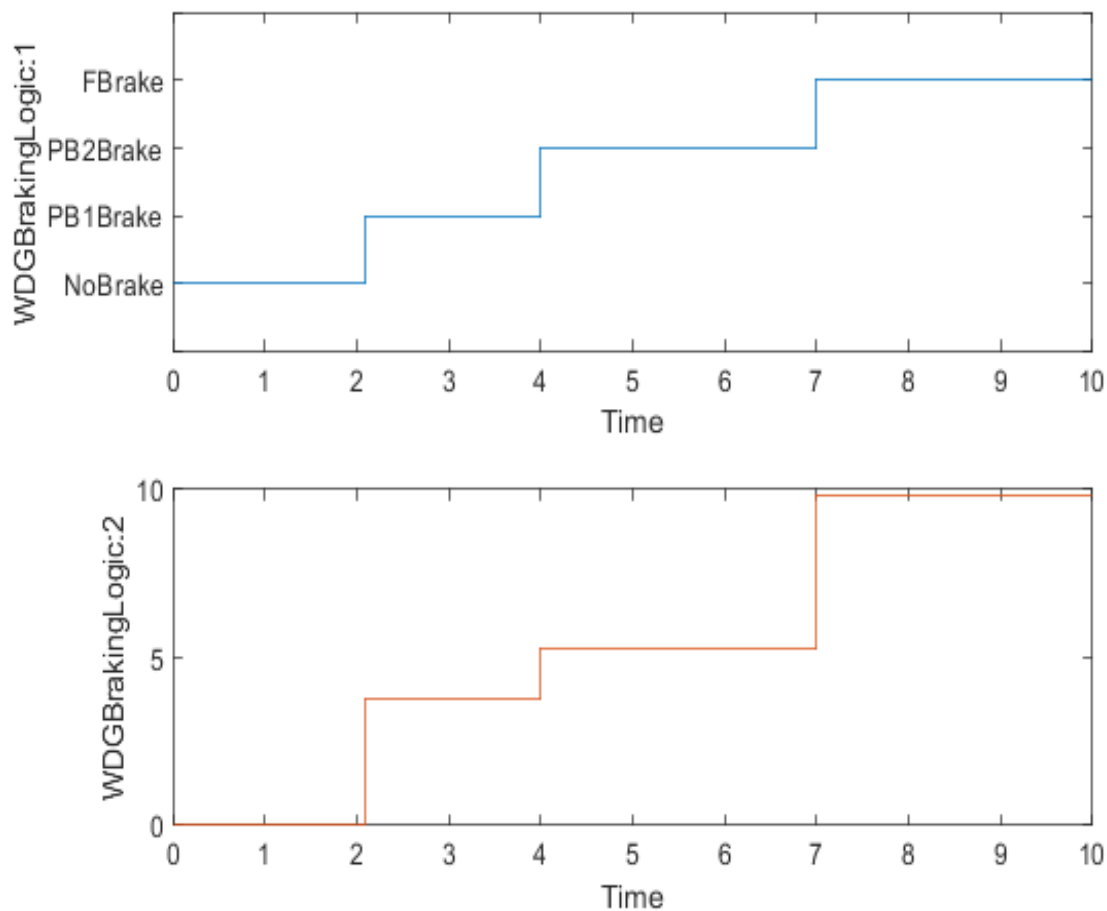
Model: WDGBrakingLogic  
Harness: WDGBrakingLogic\_Harness  
Harness Owner: WDGBrakingLogic  
Release: Current  
Simulation Mode: normal  
Override SIL or PIL Mode: 0  
Configuration Set: csMultiInstance  
Start Time: 0  
Stop Time: 10  
Checksum: 3037473055 3838432956 20540375 1193216764  
Simulink Version: 10.4  
Model Version: 3.0  
Model Author: mabualqu  
Date: Wed Sep 22 18:13:39 2021  
User ID: mabualqu  
Model Path: C:\Users\mabualqu\hlf2\ISO\_06\_08\_SwU\WPs\ISO\_06\_08\_5\_1\_SwUnDesSpec\WDGBrakingLogic\WDGBrakingLogic\_Harness.slx  
Machine Name: BAT917931WIN64

Solver Name: FixedStepDiscrete  
Solver Type: Fixed-Step  
Fixed Step Size: 0.10000000000000001  
Simulation Start Time: 2021-09-22 19:07:12  
Simulation Stop Time: 2021-09-22 19:07:17  
Platform: PCWIN64

## Simulation Output

Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
WDGBrakingLogic:1	BrStatus		0.1	zoh	union	<a href="#">Link</a>
WDGBrakingLogic:2	double	m/s^2	0.1	zoh	union	<a href="#">Link</a>

Name	Data Type	Units	Sample Time	Interp	Sync
WDGBrakingLogic:1	BrStatus		0.1	zoh	union
WDGBrakingLogic:2	double	m/s^2	0.1	zoh	union



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Simulation Logs:

Symbol 'BusMultiObjectTracker' is defined in [DD HLF DataTypes.sldd](#) and [base workspace](#). The definition in [DD HLF DataTypes.sldd](#) is used.

Symbol 'BusMultiObjectTrackerTracks' is defined in [DD HLF DataTypes.sldd](#) and [base workspace](#). The definition in [DD HLF DataTypes.sldd](#) is used.

Symbol 'BusRadarDetectionsObjectAttributes' is defined in [DD\\_HLF\\_DataTypes.sldd](#) and [base workspace](#). The definition in [DD\\_HLF\\_DataTypes.sldd](#) is used.

Symbol 'BusVehiclePose' is defined in [DD\\_HLF\\_DataTypes.sldd](#) and [base workspace](#). The definition in [DD\\_HLF\\_DataTypes.sldd](#) is used.

Symbol 'LaneSensor' is defined in [DD\\_HLF\\_DataTypes.sldd](#) and [base workspace](#). The definition in [DD\\_HLF\\_DataTypes.sldd](#) is used.

Symbol 'LaneSensorBoundaries' is defined in [DD\\_HLF\\_DataTypes.sldd](#) and [base workspace](#). The definition in [DD\\_HLF\\_DataTypes.sldd](#) is used.

Symbol 'Ts' is defined in [DD\\_HLF\\_Configuration.sldd](#) and [base workspace](#). The definition in [DD\\_HLF\\_Configuration.sldd](#) is used.

Symbol 'max\_ac' is defined in [DD\\_PFC\\_Configuration.sldd](#) and [base workspace](#). The definition in [DD\\_PFC\\_Configuration.sldd](#) is used.

Symbol 'max\_dc' is defined in [DD\\_PFC\\_Configuration.sldd](#) and [base workspace](#). The definition in [DD\\_PFC\\_Configuration.sldd](#) is used.

Symbol 'tau' is defined in [DD\\_PFC\\_Configuration.sldd](#) and [base workspace](#). The definition in [DD\\_PFC\\_Configuration.sldd](#) is used.

Symbol 'tau2' is defined in [DD\\_PFC\\_Configuration.sldd](#) and [base workspace](#). The definition in [DD\\_PFC\\_Configuration.sldd](#) is used.

'[BrakeStatus](#)' is defined, but is never used in the Test Sequence block. [Delete this object.](#)

'[decel](#)' is defined, but is never used in the Test Sequence block. [Delete this object.](#)



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## interface\_tests

### Test Result Information

Result Type:	Test Suite Result
Parent:	<a href="#">WDGBrakingLogic Test</a>
Start Time:	22-Sep-2021 19:07:11
End Time:	22-Sep-2021 19:07:11
Outcome:	Total: 1, Disabled: 1

### Test Suite Information

Name: interface\_tests

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## TC1

### Test Result Information

Result Type:	Test Case Result
Parent:	<a href="#">interface_tests</a>
Start Time:	22-Sep-2021 19:07:11
End Time:	22-Sep-2021 19:07:11
Outcome:	Disabled

### Test Case Information

Name:	TC1
Type:	Baseline Test

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## fault\_injection\_tests

### Test Result Information

Result Type: Test Suite Result  
Parent: [WDGBrakingLogic Test](#)  
Start Time: 22-Sep-2021 19:07:11  
End Time: 22-Sep-2021 19:07:11  
Outcome: Total: 1, Disabled: 1

### Test Suite Information

Name: fault\_injection\_tests  
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## TC1

### Test Result Information

Result Type: Test Case Result  
Parent: [fault\\_injection\\_tests](#)  
Start Time: 22-Sep-2021 19:07:11  
End Time: 22-Sep-2021 19:07:11  
Outcome: Disabled

### Test Case Information

Name: TC1  
Type: Baseline Test  
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## resource\_usage\_tests

### Test Result Information

Result Type: Test Suite Result  
Parent: [WDGBrakingLogic Test](#)  
Start Time: 22-Sep-2021 19:07:11  
End Time: 22-Sep-2021 19:07:11  
Outcome: Total: 1, Disabled: 1

### Test Suite Information

Name: resource\_usage\_tests

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## TC1

### Test Result Information

Result Type: Test Case Result

Parent: [resource\\_usage\\_tests](#)

Start Time: 22-Sep-2021 19:07:11

End Time: 22-Sep-2021 19:07:11

Outcome: Disabled

### Test Case Information

Name: TC1

Type: Baseline Test

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