

Model Testing Report

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Table of Contents

1. Unit: ControllerModeSelector	3
1.1. Artifact Summary	3
1.2. Test Case Analysis	3
1.2.1. Requirements Linked to Tests	3
1.2.2. Tests Linked to Requirements	3
1.2.3. Tests Case Breakdown	4
1.3. Simulation Test Result Analysis	4
1.3.1. Model Test Status	4
1.3.2. Model Coverage	4
2. Unit: EstimateLaneCenter	5
2.1. Artifact Summary	5
2.2. Test Case Analysis	5
2.2.1. Requirements Linked to Tests	5
2.2.2. Tests Linked to Requirements	5
2.2.3. Tests Case Breakdown	6
2.3. Simulation Test Result Analysis	6
2.3.1. Model Test Status	6
2.3.2. Model Coverage	6
3. Unit: FindLeadCar	7
3.1. Artifact Summary	7
3.2. Test Case Analysis	7
3.2.1. Requirements Linked to Tests	7
3.2.2. Tests Linked to Requirements	7
3.2.3. Tests Case Breakdown	8
3.3. Simulation Test Result Analysis	8
3.3.1. Model Test Status	8
3.3.2. Model Coverage	8
4. Unit: LaneCenterCoordChange	9
4.1. Artifact Summary	9
4.2. Test Case Analysis	9
4.2.1. Requirements Linked to Tests	9
4.2.2. Tests Linked to Requirements	9
4.2.3. Tests Case Breakdown	10
4.3. Simulation Test Result Analysis	10
4.3.1. Model Test Status	10
4.3.2. Model Coverage	10
5. Unit: MPCController	11
5.1. Artifact Summary	11
5.2. Test Case Analysis	11
5.2.1. Requirements Linked to Tests	11
5.2.2. Tests Linked to Requirements	11
5.2.3. Tests Case Breakdown	12
5.3. Simulation Test Result Analysis	12

5.3.1. Model Test Status	12
5.3.2. Model Coverage.....	12
6. Unit: PreviewCurvature	13
6.1. Artifact Summary.....	13
6.2. Test Case Analysis.....	13
6.2.1. Requirements Linked to Tests.....	13
6.2.2. Tests Linked to Requirements.....	13
6.2.3. Tests Case Breakdown	14
6.3. Simulation Test Result Analysis	14
6.3.1. Model Test Status	14
6.3.2. Model Coverage.....	14
7. Unit: WDGBrakeTimeCalc	15
7.1. Artifact Summary.....	15
7.2. Test Case Analysis.....	15
7.2.1. Requirements Linked to Tests.....	15
7.2.2. Tests Linked to Requirements.....	15
7.2.3. Tests Case Breakdown	16
7.3. Simulation Test Result Analysis	16
7.3.1. Model Test Status	16
7.3.2. Model Coverage.....	16
8. Unit: WDGBrakingLogic	17
8.1. Artifact Summary.....	17
8.2. Test Case Analysis.....	17
8.2.1. Requirements Linked to Tests.....	17
8.2.2. Tests Linked to Requirements.....	17
8.2.3. Tests Case Breakdown	18
8.3. Simulation Test Result Analysis	18
8.3.1. Model Test Status	18
8.3.2. Model Coverage.....	18
9. Unit: WDGTTCalculation	19
9.1. Artifact Summary.....	19
9.2. Test Case Analysis.....	19
9.2.1. Requirements Linked to Tests.....	19
9.2.2. Tests Linked to Requirements.....	19
9.2.3. Tests Case Breakdown	20
9.3. Simulation Test Result Analysis	20
9.3.1. Model Test Status	20
9.3.2. Model Coverage.....	20

1. Unit: ControllerModeSelector

1.1. Artifact Summary

Artifact Group	Artifact Type	Number of Artifacts
Requirements	Functional requirement	1
Design	Block diagram	1
	Model reference	0
	Subsystem	0
	Stateflow chart	0
	MATLAB function	0
	Data dictionary file	7
Tests	Test case	4
	Test case result	8

1.2. Test Case Analysis

1.2.1. Requirements Linked to Tests

1 out of 1 unit requirements with links to tests

0 unit requirements missing linked tests

1.2.1.1. Tests per Requirement

Linked Test Cases	0	1	2	3	>3
Requirements	0	1	0	0	0

1.2.2. Tests Linked to Requirements

1 out of 4 unit tests with links to requirements

3 unit tests missing linked requirements

1.2.2.1. Requirements per Test

Linked Requirements	0	1	2	3	>3
Test Cases	3	1	0	0	0

1.2.3. Tests Case Breakdown

Tests by Type

Type	Test Cases
Simulation	0
Equivalence	0
Baseline	4

Tests with Tag

Tag	Test Cases
-	4

1.3. Simulation Test Result Analysis

1.3.1. Model Test Status

2 out of 4 unit tests passed

0 unit tests failed

0 unit tests without pass/fail criteria

0 unit tests untested

2 unit tests disabled

1.3.2. Model Coverage

1.3.2.1. Aggregated Coverage

	Achieved	Justified	Missed
Execution	100%	0%	0%
Decision	100%	0%	0%
Condition	100%	0%	0%
MC/DC	100%	0%	0%

2. Unit: EstimateLaneCenter

2.1. Artifact Summary

Artifact Group	Artifact Type	Number of Artifacts
Requirements	Functional requirement	6
Design	Block diagram	1
	Model reference	0
	Subsystem	4
	Stateflow chart	0
	MATLAB function	1
	Data dictionary file	4
Tests	Test case	4
	Test case result	0

2.2. Test Case Analysis

2.2.1. Requirements Linked to Tests

0 out of 6 unit requirements with links to tests

6 unit requirements missing linked tests

2.2.1.1. Tests per Requirement

Linked Test Cases	0	1	2	3	>3
Requirements	6	0	0	0	0

2.2.2. Tests Linked to Requirements

0 out of 4 unit tests with links to requirements

4 unit tests missing linked requirements

2.2.2.1. Requirements per Test

Linked Requirements	0	1	2	3	>3
Test Cases	4	0	0	0	0

2.2.3. Tests Case Breakdown

Tests by Type

Type	Test Cases
Simulation	0
Equivalence	0
Baseline	4

Tests with Tag

Tag	Test Cases
-	4

2.3. Simulation Test Result Analysis

2.3.1. Model Test Status

0 out of 4 unit tests passed

0 unit tests failed

0 unit tests without pass/fail criteria

4 unit tests untested

0 unit tests disabled

2.3.2. Model Coverage

2.3.2.1. Aggregated Coverage

	Achieved	Justified	Missed
Execution	No data available	No data available	No data available
Decision	No data available	No data available	No data available
Condition	No data available	No data available	No data available
MC/DC	No data available	No data available	No data available

3. Unit: FindLeadCar

3.1. Artifact Summary

Artifact Group	Artifact Type	Number of Artifacts
Requirements	Functional requirement	4
Design	Block diagram	1
	Model reference	0
	Subsystem	0
	Stateflow chart	0
	MATLAB function	1
	Data dictionary file	4
Tests	Test case	4
	Test case result	0

3.2. Test Case Analysis

3.2.1. Requirements Linked to Tests

0 out of 4 unit requirements with links to tests

4 unit requirements missing linked tests

3.2.1.1. Tests per Requirement

Linked Test Cases	0	1	2	3	>3
Requirements	4	0	0	0	0

3.2.2. Tests Linked to Requirements

0 out of 4 unit tests with links to requirements

4 unit tests missing linked requirements

3.2.2.1. Requirements per Test

Linked Requirements	0	1	2	3	>3
Test Cases	4	0	0	0	0

3.2.3. Tests Case Breakdown

Tests by Type

Type	Test Cases
Simulation	0
Equivalence	0
Baseline	4

Tests with Tag

Tag	Test Cases
-	4

3.3. Simulation Test Result Analysis

3.3.1. Model Test Status

0 out of 4 unit tests passed

0 unit tests failed

0 unit tests without pass/fail criteria

4 unit tests untested

0 unit tests disabled

3.3.2. Model Coverage

3.3.2.1. Aggregated Coverage

	Achieved	Justified	Missed
Execution	No data available	No data available	No data available
Decision	No data available	No data available	No data available
Condition	No data available	No data available	No data available
MC/DC	No data available	No data available	No data available

4. Unit: LaneCenterCoordChange

4.1. Artifact Summary

Artifact Group	Artifact Type	Number of Artifacts
Requirements	Functional requirement	1
Design	Block diagram	1
	Model reference	0
	Subsystem	0
	Stateflow chart	0
	MATLAB function	0
	Data dictionary file	4
Tests	Test case	6
	Test case result	0

4.2. Test Case Analysis

4.2.1. Requirements Linked to Tests

0 out of 1 unit requirements with links to tests

1 unit requirements missing linked tests

4.2.1.1. Tests per Requirement

Linked Test Cases	0	1	2	3	>3
Requirements	1	0	0	0	0

4.2.2. Tests Linked to Requirements

0 out of 6 unit tests with links to requirements

6 unit tests missing linked requirements

4.2.2.1. Requirements per Test

Linked Requirements	0	1	2	3	>3
Test Cases	6	0	0	0	0

4.2.3. Tests Case Breakdown

Tests by Type

Type	Test Cases
Simulation	0
Equivalence	0
Baseline	6

Tests with Tag

Tag	Test Cases
-	6

4.3. Simulation Test Result Analysis

4.3.1. Model Test Status

0 out of 6 unit tests passed

0 unit tests failed

0 unit tests without pass/fail criteria

6 unit tests untested

0 unit tests disabled

4.3.2. Model Coverage

4.3.2.1. Aggregated Coverage

	Achieved	Justified	Missed
Execution	No data available	No data available	No data available
Decision	No data available	No data available	No data available
Condition	No data available	No data available	No data available
MC/DC	No data available	No data available	No data available

5. Unit: MPCController

5.1. Artifact Summary

Artifact Group	Artifact Type	Number of Artifacts
Requirements	Functional requirement	8
Design	Block diagram	1
	Model reference	0
	Subsystem	1
	Stateflow chart	0
	MATLAB function	0
	Data dictionary file	5
Tests	Test case	4
	Test case result	0

5.2. Test Case Analysis

5.2.1. Requirements Linked to Tests

0 out of 8 unit requirements with links to tests

8 unit requirements missing linked tests

5.2.1.1. Tests per Requirement

Linked Test Cases	0	1	2	3	>3
Requirements	8	0	0	0	0

5.2.2. Tests Linked to Requirements

0 out of 4 unit tests with links to requirements

4 unit tests missing linked requirements

5.2.2.1. Requirements per Test

Linked Requirements	0	1	2	3	>3
Test Cases	4	0	0	0	0

5.2.3. Tests Case Breakdown

Tests by Type

Type	Test Cases
Simulation	0
Equivalence	0
Baseline	4

Tests with Tag

Tag	Test Cases
-	4

5.3. Simulation Test Result Analysis

5.3.1. Model Test Status

0 out of 4 unit tests passed

0 unit tests failed

0 unit tests without pass/fail criteria

4 unit tests untested

0 unit tests disabled

5.3.2. Model Coverage

5.3.2.1. Aggregated Coverage

	Achieved	Justified	Missed
Execution	No data available	No data available	No data available
Decision	No data available	No data available	No data available
Condition	No data available	No data available	No data available
MC/DC	No data available	No data available	No data available

6. Unit: PreviewCurvature

6.1. Artifact Summary

Artifact Group	Artifact Type	Number of Artifacts
Requirements	Functional requirement	2
Design	Block diagram	1
	Model reference	0
	Subsystem	0
	Stateflow chart	0
	MATLAB function	0
	Data dictionary file	5
Tests	Test case	4
	Test case result	0

6.2. Test Case Analysis

6.2.1. Requirements Linked to Tests

0 out of 2 unit requirements with links to tests

2 unit requirements missing linked tests

6.2.1.1. Tests per Requirement

Linked Test Cases	0	1	2	3	>3
Requirements	2	0	0	0	0

6.2.2. Tests Linked to Requirements

0 out of 4 unit tests with links to requirements

4 unit tests missing linked requirements

6.2.2.1. Requirements per Test

Linked Requirements	0	1	2	3	>3
Test Cases	4	0	0	0	0

6.2.3. Tests Case Breakdown

Tests by Type

Type	Test Cases
Simulation	0
Equivalence	0
Baseline	4

Tests with Tag

Tag	Test Cases
-	4

6.3. Simulation Test Result Analysis

6.3.1. Model Test Status

0 out of 4 unit tests passed

0 unit tests failed

0 unit tests without pass/fail criteria

4 unit tests untested

0 unit tests disabled

6.3.2. Model Coverage

6.3.2.1. Aggregated Coverage

	Achieved	Justified	Missed
Execution	No data available	No data available	No data available
Decision	No data available	No data available	No data available
Condition	No data available	No data available	No data available
MC/DC	No data available	No data available	No data available

7. Unit: WDGBrakeTimeCalc

7.1. Artifact Summary

Artifact Group	Artifact Type	Number of Artifacts
Requirements	Functional requirement	1
Design	Block diagram	1
	Model reference	0
	Subsystem	0
	Stateflow chart	0
	MATLAB function	0
	Data dictionary file	6
Tests	Test case	4
	Test case result	0

7.2. Test Case Analysis

7.2.1. Requirements Linked to Tests

0 out of 1 unit requirements with links to tests

1 unit requirements missing linked tests

7.2.1.1. Tests per Requirement

Linked Test Cases	0	1	2	3	>3
Requirements	1	0	0	0	0

7.2.2. Tests Linked to Requirements

0 out of 4 unit tests with links to requirements

4 unit tests missing linked requirements

7.2.2.1. Requirements per Test

Linked Requirements	0	1	2	3	>3
Test Cases	4	0	0	0	0

7.2.3. Tests Case Breakdown

Tests by Type

Type	Test Cases
Simulation	0
Equivalence	0
Baseline	4

Tests with Tag

Tag	Test Cases
-	4

7.3. Simulation Test Result Analysis

7.3.1. Model Test Status

0 out of 4 unit tests passed

0 unit tests failed

0 unit tests without pass/fail criteria

4 unit tests untested

0 unit tests disabled

7.3.2. Model Coverage

7.3.2.1. Aggregated Coverage

	Achieved	Justified	Missed
Execution	No data available	No data available	No data available
Decision	No data available	No data available	No data available
Condition	No data available	No data available	No data available
MC/DC	No data available	No data available	No data available

8. Unit: WDGBrakingLogic

8.1. Artifact Summary

Artifact Group	Artifact Type	Number of Artifacts
Requirements	Functional requirement	6
Design	Block diagram	1
	Model reference	0
	Subsystem	0
	Stateflow chart	1
	MATLAB function	0
	Data dictionary file	7
Tests	Test case	5
	Test case result	9

8.2. Test Case Analysis

8.2.1. Requirements Linked to Tests

4 out of 6 unit requirements with links to tests

2 unit requirements missing linked tests

8.2.1.1. Tests per Requirement

Linked Test Cases	0	1	2	3	>3
Requirements	2	4	0	0	0

8.2.2. Tests Linked to Requirements

1 out of 5 unit tests with links to requirements

4 unit tests missing linked requirements

8.2.2.1. Requirements per Test

Linked Requirements	0	1	2	3	>3
Test Cases	4	0	0	0	1

8.2.3. Tests Case Breakdown

Tests by Type

Type	Test Cases
Simulation	0
Equivalence	0
Baseline	5

Tests with Tag

Tag	Test Cases
-	5

8.3. Simulation Test Result Analysis

8.3.1. Model Test Status

2 out of 5 unit tests passed

0 unit tests failed

0 unit tests without pass/fail criteria

0 unit tests untested

3 unit tests disabled

8.3.2. Model Coverage

8.3.2.1. Aggregated Coverage

	Achieved	Justified	Missed
Execution	100%	0%	0%
Decision	100%	0%	0%
Condition	100%	0%	0%
MC/DC	100%	0%	0%

9. Unit: WDGTTCCalculation

9.1. Artifact Summary

Artifact Group	Artifact Type	Number of Artifacts
Requirements	Functional requirement	2
Design	Block diagram	1
	Model reference	0
	Subsystem	0
	Stateflow chart	0
	MATLAB function	0
	Data dictionary file	6
Tests	Test case	4
	Test case result	0

9.2. Test Case Analysis

9.2.1. Requirements Linked to Tests

0 out of 2 unit requirements with links to tests

2 unit requirements missing linked tests

9.2.1.1. Tests per Requirement

Linked Test Cases	0	1	2	3	>3
Requirements	2	0	0	0	0

9.2.2. Tests Linked to Requirements

0 out of 4 unit tests with links to requirements

4 unit tests missing linked requirements

9.2.2.1. Requirements per Test

Linked Requirements	0	1	2	3	>3
Test Cases	4	0	0	0	0

9.2.3. Tests Case Breakdown

Tests by Type

Type	Test Cases
Simulation	0
Equivalence	0
Baseline	4

Tests with Tag

Tag	Test Cases
-	4

9.3. Simulation Test Result Analysis

9.3.1. Model Test Status

0 out of 4 unit tests passed

0 unit tests failed

0 unit tests without pass/fail criteria

4 unit tests untested

0 unit tests disabled

9.3.2. Model Coverage

9.3.2.1. Aggregated Coverage

	Achieved	Justified	Missed
Execution	No data available	No data available	No data available
Decision	No data available	No data available	No data available
Condition	No data available	No data available	No data available
MC/DC	No data available	No data available	No data available