Team N / Year 2024-2025/ Project “Rock Your Baby”

# Complete Design Document

Version X.X

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**Purpose of this document**

The purpose of this document is to detail the steps of a V-Design process for the electronic product developed in the course “Rock Your Baby” in Q1 and Q2. It provides a comprehensive guide for the development and production of this product.

The V-Design process, also known as the V-model, is a systematic approach commonly used in the engineering and manufacturing industries to ensure efficient and high-quality product development.

This document serves several important purposes:

**Guidance:** It provides a structured outline of the entire product development lifecycle, starting from the initial concept and requirements gathering phase to the final testing, verification, and validation stages. It outlines the necessary steps and activities to be undertaken at each stage, helping to ensure that the project progresses smoothly and according to plan.

**Organization:** The document brings clarity and structure to the development process by breaking it down into distinct stages and activities. It provides a clear roadmap for the entire team, ensuring that everyone understands their roles, responsibilities, and the sequence of tasks to be completed.

**Communication:** The document acts as a communication tool, enabling effective collaboration between the team’s member involved in the project. By documenting the steps, it helps to align expectations and foster a common understanding of the product's design, functionality, and performance requirements.

**Risk Management:** It facilitates the identification and mitigation of potential risks and issues throughout the development process. By outlining the steps involved in each phase, the document helps in identifying potential bottlenecks, technical challenges, or gaps in requirements early on, allowing for timely mitigation strategies.

**Documentation:** The document serves as a record of the development process, providing a historical reference for future use. It helps in knowledge retention, allowing the organization to learn from past experiences and improve the efficiency and effectiveness of future projects.

Overall, a document detailing the steps of a V-Design process ensures a systematic and well-structured approach to product development, enabling the team to deliver a high-quality electronic product that meets the desired specifications, within the allocated time and resources.

**Text like this with a border and a grey background provides explanations, instructions, and examples. You can remove these from your group’s document if you wish.**

**Red texts are deadlines to keep in mind***.*

**Definitions of some of the key concepts used in this Document:**

**A requirement** is a specific statement or condition that defines what a product or system must be able to do, possess, or achieve. It outlines the necessary features, functions, performance criteria, and constraints that a product or system must meet to fulfill its intended purpose or satisfy user needs. Requirements provide the foundation for designing, developing, and evaluating the success of a product or system.

**A full signal specification** should contain: a clear unique name, description, a unit, a data type (if digital), a range and optionally (but recommended): a timing information/ sampling frequency/ refresh rate/ hands shaking/ protocol detail.

The goal of the **Design phase** is to create a detailed and comprehensive plan for the development of a product or system. It involves translating the requirements and specifications into a tangible design that outlines how the product will be structured, organized, and implemented. The goal is to create a design that optimally meets the requirements, considers technical feasibility, incorporates best practices, and aligns with the overall project objectives.

The goal of **testing** at the submodule/ product level is to verify the functionality, performance, and reliability of (an individual submodule within) your product. This level of testing focuses on isolating and evaluating the behavior of a submodule/ product to ensure they meet the specified requirements and operate correctly (within the overall product).

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## Requirements and Architecture

This section describes the requirements and overall architecture of the product. It is the first step of the V-design process.

***This Section 1 is due in Week 7, Q1, Thursday 17th October, 12h30.***

***This Design Document is a collaborative and living Document, we advise you to update your version number and date accordingly, in agreement with your team.***

*Instructions: Please keep in mind that this Section 1 can be updated at a later stage depending on eventual choices/ changes your team makes in the rest of the document.*

*If these changes are made after the submission deadline for Section 1: it will not modify your grade, BUT it is very important you have a coherent document where information given in Section 1 are aligned with the rest of the Document (also important for the final individual oral exam during which you are can take this document with you).*

### Architecture and Requirements at Product Level (System level)

#### Goal of the product

*Instructions: Briefly explain the goal of the product in simple words and in maximum 3 sentences (a friend with no advance knowledge in Electrical Engineering should be able to understand it).*

#### Set of Requirements at Product level (System Level)

A requirement is a specific statement or condition that defines what a product or system must be able to do, possess, or achieve. It outlines the necessary features, functions, performance criteria, and constraints that a product or system must meet to fulfill its intended purpose or satisfy user needs. Requirements provide the foundation for designing, developing, and evaluating the success of a product or system.

* Requirement *“1”*:
* Requirement *“As many as required”:*

#### Description of the Inputs and Outputs at Product level (System Level)

Instructions:

- Clearly describe the Input and Output signals of your product. These are listed in the document (provided by the Teachers) describing the Challenge (it is what your “Customer” needs in this case).

* Please do use Figures, Diagrams and Tables to illustrate your description.
* Input *“1”*:
* Input *“As many as required”:*
* Output *“1”*:
* Output *“As many as required”:*

#### Specification of the Inputs and Outputs at Product level (System Level)

A full signal specification should contain: a clear unique name, description, a unit, a data type (if digital), a range and optionally (but recommended): a timing information/ sampling frequency/ refresh rate/ hands shaking/ protocol detail. These should be extracted from the document describing the Challenge (your “Customer” in this case).

Instructions: Table 1 is a template used to specify your Signal “1”. Copy-past and fill-up this table as many times you have signals to specify.

|  |  |
| --- | --- |
| **Specifications of the Input signal “*1”* of the product** | |
| Unit |  |
| Data type |  |
| Range |  |
| Timing information  (optional) |  |

Table 1: *(Menu – Reference - Insert caption) - Example of title -* Specifications of the Input signal “*1”* of the product.

### Architecture and Requirements at Submodule Level (Sub-system level)

#### Overview of the product with a high-level context diagram

The product of Rock-Your-Baby will have 5 submodules:

**“Communication”** submodule: for this subsystem, you will need to go through an entire V-design process in Quartile 1 (Q1). The documentation of the complete V-Design process of the “Communication” subsystem is described in Section 2. This section 2 is due by the end of Q1 - while for the other subsystems, the V-Design process will be finalized during Q2. This is to ensure your team have a reliable communication between the other 4 sub-modules.

**“Heartbeat”** submodule.

**“Crying”** submodule.

**“Decision Making”** submodule.

**“Motor drivers”** submodule.

Instructions:

* Provide a brief overview of the product and its submodules.
* Include the high-level context diagram(s) for the system with all subsystems. A high-level diagram explains the relative position of each sub-module at system (or "product") level and the connections between them.

A diagram of a sub-module

Description automatically generated

Figure : *(Menu – Reference - Insert caption) - Example of title –* Random example of High-Level Diagram of….

#### Goal of each submodule

*Instructions: Provide the goal of each submodule in a few simple words (maximum 3 sentences).*

##### Goal of Submodule “Communication”

##### Goal of Submodule “Heartbeat”

##### Goal of Submodule “Crying”

##### Goal of Submodule “Decision Making”

##### Goal of Submodule “Motors drivers”

#### Set of requirements for each submodule

A requirement is a specific statement or condition that defines what a product or system must be able to do, possess, or achieve. It outlines the necessary features, functions, performance criteria, and constraints that a product or system must meet to fulfill its intended purpose or satisfy user needs. Requirements provide the foundation for designing, developing, and evaluating the success of a product or system.

*Instructions: Provide the set of requirements for each sub-module.*

##### Set of requirements for the Submodule “Communication”

* Requirement *“1”*:
* Requirement *“As many as required.”*
* Requirement for the Graphical User Interface/ Display

##### Set of requirements for the Submodule “Heartbeat”

* Requirement *“1”*:
* Requirement *“As many as required.”*
* Requirement for the Graphical User Interface/ Display

##### Set of requirements for the Submodule “Crying”

* Requirement *“1”*:
* Requirement *“As many as required.”*
* Requirement for the Graphical User Interface/ Display

##### Set of requirements for the Submodule “Decision Making”

* Requirement *“1”*:
* Requirement *“As many as required.”*
* Requirement for the Graphical User Interface/ Display

##### Set of requirements for the Submodule “Motors drivers”

* Requirement *“1”*:
* Requirement *“As many as required.”*
* Requirement for the Graphical User Interface/ Display

### Signals description at submodule level (Sub-system level)

#### Internal signals: description

Internal signals are the signals between your submodules.

Instructions:

* Clearly name, describe (specify the goal) of the internal signals (between sub-modules) of your product.
* Justify your choice (why you made this decision.
* In this section, you do not need yet to specify your internal signal as you did in the Section 1.1.4. You will specify these signals in Section 2 (for the communication sub-module) and Section 3 (for the other submodules).
* Please do use Figures, Diagrams and Tables to illustrate your description.

##### Internal Signal “X”

*Instruction: Please rename the “Signal X” with your chosen name*

###### Description of the signal “X” with justification

##### Internal Signal “Y”

*Instruction: Please rename the “Signal Y” with your chosen name*

###### Description of the signal “Y” with justification

##### Internal Signal “Add as many signals as required.”

###### Description of the signal “Add as many signals as required” with justification.

## Design description of the “Communication” submodule

***This Section 2 is due in Week 8, Q1, Thursday 24th October, 12h30.***

***This Design Document is a collaborative and living Document, we advise you to update your version number and date accordingly, in agreement with your team.***

*Instructions: Please keep in mind that this Section 2 can be updated at a later stage depending on eventual choices/ changes your team makes in the rest of the document.*

*If these changes are made after the submission deadline for Section 2: it will not modify your grade, BUT it is very important you have a coherent document where information given in Section 2 are aligned with the rest of the Document (also important for the final individual oral exam during which you are can take this document with you).*

***This submodule is addressed separately in this document because of its central role in the product. This submodule is also evaluated separately at the end of Quartile 1, Week 8.***

*If a team fails to demonstrate a working “Communication submodule” at the end of Quartile 1, Week 8: a working back-up/fail-safe solution will be worked out with the team.*

*This section should still reflect on the initial choice of the team and if the provided back-up/fail-safe solution is used/implemented after all, this choice must be duly justified in the appropriate Section 3.5.*

*Instructions: This section contain the design description and the test of the “Communication” sub-module.*

### Goal(s) of the “Communication” submodule

Instructions: Only refer here (with a hyperlink or a “copy-past”) to the goals and requirements you had written down in Section 1 for this sub-module.

### Two Design options for the “Communication” submodule

The goal of the **Design phase** is to create a detailed and comprehensive plan for the development of a product or system. It involves translating the requirements and specifications into a tangible design that outlines how the product will be structured, organized, and implemented.

The goal is to create a design that optimally meets the requirements, considers technical feasibility, incorporates best practices, and aligns with the overall project objectives.

Instructions:

* Describe two different design options. This means that both options should be a valid solution and should significantly differ from each other.
* Please do use Figures, Diagrams and Tables to illustrate your descriptions.

#### Design Option *1*

#### Design Option *2*

### Final Design choice for the “Communication” submodule

*Instructions:*

* *Compare the two design options with each other and explain which final design implementation you have made.*
* *Justify (why) you chose your final Design implementation vs the other (a table with a comparison of key elements relevant to achieve the goal of the Design).*
* *Please do use Figures, Diagrams and Tables to illustrate your descriptions.*

### Signals description and specifications for the Communication Sub-system

Instructions: Please add as many tables as you have input and output signals for this sub-module.

|  |  |
| --- | --- |
| **Specifications of the *Input or Output* signal “*1”* of the Communication Sub-system** | |
| Unit |  |
| Data type |  |
| Range |  |
| Timing information  (optional) |  |

Table 2: *(Menu – Reference - Insert caption) - Example of title -* Specifications of the Input signal “*1”* of the sub-system.

### Tests of the “Communication” submodule

*Instructions:*

* *Describe how you have tested your design choices vs. the specifications you have given in Sections 2.1 and 2.2.*
* *Each element of the specifications must be verified with your test(s) for each signal!*
* Please do use Figures, Diagrams and Tables to illustrate your description.

#### Tests “Signal 1” with respect to its specifications.

*Instruction: Please rename the “Signal 1” with your chosen name.*

#### Tests “Signal 2” with respect to its specifications.

*Instruction: Please rename the “Signal 2” with your chosen name.*

#### Tests “Add as many Signals as required” with respect to its specifications.

### Justification to fall back to the back-up solution provided (optional)

*Instruction: This Section is to be completed only if the Team decides to use the back-up/fail-safe solution provided (for the “Communication” submodule). Otherwise: please delete this section.*

* *Please explain why your team has decided to use this solution.*
* *If your initial V-design process (for the “Communication” submodule) did not result in a “working” submodule, please reflect on what happened and why it was not working.*
* *What would you do differently if you could do the V-Design process (of the “Communication” submodule) all over again?*

## Design Descriptions

***The first complete version of Section 3 is due in Week 3, Q2, Thursday 28th November, 12h30.***

***The Final version of this Section 3 (in case if eventual changes in the Design choices) will be re-evaluated together with Section 4 and 5 in Week 5 of Q2.***

***This Design Document is a collaborative and living Document, we advise you to update your version number and date accordingly, in agreement with your team.***

*Instructions: Please keep in mind that this Section 3 can be updated at a later stage depending on eventual choices/ changes your team makes in the rest of the document.*

*It is very important you have a coherent document where information given in Section 3 are aligned with the rest of the Document (also important for the final individual oral exam during which you are can take this document with you).*

The goal of the **Design phase** is to create a detailed and comprehensive plan for the development of a product or system. It involves translating the requirements and specifications into a tangible design that outlines how the product will be structured, organized, and implemented.

The goal is to create a design that optimally meets the requirements, considers technical feasibility, incorporates best practices, and aligns with the overall project objectives.

### Submodule “Heartbeat”

#### Goals and Guidelines for the Submodule “Heartbeat”

Instructions: Only refer here (with a hyperlink or a “copy-past”) to the goals and requirements you had written down in Section 1 for this sub-module.

#### Three Design options for the Submodule “Heartbeat”

Instructions:

* Describe three different design options. This means that both options should be a valid solution and should significantly differ from each other.
* Please do use Figures, Diagrams and Tables to illustrate your descriptions.

#### Final Design choice for the Submodule “Heartbeat”

*Instructions:*

* *Compare the three design options with each other and explain which final design implementation you have made.*
* *Justify (why) you chose your final Design implementation vs the other (a table with a comparison of key elements relevant to achieve the goal of the Design).*
* *Please do use Figures, Diagrams and Tables to illustrate your descriptions.*

#### Signal specifications of for the Submodule “Heartbeat”

Instructions: Please add as many tables as you have input and output signals for this sub-module.

|  |  |
| --- | --- |
| **Specifications of the *Input or Output* signal “*1”* of the Heartbeat Sub-system** | |
| Unit |  |
| Data type |  |
| Range |  |
| Timing information  (optional) |  |

Table 3: *(Menu – Reference - Insert caption) - Example of title -* Specifications of the Input signal “*1”* of the sub-system.

### Submodule “Crying”

#### Goals and Guidelines for the Submodule “Crying”

Instructions: Only refer here (with a hyperlink or a “copy-past”) to the goals and requirements you had written down in Section 1 for this sub-module.

#### Three Design options for the Submodule “Crying”

Instructions:

* Describe three different design options. This means that both options should be a valid solution and should significantly differ from each other.
* Please do use Figures, Diagrams and Tables to illustrate your descriptions.

#### Final Design choice for the Submodule “Crying”

*Instructions:*

* *Compare the three design options with each other and explain which final design implementation you have made.*
* *Justify (why) you chose your final Design implementation vs the other (a table with a comparison of key elements relevant to achieve the goal of the Design).*
* *Please do use Figures, Diagrams and Tables to illustrate your descriptions.*

#### Signal specifications of for the Submodule “Crying”

Instructions: Please add as many tables as you have input and output signals for this sub-module.

|  |  |
| --- | --- |
| **Specifications of the *Input or Output* signal “*1”* of the Crying Sub-system** | |
| Unit |  |
| Data type |  |
| Range |  |
| Timing information  (optional) |  |

Table 4: *(Menu – Reference - Insert caption) - Example of title -* Specifications of the Input signal “*1”* of the sub-system.

### Submodule “Decision Making”

#### Goals and Guidelines for the Submodule “Decision Making”

Instructions: Only refer here (with a hyperlink or a “copy-past”) to the goals and requirements you had written down in Section 1 for this sub-module.

#### Three Design options for the Submodule “Decision Making”

Instructions:

* Describe three different design options. This means that both options should be a valid solution and should significantly differ from each other.
* Please do use Figures, Diagrams and Tables to illustrate your descriptions.

#### 

#### Final Design Choice for the Submodule “Decision Making”

*Instructions:*

* *Compare the three design options with each other and explain which final design implementation you have made.*
* *Justify (why) you chose your final Design implementation vs the other (a table with a comparison of key elements relevant to achieve the goal of the Design).*
* *Please do use Figures, Diagrams and Tables to illustrate your descriptions.*

#### Signal specifications of for the Submodule “Decision Making”

Instructions: Please add as many tables as you have input and output signals for this sub-module.

|  |  |
| --- | --- |
| **Specifications of the *Input or Output* signal “*1”* of the Decision-Making Sub-system** | |
| Unit |  |
| Data type |  |
| Range |  |
| Timing information  (optional) |  |

Table 5: *(Menu – Reference - Insert caption) - Example of title -* Specifications of the Input signal “*1”* of the sub-system.

### Submodule “Motors drivers”

#### Goals and Guidelines for the Submodule “Motor drivers”

Instructions: Only refer here (with a hyperlink or a “copy-past”) to the goals and requirements you had written down in Section 1 for this sub-module.

#### Three Design options for the Submodule “Motor drivers”

Instructions:

* Describe three different design options. This means that both options should be a valid solution and should significantly differ from each other.
* Please do use Figures, Diagrams and Tables to illustrate your descriptions.

#### Final Design Choice for the Submodule “Motor drivers”

*Instructions:*

* *Compare the three design options with each other and explain which final design implementation you have made.*
* *Justify (why) you chose your final Design implementation vs the other (a table with a comparison of key elements relevant to achieve the goal of the Design).*
* *Please do use Figures, Diagrams and Tables to illustrate your descriptions.*

#### Signal specifications of for the Submodule “Motor drivers.”

Instructions: Please add as many tables as you have input and output signals for this sub-module.

|  |  |
| --- | --- |
| **Specifications of the *Input or Output* signal “*1”* of the Motor drivers’ Sub-system** | |
| Unit |  |
| Data type |  |
| Range |  |
| Timing information  (optional) |  |

Table 6: *(Menu – Reference - Insert caption) - Example of title -* Specifications of the Input signal “*1”* of the sub-system.

### Final check of your signal’s specifications

Instructions: In the Section 1.2 of this document, you have set an architecture with internal signals. Each internal signal could be at the same time an input and an output for different sub-modules or for your final product.

Copy-past here the drawing of your architecture from the Section 1.2 and summarize all your internal signal’s specifications in an overview table. For this you need to reuse your signal’s specifications of the sub-modules you have given in this Section 3.

## Tests at Submodule level (Sub-system level)

***This Section 4 is due in Week 5, Q2, Thursday 12th December, 12h30.***

***This Design Document is a collaborative and living Document, we advise you to update your version number and date accordingly, in agreement with your team.***

*Instructions: Please keep in mind that this Section 4 can be updated at a later stage depending on eventual choices/ changes your team makes in the rest of the document.*

*It is very important you have a coherent document where information given in Section 4 are aligned with the rest of the Document (also important for the final individual oral exam during which you are can take this document with you).*

**The goal of testing** at the submodule level is to verify the functionality, performance, and reliability of individual submodule within your product. This level of testing focuses on isolating and evaluating the behavior of a specific submodule to ensure they meet the specified requirements and operate correctly within the overall product.

The objectives of sub-module testing are:

- **Functionality Validation**: Verify that each submodule performs its intended functions accurately and reliably, according to the defined requirements (*as defined in Section 3*).

- **Interface Compatibility**: Ensure that the interfaces between sub-modules are correctly implemented and that data exchange and communication between them function as expected (as defined in *Sections 1 and 3*).

By conducting thorough testing at the submodule level, potential issues and risks can be identified and resolved early in the development process, reducing the likelihood of more significant problems arising at higher levels of integration. This level of testing helps to ensure the overall quality and reliability of the product.

### Test of Submodule “Heartbeat.”

#### Functional Validation of the Submodule “Heartbeat.”

*Instructions:*

* *Describe how you have tested your design implementation vs. the requirements and specifications you have defined in Sections 1and 3. Each requirement must be validated (this means your test is successful: it verifies the sub-module requirement).*
* *Each verification of a requirement requires a subheading.*
* *Please do use Figures, Diagrams and Tables to illustrate your description.*

##### Functional Validation of *“Requirement 1”* the Submodule “Heartbeat.”

1. Describe the test set-up.
2. Describe the test results.
3. Explain why the result validates the requirement

##### Functional Validation of *“As many requirements as needed”* the Submodule “Heartbeat.”

1. Describe the test set-up.
2. Describe the test results.
3. Explain why the result validates the requirement.

### Test of Submodule “Crying.”

#### Functional Validation of the Submodule “Crying.”

*Instructions:*

* *Describe how you have tested your design implementation vs. the requirements and specifications you have defined in Sections 1and 3. Each requirement must be validated (this means your test is successful: it verifies the sub-module requirement).*
* *Each verification of a requirement requires a subheading.*
* *Please do use Figures, Diagrams and Tables to illustrate your description.*

##### Functional Validation of *“Requirement 1”* the Submodule “Crying.”

1. Describe the test set-up.
2. Describe the test results.
3. Explain why the result validates the requirement.

##### Functional Validation of *“As many requirements as needed”* the Submodule “Crying.”

1. Describe the test set-up.
2. Describe the test results.
3. Explain why the result validates the requirement.

### Test of Submodule “Decision Making.”

#### Functional Validation of the Submodule “Decision Making.”

*Instructions:*

* *Describe how you have tested your design implementation vs. the requirements and specifications you have defined in Sections 1 and 3. Each requirement must be validated (this means your test is successful: it verifies the sub-module requirement).*
* *Each verification of a requirement requires a subheading.*
* *Please do use Figures, Diagrams and Tables to illustrate your description.*

##### Functional Validation of *“Requirement 1”* the Submodule “Decision Making.”

1. Describe the test set-up.
2. Describe the test results.
3. Explain why the result validates the requirement.

##### Functional Validation of *“As many requirements as needed”* the Submodule “Decision Making.”

1. Describe the test set-up.
2. Describe the test results.
3. Explain why the result validates the requirement.

### Test of Submodule “Motors drivers”

#### Functional Validation of the Submodule “Motors drivers.”

*Instructions:*

* *Describe how you have tested your design implementation vs. the requirements and specifications you have defined in Sections 1 and 3. Each requirement must be validated (this means your test is successful: it verifies the sub-module requirement).*
* *Each verification of a requirement requires a subheading.*
* *Please do use Figures, Diagrams and Tables to illustrate your description.*

##### Functional Validation of *“Requirement 1”* the Submodule “Motors drivers.”

1. Describe the test set-up.
2. Describe the test results.
3. Explain why the result validates the requirement.

##### Functional Validation of *“As many requirements as needed”* the Submodule “Motor drivers.”

1. Describe the test set-up.
2. Describe the test results.
3. Explain why the result validates the requirement.

## Tests at Product level (System level)

***This Section 5 is due in Week 5, Q2, Thursday 12th December, 12h30.***

The goal of testing at product level is to assess the overall functionality, performance, and quality of the complete and integrated product you have designed. This level of testing focuses on evaluating how all the individual sub-systems work together as a cohesive unit to meet the specified requirements and deliver the desired features and capabilities.

The objectives of product-level testing include:

**Integration Validation:** Ensure that the integration of different subsystems is seamless and that they interact correctly with each other, exchanging data and communicating as expected.

**Functional Verification:** Verify that the product functions as intended, performs all the required tasks, and meets the specified functional requirements.

By conducting comprehensive testing at the product level, your team can identify and resolve any issues, weaknesses, or gaps in the product's functionality, performance, or usability before it is released to “customers or deployed in the intended environment”.

*Instructions:*

* *Describe a test plan that lists how you have tested your design implementation vs. the requirements and specifications you have defined in Sections 1 and 3.*
* *Please do use Figures, Diagrams and Tables to illustrate your description.*

*If the system has been functioning as intended, we would suggest celebrating.*

Appendix A: Acronyms

Instructions: Provide a list of acronyms and associated literal translations used within the document. List the acronyms in alphabetical order using a tabular format as depicted below.

Table - Acronyms

| Acronym | Literal Translation |
| --- | --- |
| *<Acronym>* | *<Literal Translation>* |
| RYB | Rock Your Baby |
| Q1 | Quartile 1 |
| <Acronym> | <Literal Translation> |

Appendix B: Referenced Documents

Instructions: Summarize the relationship of this document to other relevant documents. Provide identifying information for all documents used to arrive at and/or referenced within this document (e.g., related and/or companion documents, prerequisite documents, relevant technical documentation, etc.).

Table 8 - Referenced Documents

| Document Name | Document Location and/or URL | Issue Date |
| --- | --- | --- |
| <Document Name> | <Document Location and/or URL> | <MM/DD/YYYY> |
| <Document Name> | <Document Location and/or URL> | <MM/DD/YYYY> |
| <Document Name> | <Document Location and/or URL> | <MM/DD/YYYY> |