



भारतीय प्रौद्योगिकी संस्थान भिलाई

जिला-दुर्ग, छत्तीसगढ़-491001

Indian Institute of Technology, Bhilai

Dist.- Durg, Chhattisgarh - 491001

Website: [www.iitbhilai.ac.in](http://www.iitbhilai.ac.in)

Annexure - II

**Advertisement for the post of Project Engineer in MeitY funded Research Project**

Applications are invited from Indian nationals for the position of Project Engineer in a MeitY funded research project with the following details:

**Title of the Project:** High Precision Interfacing Circuits for Capacitive based Sensors (HPICCS) for Defense Applications

Principal Investigator	Co- Principal Investigator:
<u>Dr. Vivek Raghuwanshi</u> Assistant Professor Electronics and Communication Engineering (ECE), IIT Bhilai, Kutelabhata, Khapri, Durg Chhattisgarh- 491001 vivekr@iitbhilai.ac.in	<u>Dr. Santosh Biswas</u> Professor Department of Computer Science and Engineering IIT Bhilai, Kutelabhata, Khapri, Durg Chhattisgarh- 491001 santosh@iitbhilai.ac.in

For other details and the project team, please visit the homepage

[https://www.iitbhilai.ac.in/index.php?pid=rec\\_staff](https://www.iitbhilai.ac.in/index.php?pid=rec_staff)

<https://www.iitbhilai.ac.in/index.php?pid=vivekr>

**Name of the position:** Project Engineer

**Number of Positions:** 1

**Essential qualifications:** M.Tech/ME in VLSI /Microelectronics (or) allied areas with experience in semiconductor devices and circuits.

**Age limit:** 45

**Salary:** 40000-70000

**Desirable:** Preference will be given to candidates who have experience in VLSI design tools. A strong orientation towards the design and development of semiconductor devices is desired.

**Other benefits:** The selected candidates are eligible to register for PhD degree at IIT Bhilai as per institute rules. However, s(he) needs to apply for PhD position when the advertisement is open.

**Duration:** The initial appointment will be given for 12 months and is extendable based on performance till the project completion (Total project duration 4 years).



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**How to apply:** Interested candidates must apply on the **application form attached** with this advertisement. Duly completed application form along with candidate's resume must be mailed or emailed to the PI so as to reach him by **15/09/2024**. **Sending cv only will not be considered as valid Application.** Only shortlisted candidates will be called for interview. The positions are available immediately.

**Due date:** The application must reach the PI, **Dr. Vivek Raghuwanshi**, ([vivekr@iitbhilai.ac.in](mailto:vivekr@iitbhilai.ac.in)), by 15/09/2024 (Sunday). Clearly mention in the email the title of the position (Project Engineer) being applied for.

**Terms & Conditions:**

- i. No TA/DA will be provided to the candidate for the interview
- ii. Candidates applying for more than one post should send separate application for each post.
- iii. The PI shall not be responsible for postal/courier delay if any, or any other reason for non- receipt of the document at the specified time and will result in disqualification/rejection of the application.
- iv. The decision of the selection committee will be final.
- v. If the number of shortlisted candidates called for the interview is large, the selection committee may decide to restrict the number of candidates for the interview to a reasonable limit after considering qualifications and experience over and above the minimum prescribed in the advertisement.
- vi. The appointment of the candidate will be governed by the terms and conditions of the Institute/ funding agency particularly applicable to the said project as and when required.
- vii. The selected candidate will have to join duty immediately on receipt of the offer.
- viii. The fellowship may be terminated with a 30-day notice before completion of the tenure if performance till date is not deemed satisfactory.



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