#### ShanghaiTech University

**EE 115B: Digital Circuits** 

Fall 2022

Lecture 5

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# EE115 Analog and *Digital Circuits*

# Topic 5: Boolean Algebra and Logic Simplification

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#### **Boolean Algebra and Logic Simplification**

- Boolean Algebra
- Logic Simplification
  - Karnaugh Map
  - Quine-McCluskey Method



#### **Boolean Addition**

In Boolean algebra, a **variable** is a symbol used to represent an action, a condition, or data. A single variable can only have a value of 1 or 0.

The **complement** represents the inverse of a variable and is indicated with an overbar. Thus, the complement of A is  $\overline{A}$ .

A literal is a variable or its complement.

Addition is equivalent to the OR operation. The sum term is 1 if one or more if the literals are 1. The sum term is zero only if each literal is 0.



Determine the values of A, B, and C that make the sum term of the expression  $\overline{A} + B + \overline{C} = 0$ ?



Each literal must = 0; therefore A = 1, B = 0 and C = 1.

#### **Boolean Multiplication**

In Boolean algebra, multiplication is equivalent to the AND operation. The product of literals forms a product term. The product term will be 1 only if all of the literals are 1.



What are the values of the A, B and C if the product term of  $A \cdot \overline{B} \cdot \overline{C} = 1$ ?



Each literal must = 1; therefore A = 1, B = 0 and C = 0.

#### **Commutative Laws**

The **commutative laws** are applied to addition and multiplication. For addition, the commutative law states

In terms of the result, the order in which variables are ORed makes no difference.

$$A + B = B + A$$

For multiplication, the commutative law states

In terms of the result, the order in which variables are ANDed makes no difference.

$$AB = BA$$

#### **Associative Laws**

The **associative laws** are also applied to addition and multiplication. For addition, the associative law states

When ORing more than two variables, the result is the same regardless of the grouping of the variables.

$$A + (B + C) = (A + B) + C$$

For multiplication, the associative law states

When ANDing more than two variables, the result is the same regardless of the grouping of the variables.

$$A(BC) = (AB)C$$

#### **Distributive Law**

The **distributive law** is the factoring law. A common variable can be factored from an expression just as in ordinary algebra. That is

$$AB + AC = A(B+C)$$

The distributive law can be illustrated with equivalent circuits:

$$\begin{array}{c}
B \\
C
\end{array}$$

$$A \\
A$$

$$A \\
B$$

$$A \\
C$$

$$AC$$

$$AB + AC$$

#### Rules of Boolean Algebra (1)

1. 
$$A + 0 = A$$

2. 
$$A + 1 = 1$$

3. 
$$A \cdot 0 = 0$$

4. 
$$A \cdot 1 = 1$$
 A&1=A

5. 
$$A + A = A$$

6. 
$$A + \overline{A} = 1$$

7. 
$$A \cdot A = A$$

8. 
$$A \cdot \overline{A} = 0$$

9. 
$$\overline{\overline{A}} = A$$

10. 
$$A + AB = A$$

11. 
$$A + \overline{AB} = A + B$$

12. 
$$(A + B)(A + C) = A + BC$$

#### FIGURE 4-8

**Rule 1:** A + O = A A variable ORed with 0 is always equal to the variable. If the input variable A is 1, the output variable X is 1, which is equal to A. If A is 0, the output is 0, which is also equal to A. This rule is illustrated in Figure 4–8, where the lower input is fixed at 0.

$$A = 1$$

$$0$$

$$X = 1$$

$$0$$

$$X = 0$$

$$0$$

$$X = A + 0 = A$$

**Rule 10:** A + AB = A This rule can be proved by applying the distributive law, rule 2, and rule 4 as follows:

$$A + AB = A \cdot 1 + AB = A(1 + B)$$
 Factoring (distributive law)  
=  $A \cdot 1$  Rule 2:  $(1 + B) = 1$   
=  $A$  Rule 4:  $A \cdot 1 = A$ 

#### TABLE 4-2

Rule 10: A + AB = A. Open file T04-02 to verify.

$\boldsymbol{A}$	В	AB	A + AB	_
0	0	0	0	$A \rightarrow \bigcirc$
0	1	0	0	
1	0	0	1	$B \longrightarrow$
1	1	1	1	<b>↓</b>
<b>†</b>	eq	ual ———		A straight connection

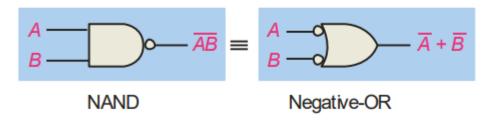
#### **DeMorgan's Theorem (1)**

#### DeMorgan's 1st Theorem

The complement of a product of variables is equal to the sum of the complemented variables.

$$\overline{AB} = \overline{A} + \overline{B}$$

Applying DeMorgan's first theorem to gates:



Inputs		Output	
Α	В	ĀB	$\overline{A} + \overline{B}$
0	0	1	1
0	1	1	1
1	0	1	1
1	1	0	0

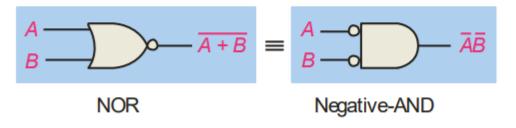
#### DeMorgan's Theorem (2)

#### DeMorgan's 2<sup>nd</sup> Theorem

The complement of a sum of variables is equal to the product of the complemented variables.

$$\overline{A+B} = \overline{A} \cdot \overline{B}$$

Applying DeMorgan's second theorem to gates:



Inputs		Output		
Α	В	$\overline{A+B}$	ĀĒ	
0	0	1	1	
0	1	0	0	
1	0	0	0	
1	1	0	0	

#### DeMorgan's Theorem (3)



Apply DeMorgan's theorem to remove the overbar covering both terms from the expression  $X = \overline{\overline{C} + D}$ .



To apply DeMorgan's theorem to the expression, you can break the overbar covering both terms and change the sign between the terms. This results in  $X = \overline{C} \cdot \overline{D}$ . Deleting the double bar gives  $X = C \cdot \overline{D}$ .

#### **Example**

Apply DeMorgan's theorems.

$$\overline{A\overline{B} + \overline{C}D + EF}$$

#### Solution:

Let  $A\overline{B} = X$ ,  $\overline{CD} = Y$ , and EF = Z. The expression  $\overline{AB} + \overline{CD} + EF$  is of the form  $\overline{X} + \overline{Y} + \overline{Z} = \overline{X}\overline{Y}\overline{Z}$  and can be rewritten as

$$\overline{A\overline{B} + \overline{C}D + EF} = (\overline{A\overline{B}})(\overline{\overline{C}D})(\overline{EF})$$

Next, apply DeMorgan's theorem to each of the terms  $\overline{AB}$ ,  $\overline{CD}$ , and  $\overline{EF}$ .

$$(\overline{A}\overline{B})(\overline{\overline{C}D})(\overline{EF}) = (\overline{A} + B)(C + \overline{D})(\overline{E} + \overline{F})$$



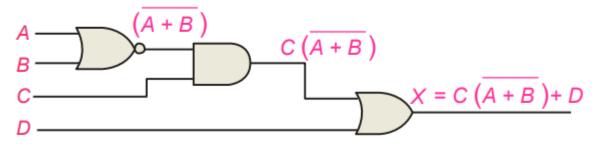
#### **Boolean Analysis of Logic Circuits**

Combinational logic circuits can be analyzed by writing the expression for each gate and combining the expressions according to the rules for Boolean algebra.



Apply Boolean algebra to derive the expression for *X*.

Write the expression for each gate:



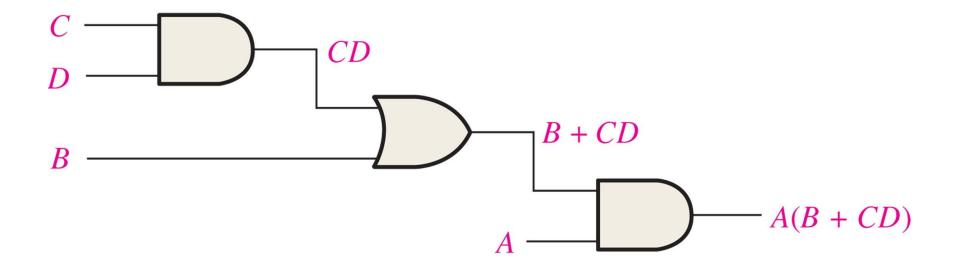
Applying DeMorgan's theorem and the distribution law:

$$X = C (\overline{A} \overline{B}) + D = \overline{A} \overline{B} C + D$$



#### **Example: Boolean expression for a logic circuit**

**FIGURE 4-18** A combinational logic circuit showing the development of the Boolean expression for the output.



# Example: Constructing a truth table for a logic circuit

#### TABLE 4-5

Truth table for the logic circuit in Figure 4–18.

Inputs				Output
$\boldsymbol{A}$	В	$\boldsymbol{C}$	D	A(B + CD)
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	0
0	1	0	1	0
0	1	1	0	0
0	1	1	1	0
1	0	0	0	0
1	0	0	1	0
1	0	1	0	0
1	0	1	1	1
1	1	0	0	1
1	1	0	1	1
1	1	1	0	1
1	1	1	1	1

#### **SOP and POS Forms**

Boolean expressions can be written in the **sum-of-products** form (SOP) or in the product-of-sums form (POS). These forms can simplify the implementation of combinational logic, particularly with PLDs. In both forms, an overbar cannot extend over more than one variable.

An expression is in SOP form when two or more product terms are summed as in the following examples:

$$\overline{A} \overline{B} \overline{C} + AB$$

$$\overline{A} \, \overline{B} \, \overline{C} + A \, B$$
 
$$A \, B \, \overline{C} + \overline{C} \, \overline{D}$$

$$CD + \overline{E}$$

An expression is in POS form when two or more sum terms are multiplied as in the following examples:

$$(A+B)(\overline{A}+C)$$

$$(A+B)(\overline{A}+C)$$
  $(A+B+\overline{C})(B+D)$   $(\overline{A}+B)C$ 

$$(\overline{A} + B)C$$

#### **SOP Standard Form**

In **SOP standard form**, every variable in the domain must appear in each term. This form is useful for constructing truth tables or for implementing logic in PLDs.

You can expand a nonstandard term to standard form by multiplying the term by a term consisting of the sum of the missing variable and its complement.



Convert 
$$X = \overline{A} \overline{B} + A B C$$
 to standard form.

The first term does not include the variable C. Therefore, multiply it by the  $(C + \overline{C})$ , which = 1:

$$X = \overline{A} \overline{B} (C + \overline{C}) + A B C$$
$$= \overline{A} \overline{B} C + \overline{A} \overline{B} \overline{C} + A B C$$

## 2.5.3 逻辑函数的两种标准形式

最小项之和

最大项之积

#### 最小项 m:

- · m是乘积项
- · 包含n个因子
- · n个变量均以原变量和反变量的形式在m中出现一次

对于n变量函数 有2<sup>n</sup>个最小项

### 最小项举例:

·两变量A,B的最小项

A'B', A'B, AB', AB (2<sup>2</sup> = 4 $\uparrow$ )

· 三变量A,B,C的最小项

A'B'C', A'B'C, A'BC', A'BCAB'C', AB'C, ABC', ABC  $(2^3 = 8 \uparrow)$ 

# 最小项的编号:

最小项	取值	对应	编号
	ABC	十进制数	
A'B'C'	0 0 0	0	$m_0$
A'B'C	0 0 1	1	$m_1$
A'BC'	0 1 0	2	$m_2$
A'BC	0 1 1	3	$m_3$
AB'C'	1 0 0	4	$m_4$
AB'C	1 0 1	5	$m_5$
ABC'	1 1 0	6	$m_6$
ABC	1 1 1	7	$ m_7 $