### Differential Amplifiers

-VSS

WEE -VCC

#### Objective

- to only amplify
the difference
between the
two inputs

### assumptions

- transistors identical

- Current source ideal

- transistors are in outive mode (Saturation for MOS)

4

+ VCC Voi=Vai= Vac- Ra 2 VOZ=VCZ=VCC-RCZI

N - NOI-NOS = 0

gm=gm=gm2=40 = 20 I

### DC Analysis (cont'd)

$$T_{b} = \frac{1}{2} k V_{oV} \Rightarrow \frac{1}{2} = \frac{1}{2} k V_{oV}$$

$$\Rightarrow V_{oV} = \sqrt{T_{k}}$$

$$\Rightarrow V_{GS} = V_{oV} + V_{t} = \sqrt{T_{k}} + V_{t}$$

$$q_{m} = q_{m} = q_{m} = \sqrt{2kT_{D}} = \sqrt{2kT_{D}}$$

Big Pict 20, 20, 1.8, 1.2, 1 for 8.0°, 8.8°, 8.8°, 60-+ 900 1.30

4

u 4

2

4 Z

# AC Analysis

How to do that?

- Superposition -Sure, But tedious!

Superposition Overall 1001 | and 701 = 701 × 20 = # gmR L x =

#2' V,: off, V2: ON



$$\frac{701}{702} = \frac{-9mRc}{1+9mRc} = -\frac{1}{2}9mRc$$

$$RE$$

Therefore,

#1 201 = 29mRc > 201=(29mRc),

=> ~01=(=29mRc)2+(-=29mRc)~2 => ~00=(=29mRc)(~1,-~2) => [Ad==29mRc]

Calculated the differential gain (last lecture) using superposition:

 $A_d = \frac{v_{01}}{v_{1} - v_{5}} = \frac{1}{2}g_{mRc}$  (if  $v_{01}$  is the output)

Ad = TOOZ -1 game (if voz is the output)

Ad = 10-101-102 = gmRc (if y=v-voz is the

Let us assume that voi is the output:

=> 
$$v_{01} = (\frac{1}{2}g_{m}R_{c})(v_{1}-v_{2})$$
  
=>  $v_{01} = (\frac{1}{2}g_{m}R_{c})(v_{1}-v_{2})$   
=  $A_{2}v_{0} + (-A_{1})v_{0}$ 

= A2U, + (-Ad)UZ

=> voitif vit => vinon-inverting input Voi It if 721 > V2: inverting in put

## A Couple of Definitions

U\_d = U, - Uz : differential input

UCM = U1+U2 : Common-mode input

To = Ad Vd + Acm Vcm superposition

Ad: differential gain = To |

Vcm = 0

Acm: common-mode sain = To |

Acm: common-mode sain = Tom Vd=0

An ideal differential amplifier features

Acm=0 > the signal component common to both v, and vz, e.g., the exogenous the output. Will not appear in

In practice, Acmto > vo will also be a function of <u>Vituz</u>. However, Acm is very small (by proper design), in a commercial op-amp.

CMRR: Common-Mode Resection Ratio = | Ad | Acm | CMRR & is very large in a commercial amp.

CMRR(dB)=20 log | Ad | Acm |

Us = AU+ AUCM

Ucmo Her beit of wem

Te,= Tez (because the circuit is)

Symmetrical)

Te,+ Tez = 0 (by KCL)

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> The link between E1 & E2

Static (Mechanical Analogy: The Seesaw Half-Circuits 3mRc => \ \ \frac{7001}{7d} = \frac{1}{2}gmRc 1002 -1 28 -9mRc =>

Non a feet to a

Ti=Tz (Symmetry) 11+12=0 (by KCL)

If  $v_0 = v_0, -v_{02}$  was taken the input, then Acm=0

Example

$$T = 2mA \rightarrow 9m = 40mS$$

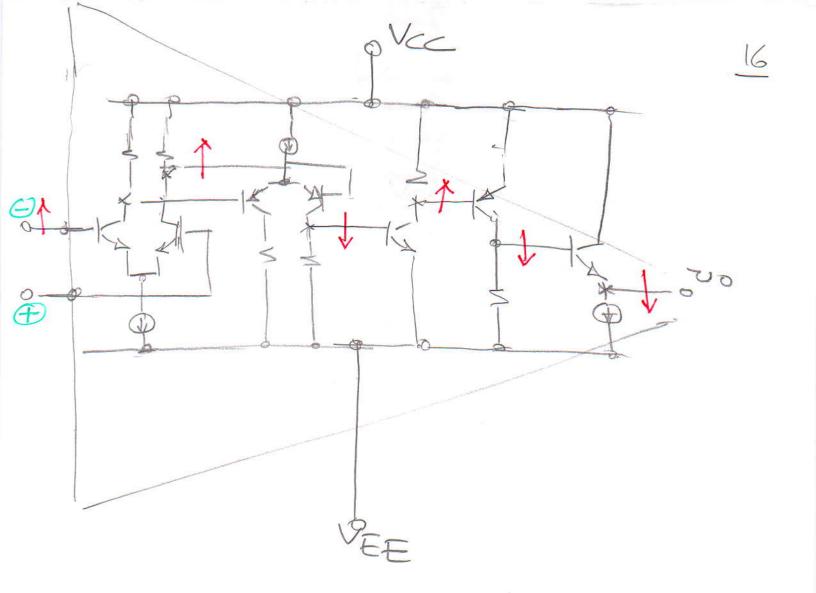
$$RC = 1 KD$$

$$REE = 20 kD$$

$$Ad = \frac{1}{2} \times 40 \times 1 lD = 20 V$$

$$Am = -40 \times 1$$

Acm = -40x1 1+40x46 = -1 +0=40=-25x10 V  $CMRR = \frac{20}{-2.5 \times 10^{2}} = \frac{2000}{2.5} = \frac{8000}{10} = \frac{800}{10}$ CMRR(dR)=58dB



An increase in the top input has resulted in a decrease in the output. Therefore, the top input is the inverting input (-) whereas the bottom input is the non-inverting input (+).