Ryerson University
Department of Electrical and Computer Engineering
ELE202-Electric Circuits Analysis
Mid-term Examination
March 4, 2022 - Duration: 120 Minutes
Examiners: Dr. Mohammadi and Dr. Jassar

I solemnly declare that I will complete this examination independently, with full compliance to the Senate Policy 60 Academic Integrity, and meeting the requirements stated in the examination.

Student Last Name: ..... **Student First Name: .....**

Student Number: ..... **Signature: ..... (Required)**

NOTES:

The test is Open Book / Open Notes.

There are 5 questions, each with subsections. Answer all questions.

NO QUESTIONS to be asked during the test. If doubt exists as to the interpretation of any question, you are urged to submit with the answer, a clear statement of any logical assumptions made.

Question No.	***Mark of each question***	***Mark obtained***
Q 1	20	
Q 2	20	
Q 3	20	
Q 4	20	
Q 5	20	
Total (100)		

Q 1 (a): [10 marks] The Current versus time graph is given in Figure 1.a. Sketch the charge versus time graph on the current versus time graph. Show all the working steps.

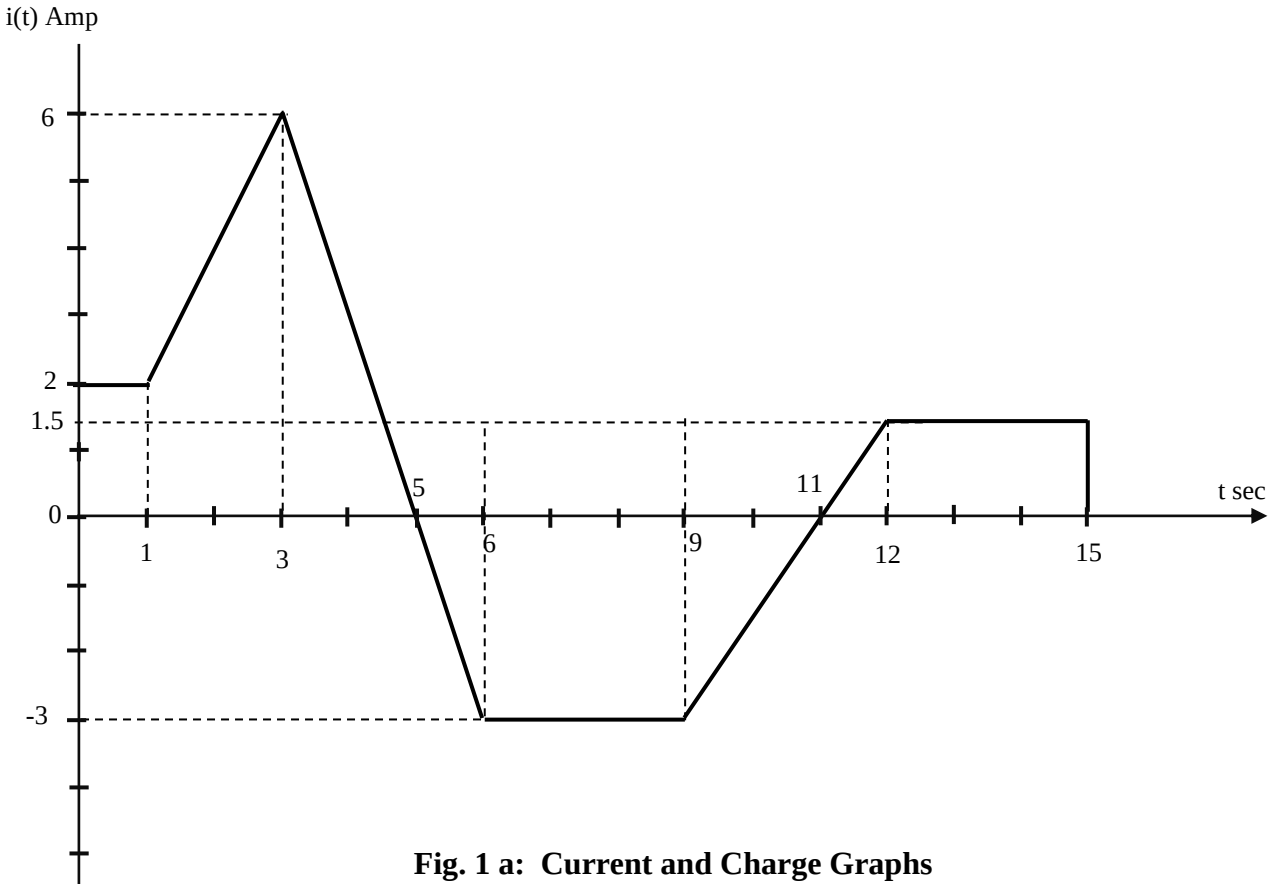

Fig. 1 a: Current and Charge Graphs

Q 1 (b): [10 marks] For the circuit shown in the Fig.1 b below. Fill the table below:

Note: The # inside the red circle refers to the table's component numbers. Assume the polarity of the voltage and direction of current as needed (some of the components are not given with the polarity of the voltage like 2, 3, 5, 8 and the direction of current like component 1).

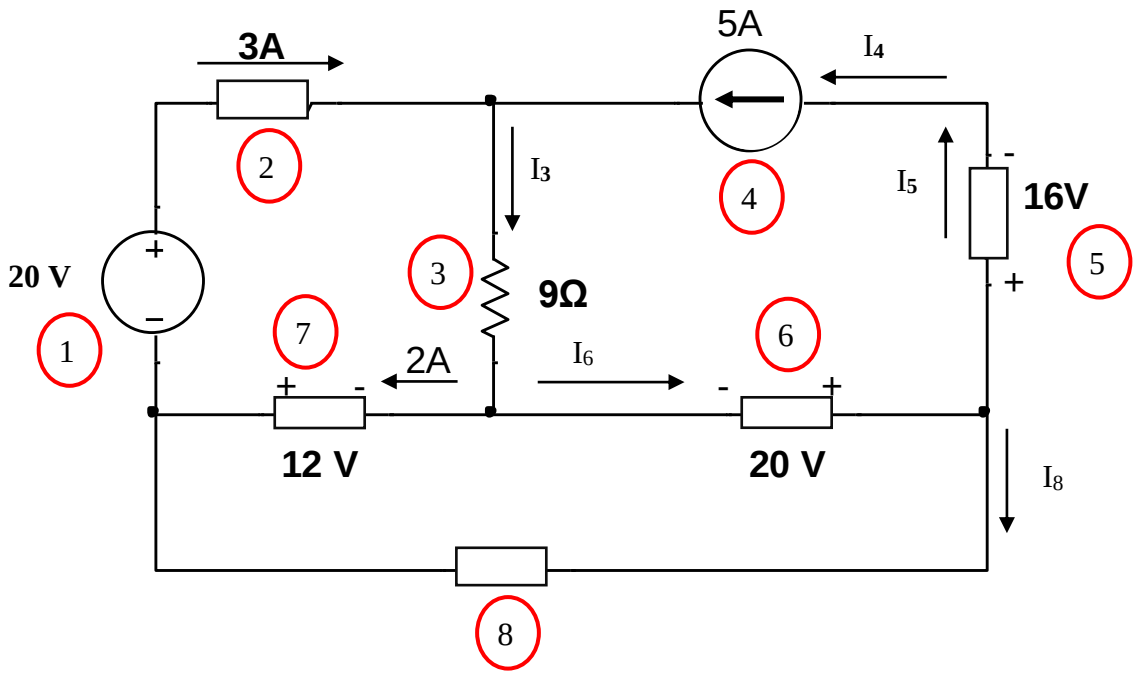

Fig. 1 b: Q1-b

Component Number	Voltage	Current	Power	Power (Absorbed or Supplied)
1				
2		**3**		
3				
4				
5	**16**			
6	**20**			
7	**12**	**2**		
8				

Q 2: [20 marks] Using Circuit Reduction (Reducing the resistors to one resistor) and basic circuit laws (KVL, KCL, Ohms Law, current division principle and voltage division principle) for the circuit shown in Figure 2, Calculate the equivalent resistance (total resistance) as seen from the source, I_1, I_2, V and the power supplied by the 4A source.

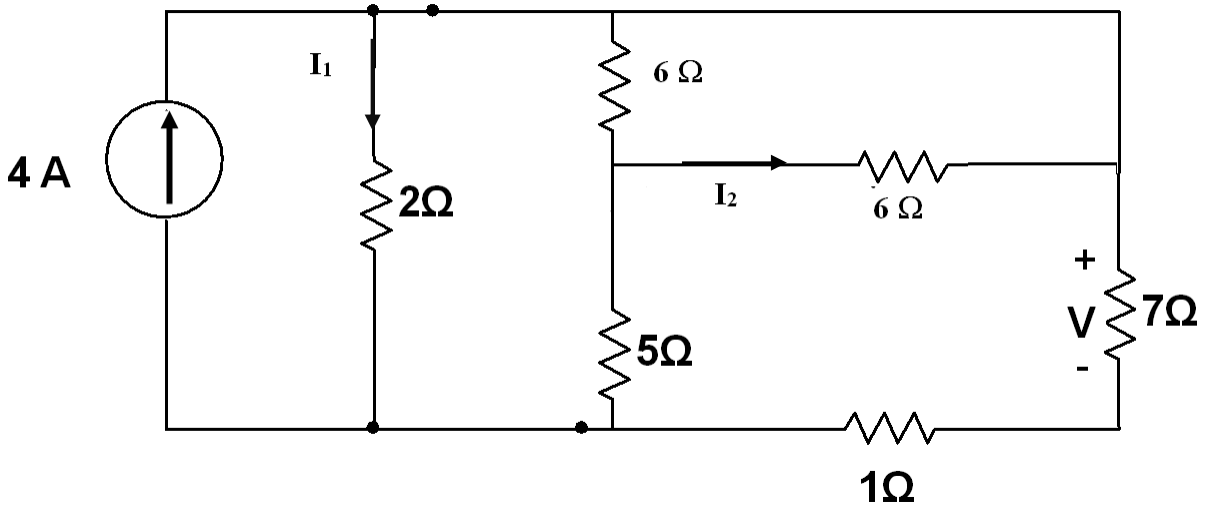

Fig.2: Circuit for Q 2

Q 3 [20 marks]: In the circuit shown in Fig. 3, Using Mesh analysis:
Identify your meshes, assign *clockwise* mesh currents, and write the mesh equations for the meshes assigned. [10]

Simplify the equations you wrote in part (A). [3]

Solve the simplified system in part (B). Provide numerical answers of your loop currents. [4]

Express V_x in terms of mesh currents. Provide numerical answer for V_x. [3]

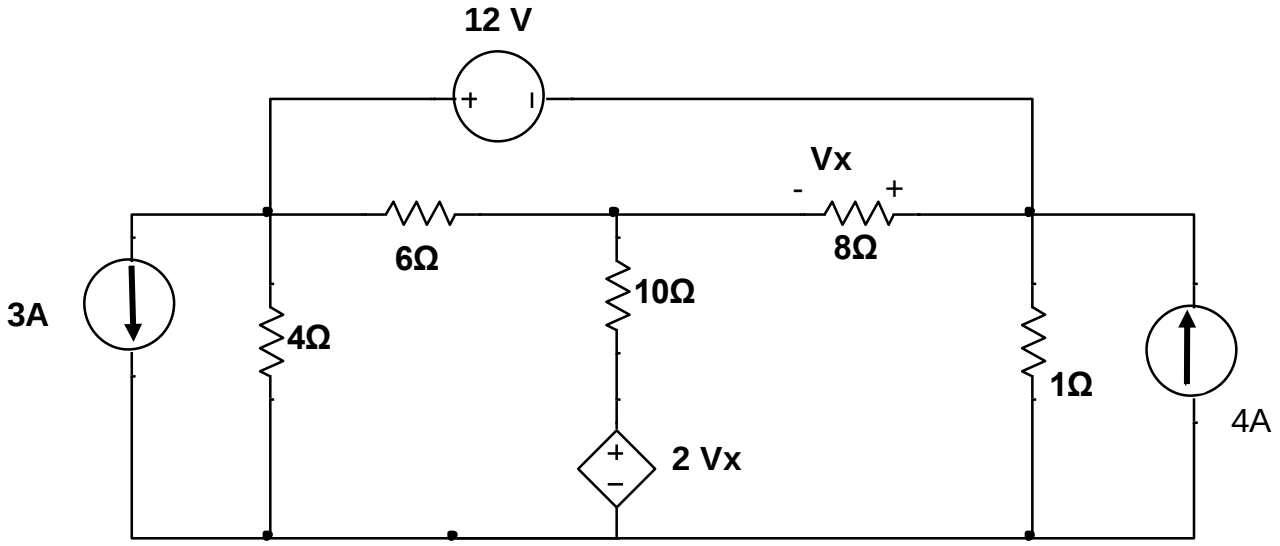

Fig. 3: Circuit for Q 3 - Mesh Analysis

Q 4 [20 marks]: In the circuit shown in Fig. 4, Use nodal analysis:

Write the nodal equations for the non-reference nodes. The reference node is at the bottom of the circuit. [9]

Simplify the equations you wrote in part (A). [4]

Solve the simplified system in part (B). Provide numerical answers for node voltages. [3]

Express I_x, I_y, and $\boldsymbol{V_x}$ in terms of node voltages, and provide numerical values for I_x, I_y, and $\boldsymbol{V_x}$ [4]

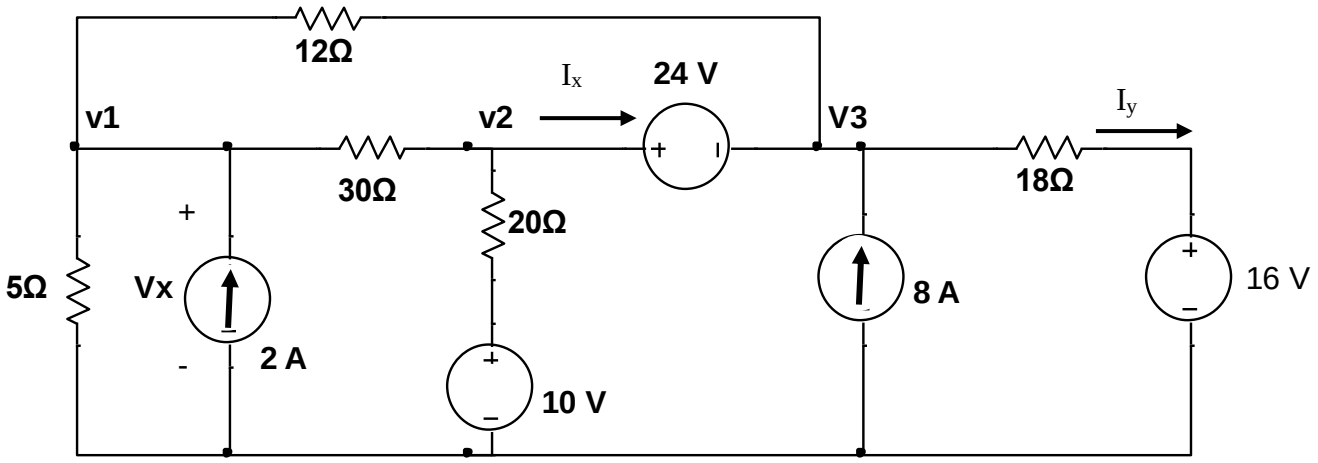

Fig. 4: Circuit for Q 4 – Nodal Analysis

Q 5: [20 marks] For the circuit shown in Fig. 5,

Determine the Thevenin's Equivalent at terminals **a** and **b**. For finding **V_{TH} only use Superposition principle** (if any other circuit analysis technique is used for finding Vth, marks will not be given). For finding R_{TH} any circuit analysis method of your choice can be used. [14]

Determine the value of the load resistor (R_L) for maximum power transfer to the load. [2]

Determine the value of the maximum power transferred to the load. [4]

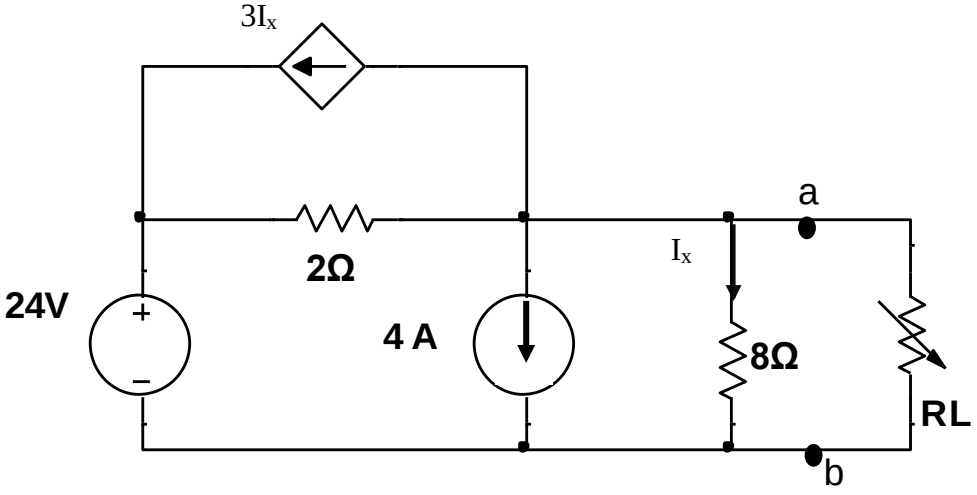

Fig. 5: Circuit for Q 5 – Thevenin's Theorem