

Department of Electrical, Computer, & Biomedical Engineering

Faculty of Engineering & Architectural Science

ELE 202

Electric Circuit Analysis

LAB COVER PAGE for Part I submission.

Lab #:		Lab Title:		
Last Na	me:			
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			Section #:	
		Submission	data and times	

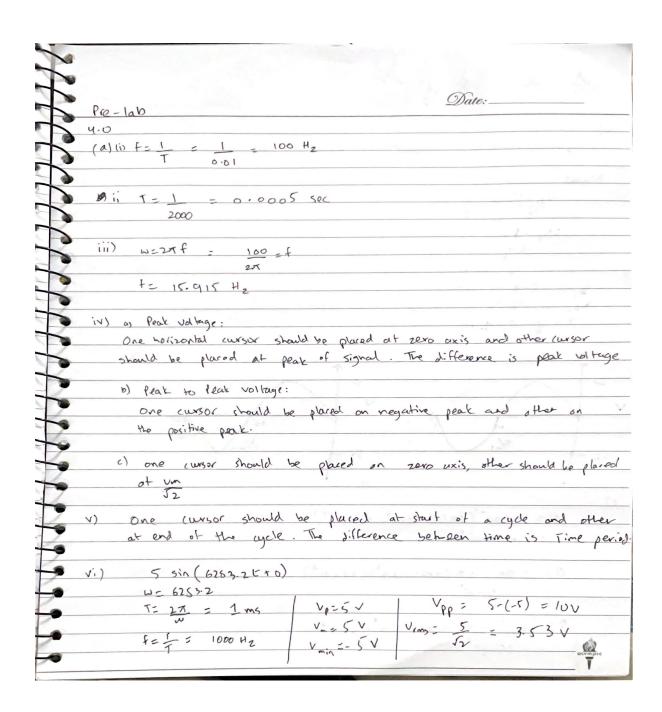
Document submission for Part I:

Due date and time:

- A completed and signed "COVER PAGE **Part I**" has to be included with your submission. The report will not be graded if the signed cover page is not included.
- Your completed handwritten pages of **Section 4.0** should be scanned (via a scanner or phone images), together with the required MultiSIM images. <u>Note:</u> MultiSIM results must be generated using the Department's licensed version of MultiSIM, and the captured screenshots should show your name (at the center-top) and the timestamp (at the bottom-right corner of your screen).
- Collate and create a *.pdf* or *.docx* file of the above, and upload it via D2L any time prior to the start of your scheduled lab. Upload instructions are provided on D2L.

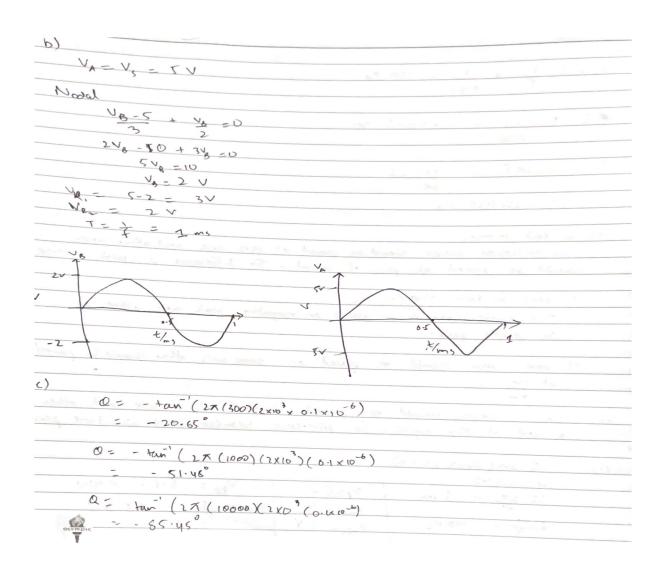
Zero marks will be assigned for the entire lab if this Part I is not submitted prior to your scheduled lab.

*By signing above, you attest that you have contributed to this submission and confirm that all work you have contributed to this submission is your own work. Any suspicion of copying or plagiarism in this work will result in an investigation of Academic Misconduct and may result in a "0" on the work, an "F" in the course, or possibly more severe penalties, as well as a Disciplinary Notice on your academic record under the Student Code of Academic Conduct, which can be found online at: www.ryerson.ca/senate/current/pol60.pdf.



A)

T	f	$\mathbf{V}_{\mathbf{P}}$	Vmax	Vmin	V _{P-P}	Vrms
(msec.)	(Hz)	(volts)	(volts)	(volts)	(volts)	(volts)
1	1000	5	5	5	10	3.53



b)

f	VA	V _B	V_{R1}	V_{R2}
(Hz)	(volts)	(volts)	(volts)	(volts)
1000	5	2	3	2

c)

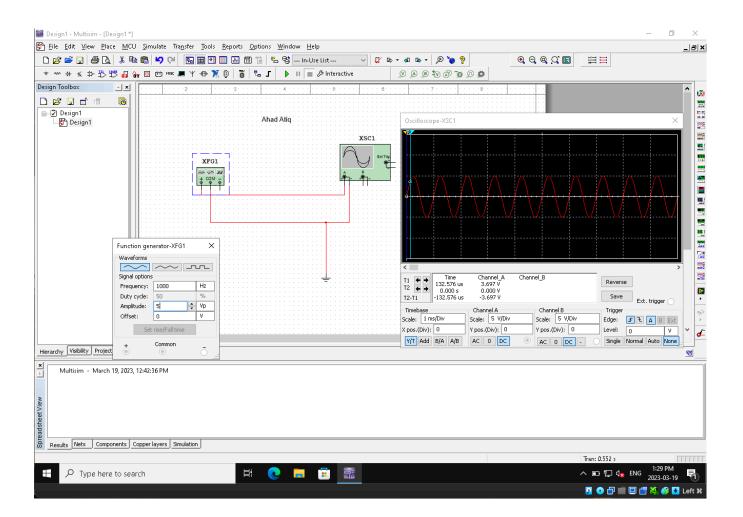
f =>	300	1000	10000	
	(Hz)	(Hz)	(Hz)	
$\Theta_{\rm o}$	20.65	51.48	85.45	

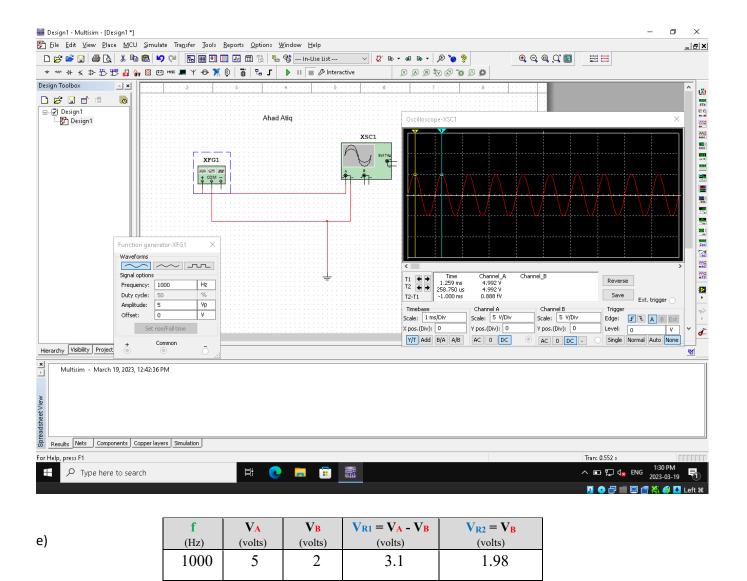
Table 2.1: Theoretical phase-shift values of Figure

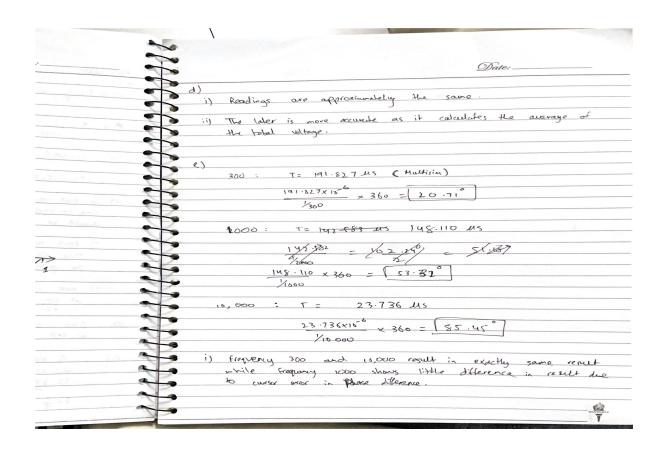
d)

T (msec.)	$\mathbf{f} = 1/\mathbf{T}$ (Hz)	V _P (volts)	V _{max} (volts)	Vmin (volts)	VP-P (volts)	Vrms (volts)
1.0	1000	5.0	4.992	4.992	10.0	3.69

Table 3.0: MultiSIM measured values of the sinewave properties







f	T	ΔT	θ ₀	
	calculated from freq., f	measured on MultiSIM	determined from ΔT	
300 (Hz)	0.003	20.71	20.65	
1000 (Hz)	0.001	53.32	51.48	
10000 (Hz)	0.0001	85.45	85.45	

f)

Table 3.2: MultiSIM measured phase-shift values of Figure 3.0 circuit

