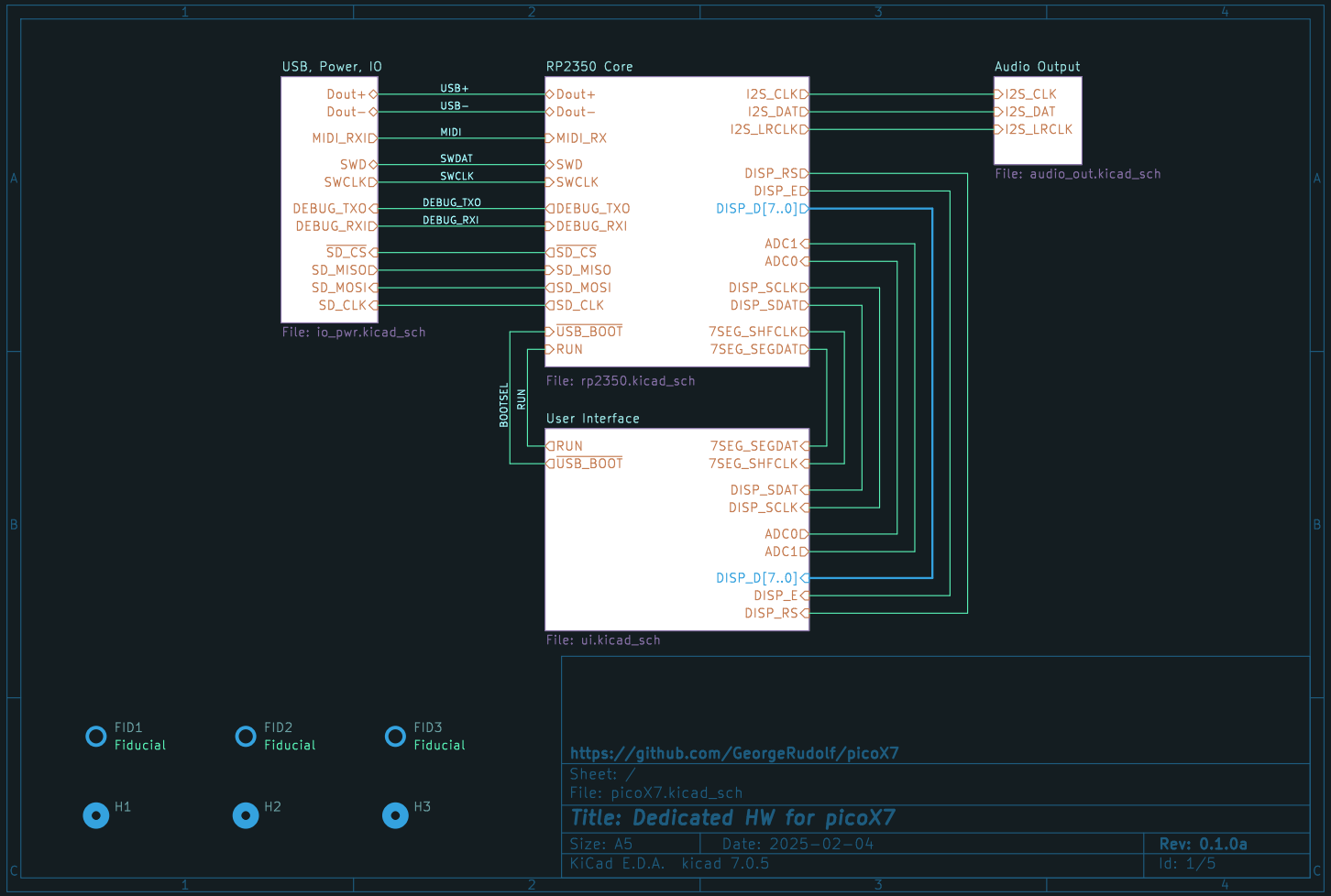
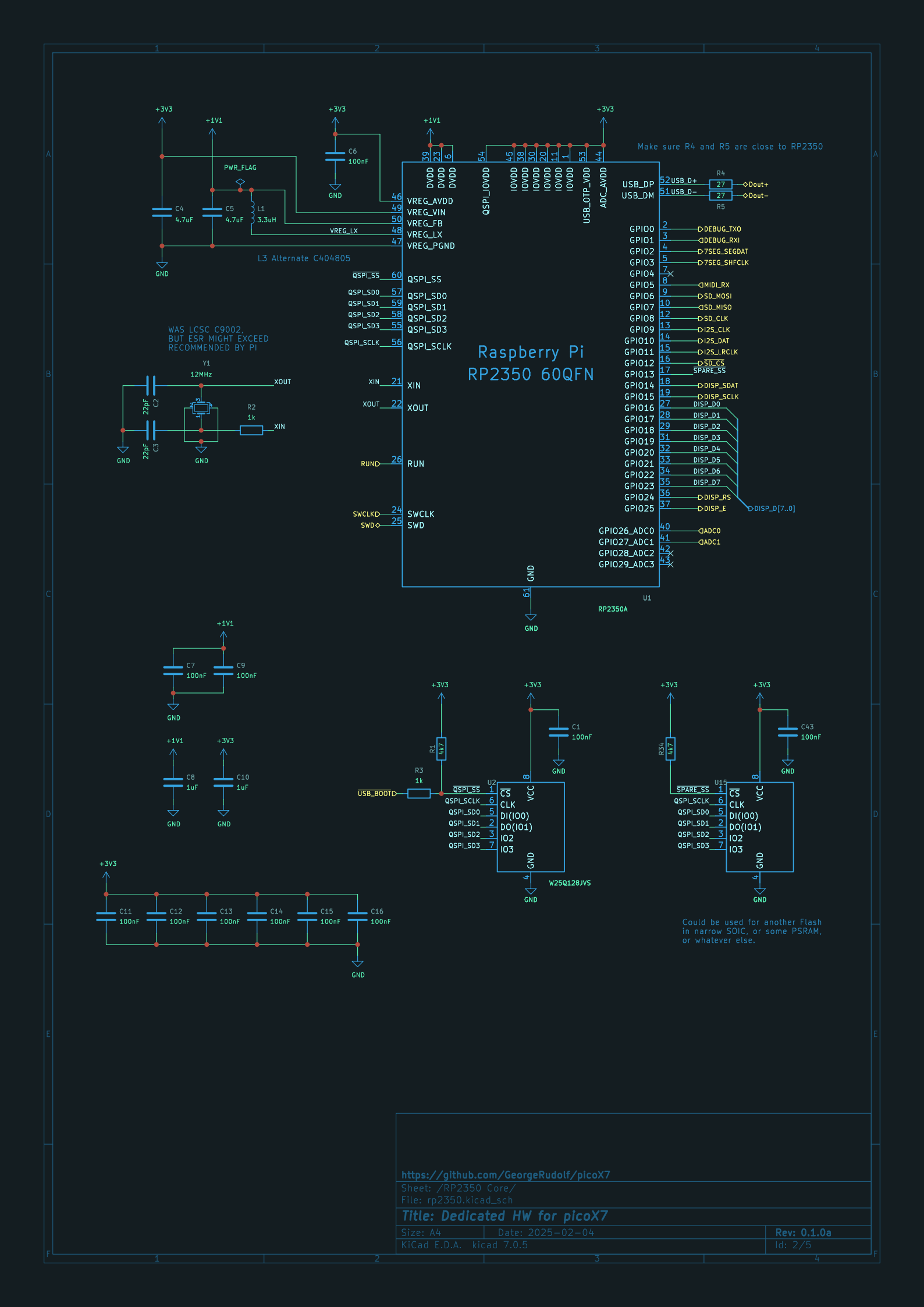
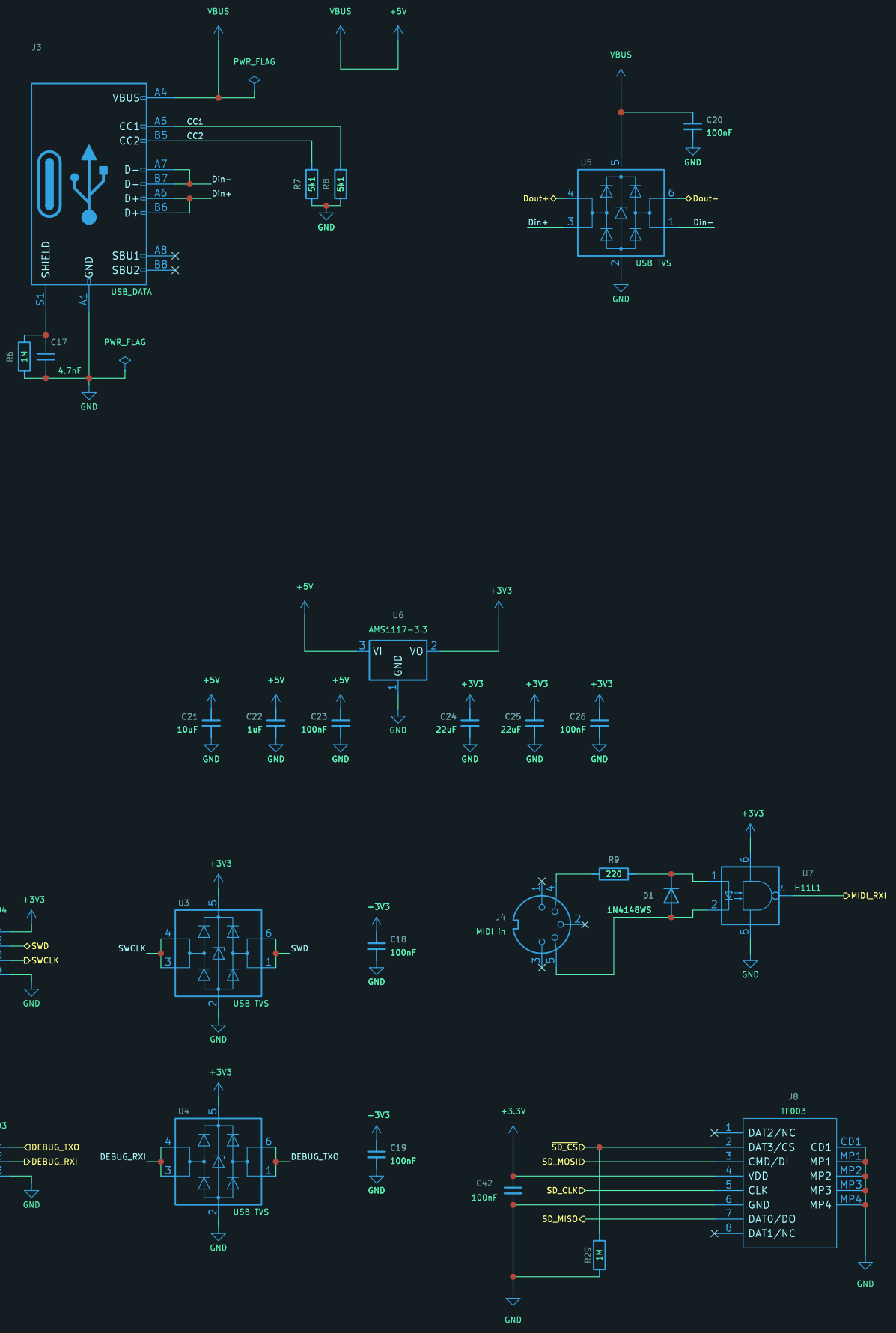

USB, Power, IO
Dout+
Dout−
MIDI_RXID
SWD
SWCLK
DEBUG_TXO
DEBUG_RXID
SD_CS
SD_MISO
SD_MOSI
SD_CLK
File: io_pwr.kicad_sch
USB+
USB−
MIDI
SWDAT
SWCLK
DEBUG_TXO
DEBUG_RXI
RP2350 Core
Dout+
Dout−
MIDI_RX
SWD
SWCLK
DEBUG_TXO
DEBUG_RXI
SD_CS
SD_MISO
SD_MOSI
SD_CLK
USB_BOOT
RUN
I2S_CLK
I2S_DAT
I2S_LRCLK
DISP_RS
DISP_ED
DISP_D[7..0]
ADC1
ADC0
DISP_SCLK
DISP_SDAT
7SEG_SHFCLK
7SEG_SEGDAT
File: rp2350.kicad_sch
Audio Output
I2S_CLK
I2S_DAT
I2S_LRCLK
File: audio_out.kicad_sch
BOOTSEL
RUN
User Interface
RUN
USB_BOOT
7SEG_SEGDAT
7SEG_SHFCLK
DISP_SDAT
DISP_SCLK
ADC0
ADC1
DISP_D[7..0]
DISP_E
DISP_RS
File: ui.kicad_sch
FID1
Fiducial
FID2
Fiducial
FID3
Fiducial
H1
H2
H3
https://github.com/GeorgeRudolf/picoX7
Sheet: /
File: picoX7.kicad_sch
Title: Dedicated HW for picoX7
Size: A5
Date: 2025-02-04
Rev: 0.1.0a
KiCad E.D.A. kicad 7.0.5
Id: 1/5


Raspberry Pi
RP2350 60QFN
RP2350A
U1
Make sure R4 and R5 are close to RP2350
L3 Alternate C404805
WAS LCSC C9002,
BUT ESR MIGHT EXCEED
RECOMMENDED BY PI
Could be used for another Flash
in narrow SOIC, or some PSRAM,
or whatever else.
W25Q128JVS
https://github.com/GeorgeRudolf/picoX7
Sheet: /RP2350 Core/
File: rp2350.kicad_sch
Title: Dedicated HW for picoX7
Size: A4
Date: 2025-02-04
Rev: 0.1.0a
KiCad E.D.A. kicad 7.0.5
Id: 2/5



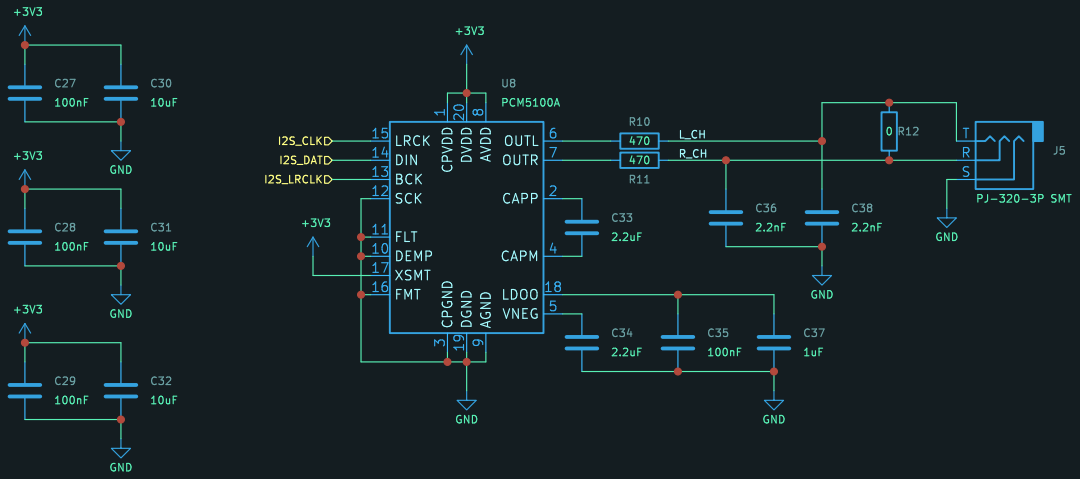
https://github.com/GeorgeRudolf/picoX7

Sheet: /USB, Power, IO/

File: io_pwr.kicad_sch

Title: Dedicated HW for picoX7

Size: A4	Date: 2025-02-04	Rev: 0.1.0a
KiCad E.D.A. kicad 7.0.5		Id: 3/5



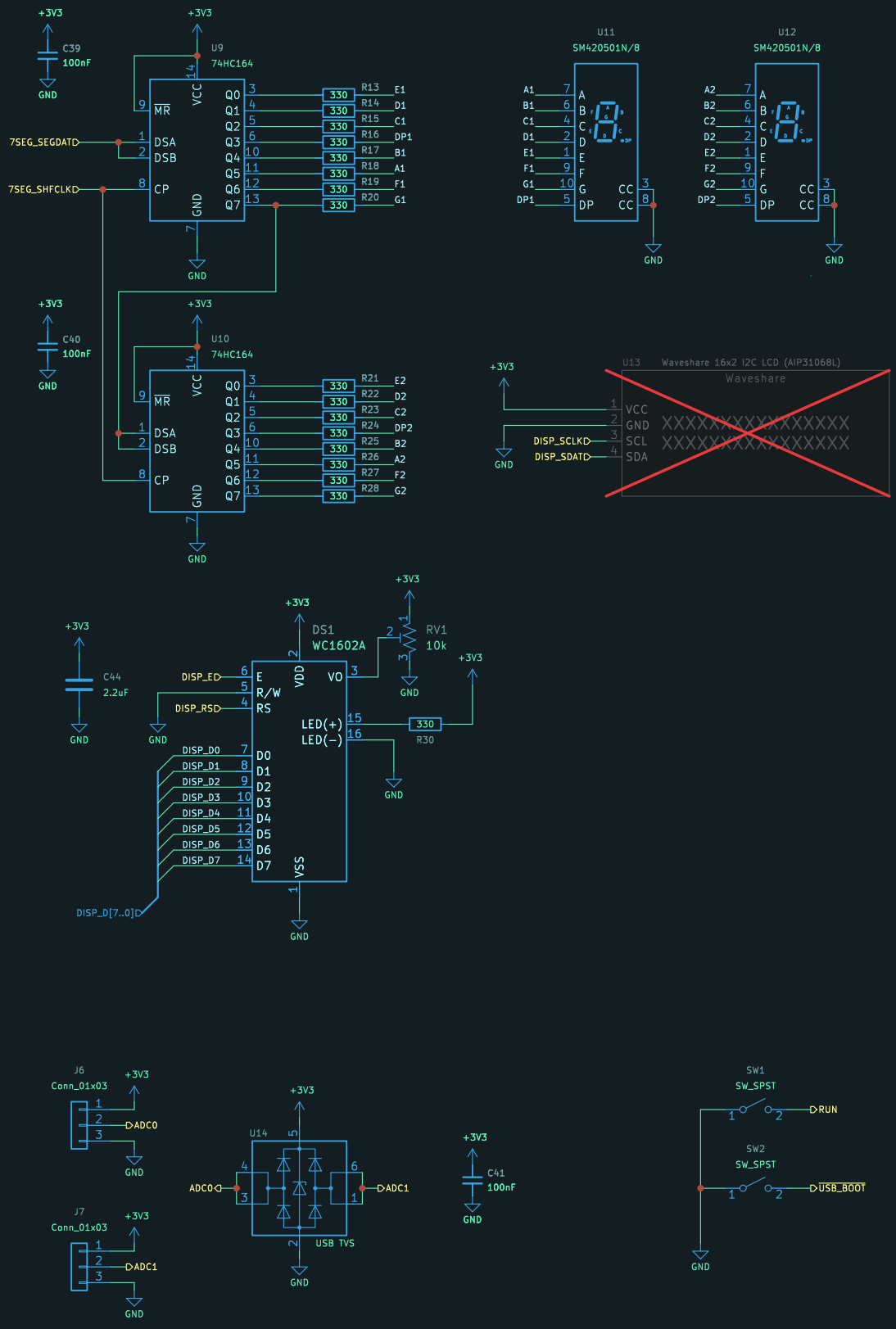
https://github.com/GeorgeRudolf/picoX7

Sheet: /Audio Output/

File: audio_out.kicad_sch

Title: Dedicated HW for picoX7

Size: A5	Date: 2025-02-04	Rev: 0.1.0a
KiCad E.D.A. kicad 7.0.5		Id: 4/5



https://github.com/GeorgeRudolf/picoX7

Sheet: /User Interface/

File: ui.kicad_sch

Title: Dedicated HW for picoX7

Size: A4	Date: 2025-02-04	Rev: 0.1.0a
KiCad E.D.A. kicad 7.0.5		Id: 5/5