# STATE MACHINES

Lecture-10

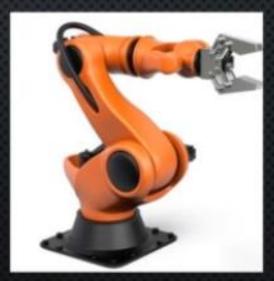
# FINITE STATE MACHINE (FSM)







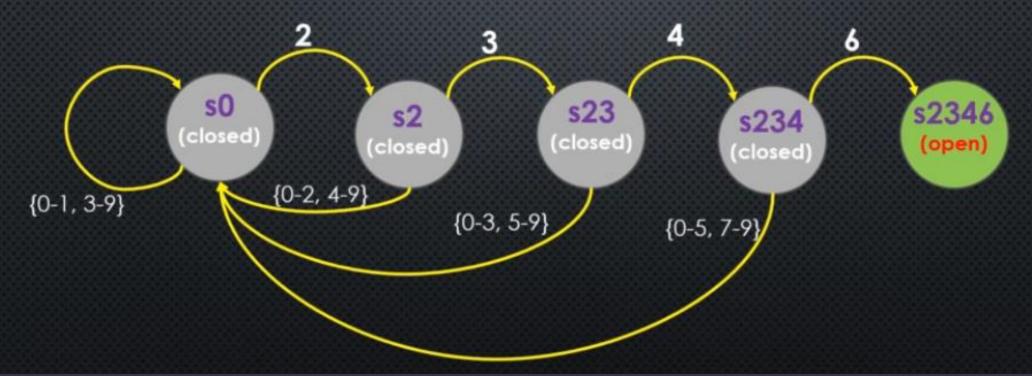




#### STATE TRANSITION DIAGRAM



Code to unlock the door is 2, 3, 4, 6



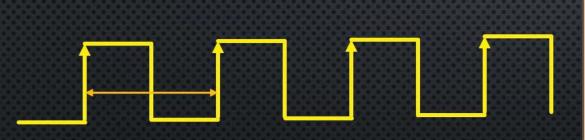
#### FSM - CONCEPTS

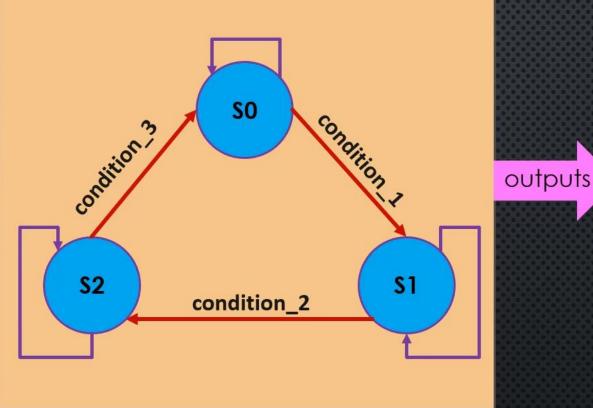
Sequential Digital Circuit



Transitions

inputs





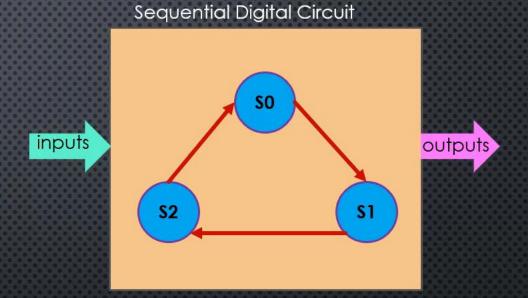
In  $SO \rightarrow if$  (condition\_1) go to S1 else stay in SO

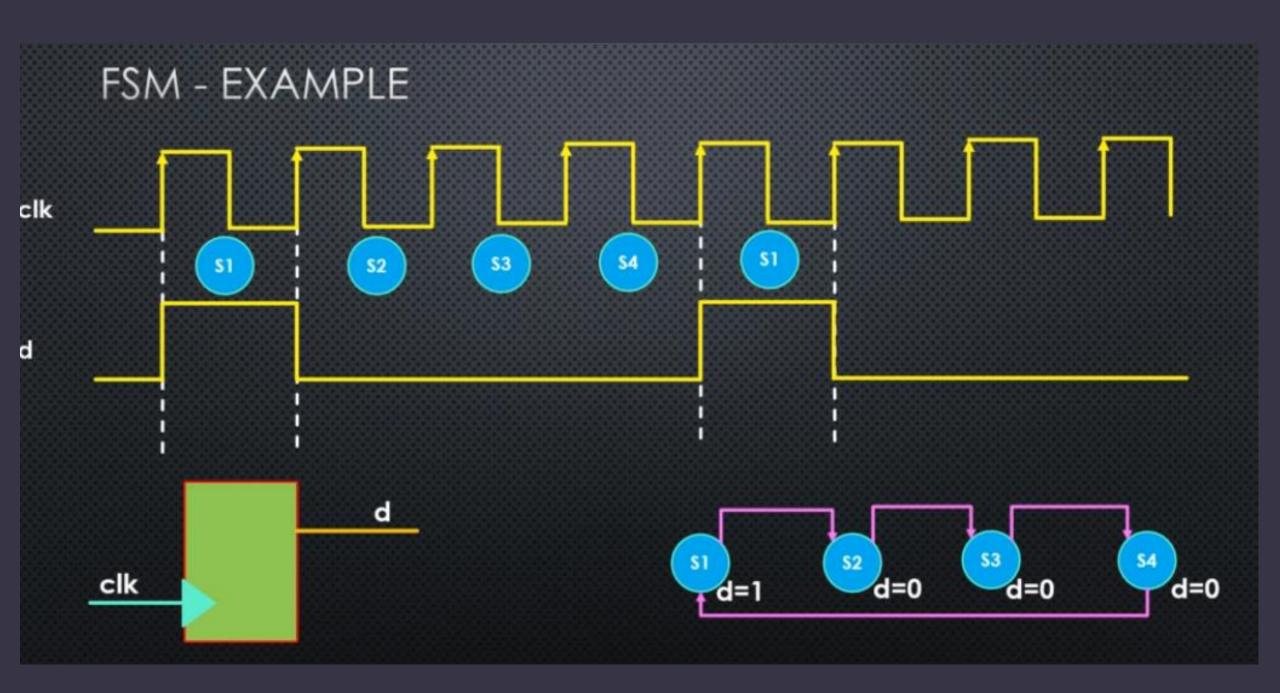
In S1→ if (condition\_2) go to S2 else stay in S1

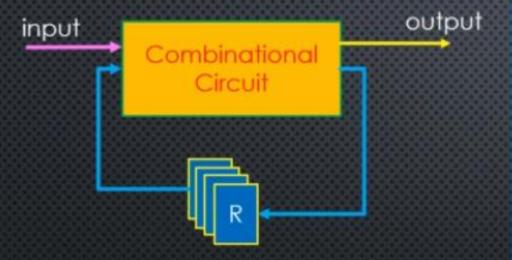
In  $S2 \rightarrow if$  (condition\_3) go to S0 else stay in S2

#### FSM - ELEMENTS

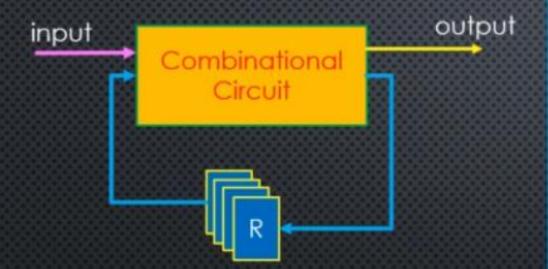
- **❖**Registers
- **\***Conditions
- Conditional Statement





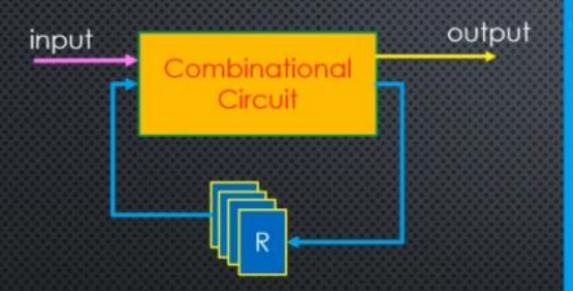


void function\_name(arguments) {



enumerate type to define list of states

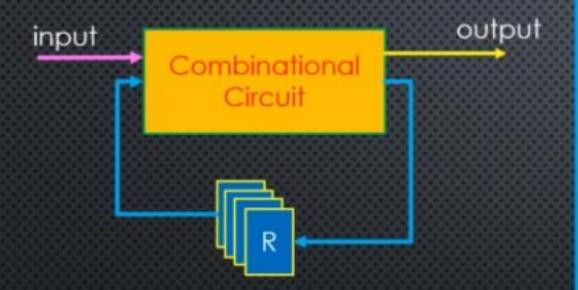
void function\_name(arguments) {



enumerate type to define list of states

void function\_name(arguments) {

states by defining static variables

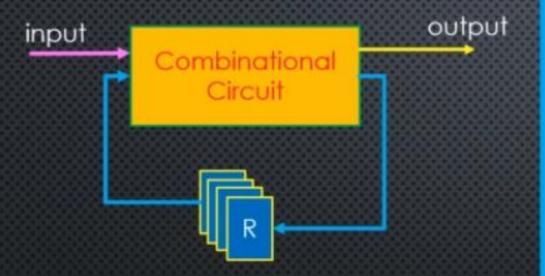


enumerate type to define list of states

void function\_name(arguments) {

states by defining static variables

next-state variable definition



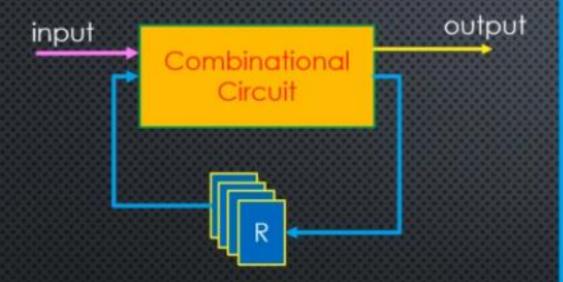
enumerate type to define list of states

void function\_name(arguments) {

states by defining static variables

next-state variable definition

output-local variable definition



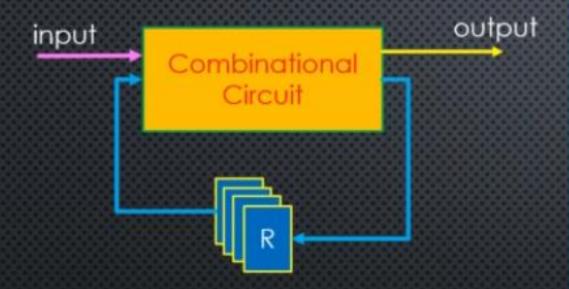
enumerate type to define list of states

void function\_name(arguments) {

states by defining static variables next-state variable definition

output-local variable definition

switch-case statement to implement all transitions



enumerate type to define list of states

void function\_name(arguments) {

states by defining static variables next-state variable definition output-local variable definition

switch-case statement to implement all transitions

**Update states** 

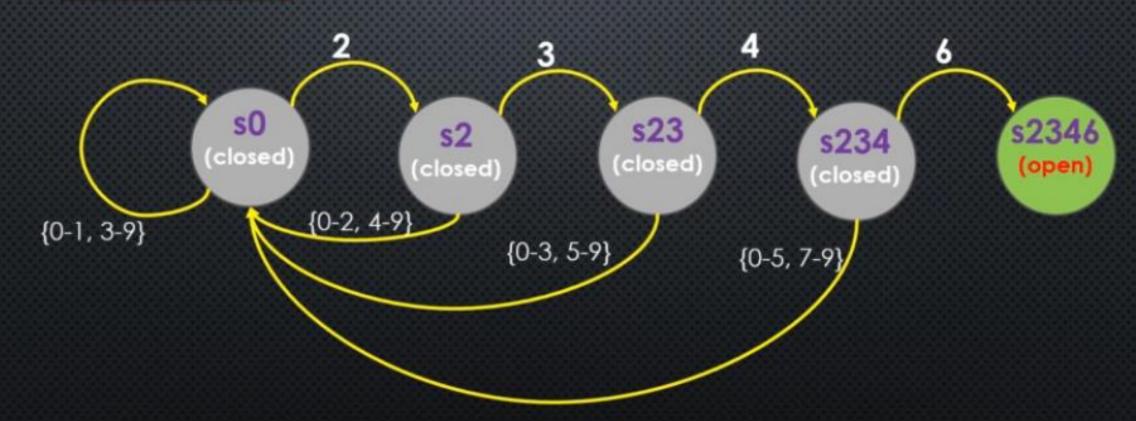
assign output-variables to output arguments

}

## SEQUENCE FINDER



Code to unlock the door is 2, 3, 4, 6



```
typedef enum {s0, s2, s23, s234, s2346} state_type;
```

```
typedef enum {s0, s2, s23, s234, s2346} state_type;
void sequence_finder(ap_uint<4> x, bool &door_open ) {
```

```
typedef enum {s0, s2, s23, s234, s2346} state_type;

void sequence_finder(ap_uint<4> x, bool &door_open ) {
    #pragma HLS INTERFACE ap_none port=x
    #pragma HLS INTERFACE ap_none port=door_open
    #pragma HLS INTERFACE ap_ctrl_none port=return
```

```
typedef enum {s0, s2, s23, s234, s2346} state_type;
void sequence_finder(ap_uint<4> x, bool &door_open ) {
#pragma HLS INTERFACE ap_none port=x
#pragma HLS INTERFACE ap_none port=door_open
#pragma HLS INTERFACE ap_ctrl_none port=return
//-----state variables------
 static state type state = s0;
 state_type next_state;
//----temporary output variables-----
 bool door_open_local = 0;
. . .
```

```
//----switch case-----
 switch(state) {
 case so:
   if (x == 2) {
     next_state = s2;
   } else {
     next_state = s0;
   door_open_local = 0;
   break;
 case s2:
   break;
```

```
case s23:
  break;
case s234:
  break;
case s2346:
 break;
default:
  break;
```

```
//----switch case-----
 switch(state) {
 case so:
   if (x == 2) {
     next_state = s2;
   } else {
     next_state = s0;
   door_open_local = 0;
   break;
 case s2:
   if (x == 3) {
     next_state = s23;
   } else {
     next_state = s0;
   door_open_local = 0;
   break;
```

```
case s23:
 break;
case s234:
  break;
case s2346:
  break;
default:
  break;
```

```
//----switch case-----
 switch(state) {
 case so:
   if (x == 2) {
     next_state = s2;
   } else {
     next_state = s0;
   door_open_local = 0;
   break;
 case s2:
   if (x == 3) {
     next_state = s23;
   } else {
     next_state = s0;
   door_open_local = 0;
   break;
```

```
case s23:
 if (x == 4) {
    next_state = s234;
 } else {
    next_state = s0;
  door_open_local = 0;
  break;
case s234:
 break;
case s2346:
  break;
default:
  break;
```

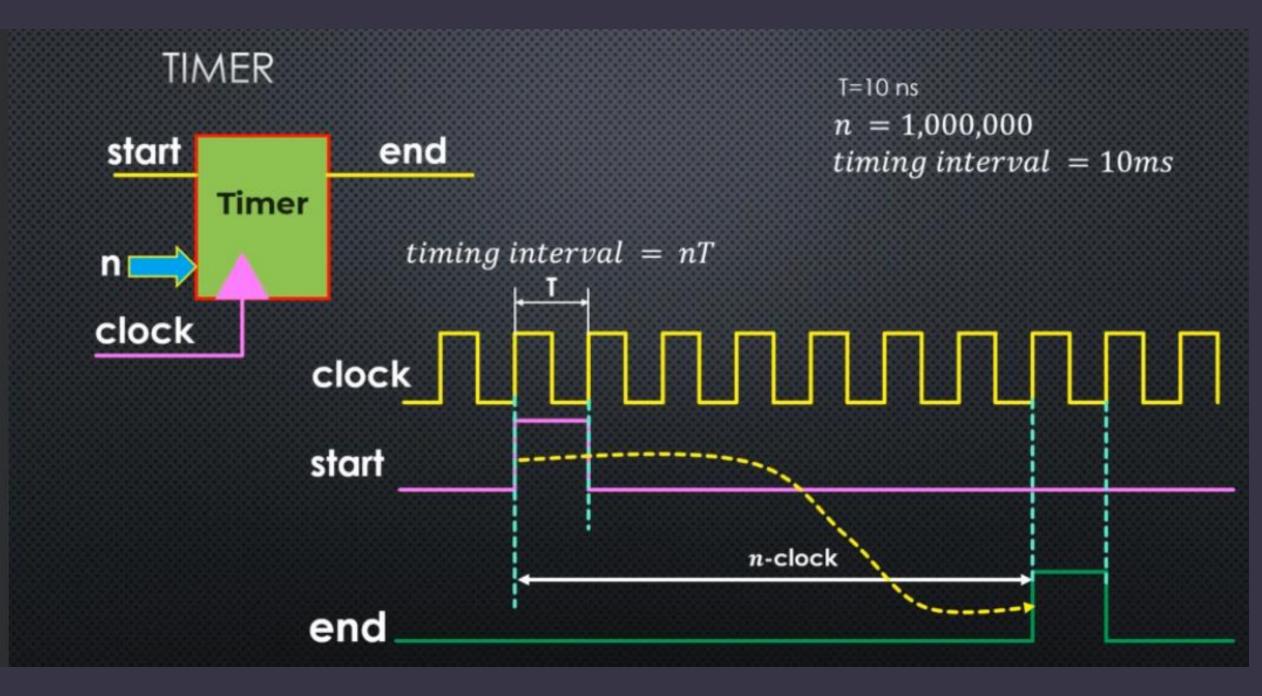
```
//----switch case-----
 switch(state) {
 case so:
   if(x == 2) {
     next_state = s2;
   } else {
     next_state = s0;
   door_open_local = 0;
   break;
 case s2:
   if (x == 3) {
     next_state = s23;
   } else {
     next state = s\theta;
   door_open_local = 0;
   break;
```

```
case s23:
  if (x == 4) {
    next_state = s234;
 } else {
    next state = s\theta;
  door_open_local = 0;
  break;
case s234:
  if (x == 6) {
    next state = s2346;
 } else {
    next_state = s0;
  door_open_local = 0;
  break;
case s2346:
  next_state = s\theta;
  door_open_local = 1;
  break;
default:
  break;
```

```
//-----state and output variable assignments ----
state = next_state;
door_open = door_open_local;
}
```

# TIMER

Lecture-10



```
#define N 16
typedef enum{idle, running} timer_state_type;
```

```
#define N 16
typedef enum{idle, running} timer_state_type;

void timer(ap_wint<N> n, bool start, bool &end) {
#pragma HLS INTERFACE ap_none port=start
#pragma HLS INTERFACE ap_none port=end
#pragma HLS INTERFACE ap_none port=n
#pragma HLS INTERFACE ap_ctrl_none port=return
```

```
#define N 16
typedef enum{idle, running} timer_state_type;

void timer(ap_uint<N> n, bool start, bool &end) {
#pragma HLS INTERFACE ap_none port=start
#pragma HLS INTERFACE ap_none port=end
#pragma HLS INTERFACE ap_none port=n
#pragma HLS INTERFACE ap_ctrl_none port=return

static timer_state_type state = idle;
static unsigned long long int timer_variable = 0;
```

```
#define N 16
typedef enum{idle, running} timer_state_type;
void timer(ap_uint<N> n, bool start, bool &end) {
#pragma HLS INTERFACE ap none port=start
#pragma HLS INTERFACE ap_none port=end
#pragma HLS INTERFACE ap_none port=n
#pragma HLS INTERFACE ap_ctrl_none port=return
 static timer state type state = idle;
 static unsigned long long int timer_variable = 0;
 timer state type next state;
 unsigned long long int next_timer_variable;
 bool end local;
```

```
#define N 16
typedef enum{idle, running} timer_state_type;
void timer(ap_uint<N> n, bool start, bool &end) {
#pragma HLS INTERFACE ap none port=start
#pragma HLS INTERFACE ap_none port=end
#pragma HLS INTERFACE ap none port=n
#pragma HLS INTERFACE ap_ctrl_none port=return
 static timer_state_type state = idle;
 static unsigned long long int timer_variable = 0;
 timer state type next state;
  unsigned long long int next timer variable;
 bool end_local;
 switch(state) {
```

```
#define N 16
typedef enum{idle, running} timer state type;
void timer(ap uint<N> n, bool start, bool &end) {
#pragma HLS INTERFACE ap_none port=start
#pragma HLS INTERFACE ap_none port=end
#pragma HLS INTERFACE ap_none port=n
#pragma HLS INTERFACE ap ctrl none port=return
 static timer_state_type state = idle;
  static unsigned long long int timer_variable = 0;
 timer_state_type next_state;
 unsigned long long int next timer variable;
 bool end_local;
  switch(state) {
 state
                = next state;
 timer_variable = next_timer_variable;
                = end_local;
  end
```

```
#define N 16
typedef enum{idle, running} timer_state_type;
void timer(ap wint<N> n, bool start, bool &end)
#pragma HLS INTERFACE ap none port=start
#pragma HLS INTERFACE ap_none port=end
#pragma HLS INTERFACE ap none port=n
#pragma HLS INTERFACE ap ctrl none port=return
  static timer state type state = idle;
  static unsigned long long int timer variable = 0;
  timer state type next state;
  unsigned long long int next timer variable;
  bool end local;
  state
                 = next state;
  timer variable = next timer variable;
                 = end_local;
  end
```

```
switch(state) {
case idle:
 if (start == 1) {
   next_state
                  = running;
   end local = 0;
   next_timer_variable = 0;
 } else {
   next state
                   = idle:
   end_local = 0;
   next timer variable = 0;
 break;
case running:
 break;
default:
                                         running
 break:
                      idle
```

```
#define N 16
typedef enum{idle, running} timer state type;
void timer(ap wint<N> n, bool start, bool &end)
#pragma HLS INTERFACE ap none port=start
#pragma HLS INTERFACE ap_none port=end
#pragma HLS INTERFACE ap none port=n
#pragma HLS INTERFACE ap ctrl none port=meturn
 static timer state type state = idle;
 static unsigned long long int timer variable = 0;
 timer state type next state;
 unsigned long long int next timer variable;
 bool end local;
                = next state;
 state
 timer variable = next timer variable;
                = end_local;
 end
```

```
switch(state) {
case idle:
 if (start == 1) {
   next state
              = running;
   end local = 0;
   next timer variable = 0;
 } else {
   next state = idle:
   end local = 0;
   next timer variable = 0;
 break;
case running:
 if (timer variable == n-1) {
   next state
                      = idle:
   end local
   next timer variable = 0;
 } else {
   next_timer_variable = timer_variable+1;
   next state
                      = running;
   end local
                     = 0:
 break;
default:
                                      running
 break;
                    idle
```

# Any Question...

# Thank you