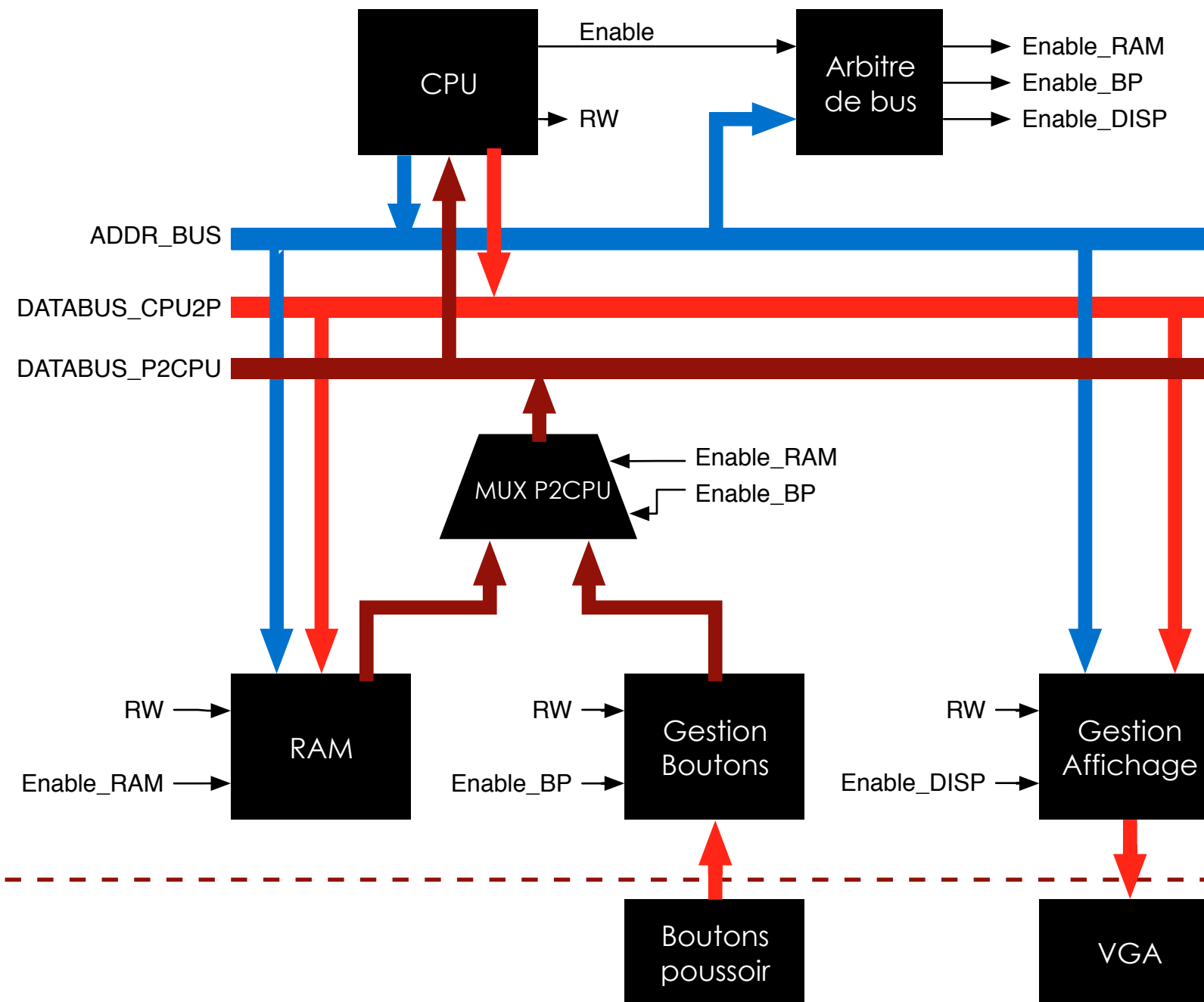
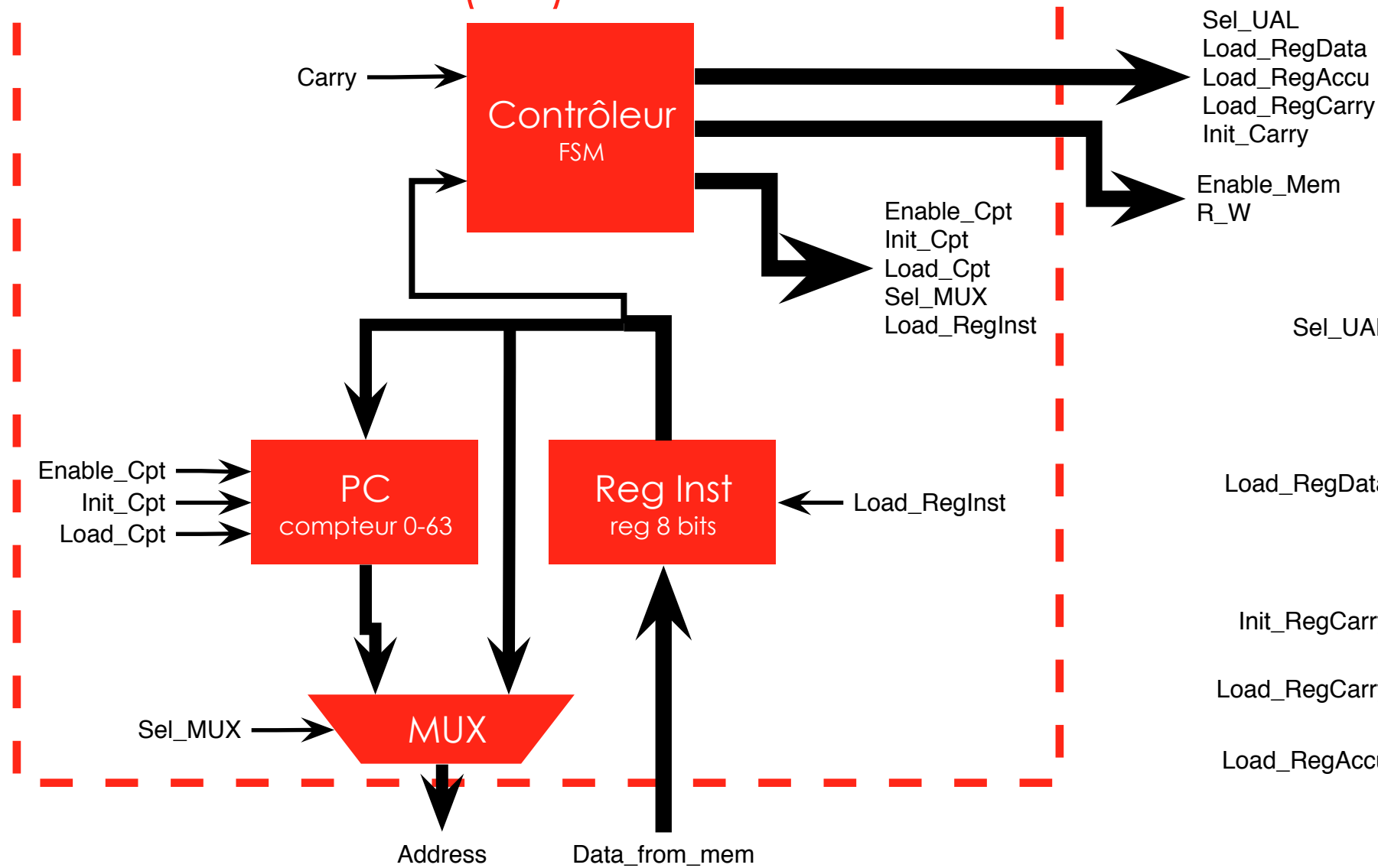


Carte Digilent Nexys 3

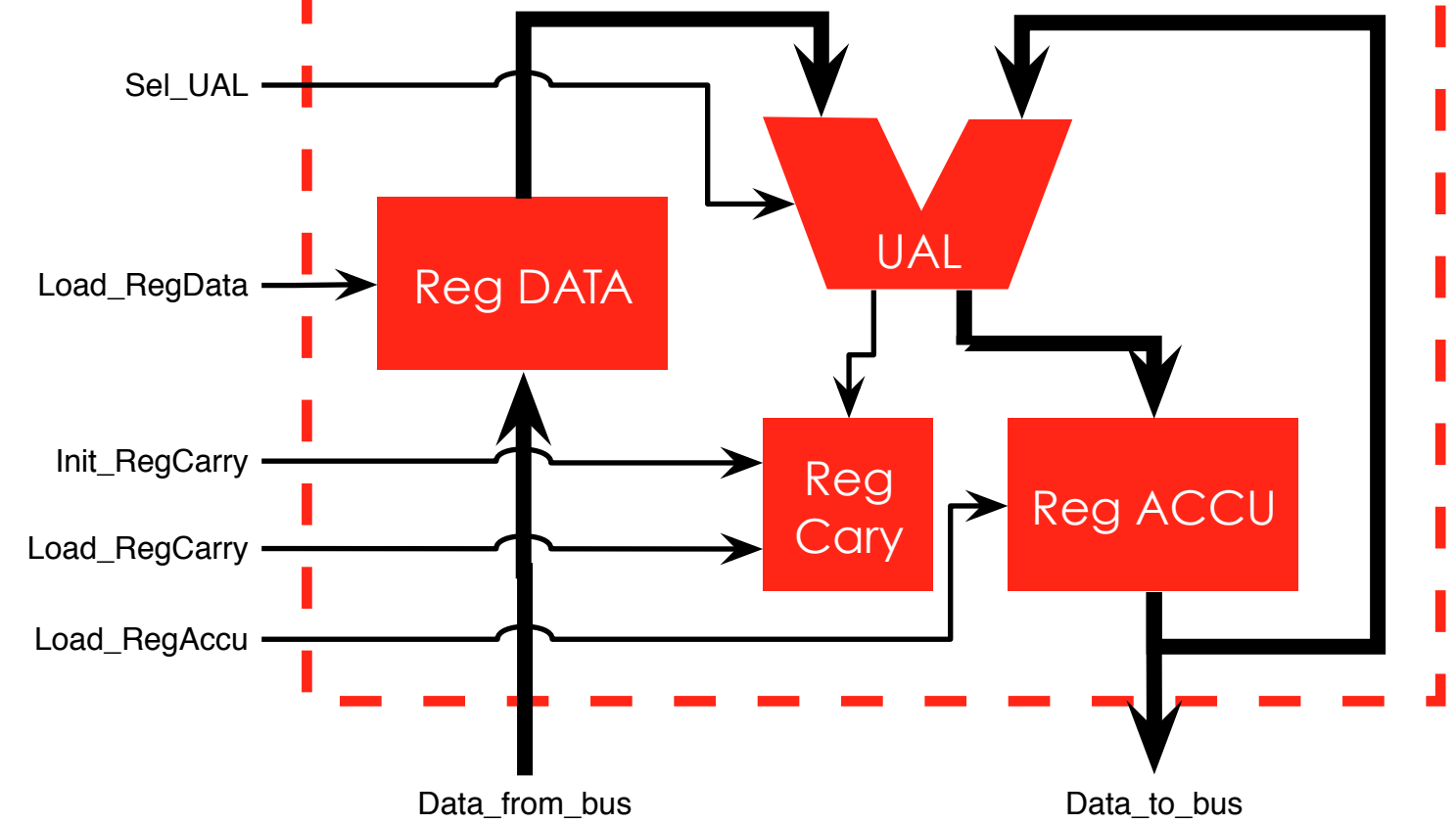
FPGA Xilinx Spartan 6



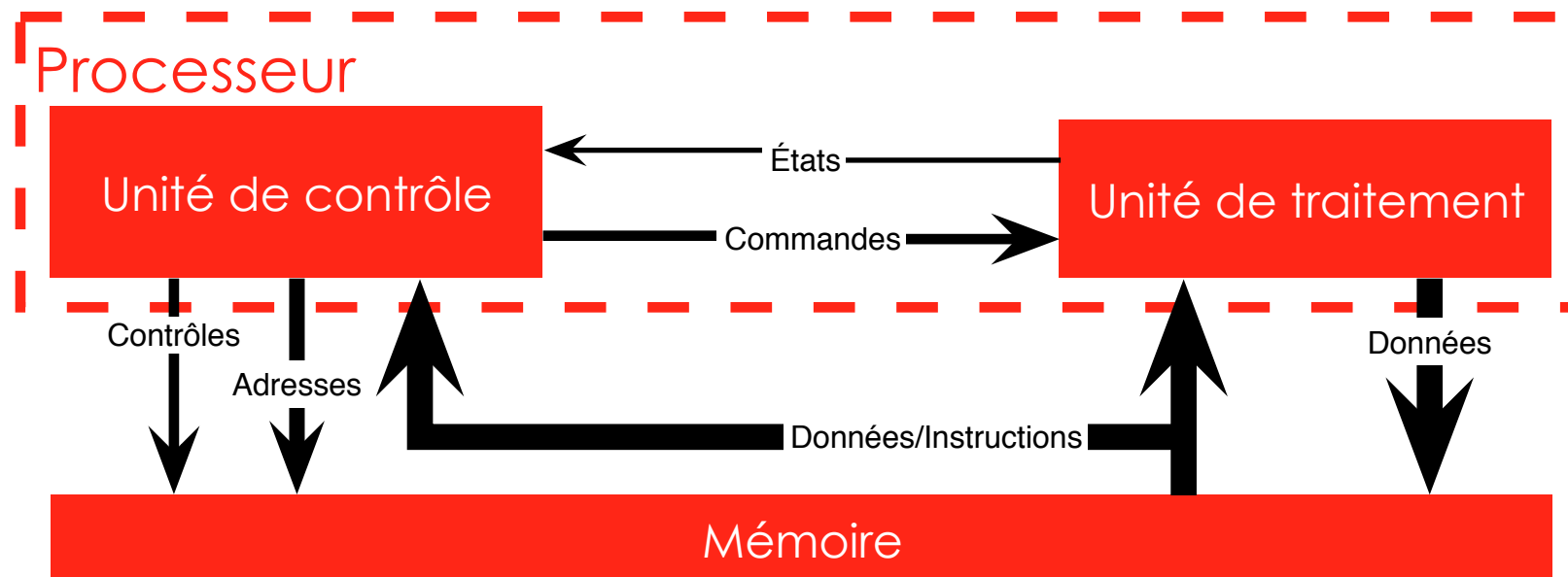
Unité de Contrôle (UC)

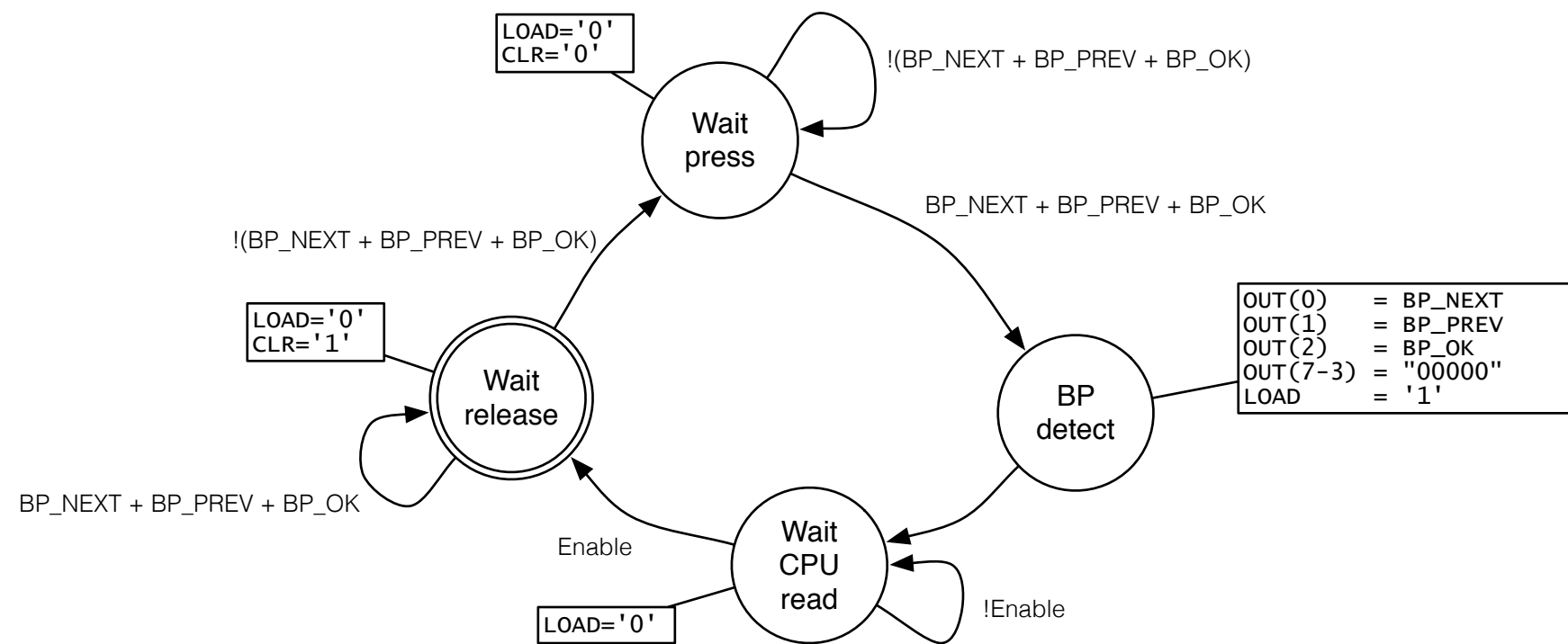
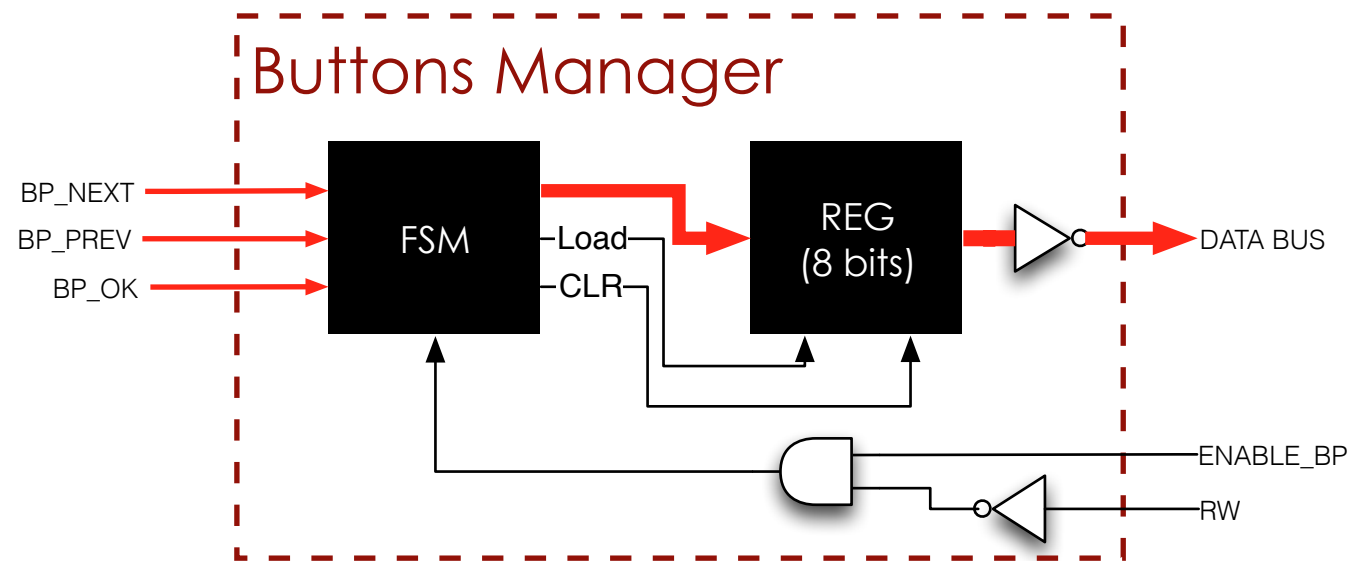


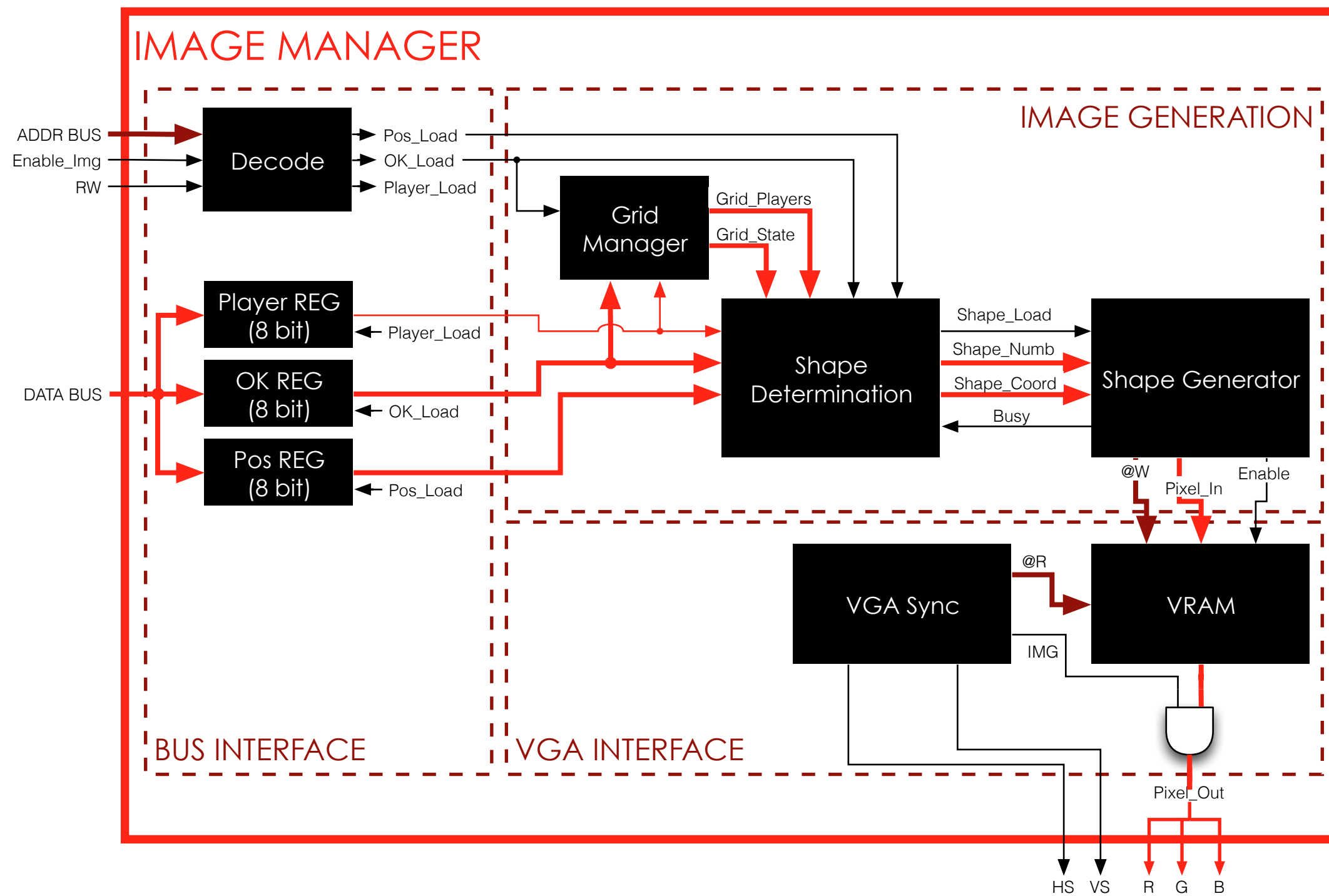
Unité de Traitement (UT)



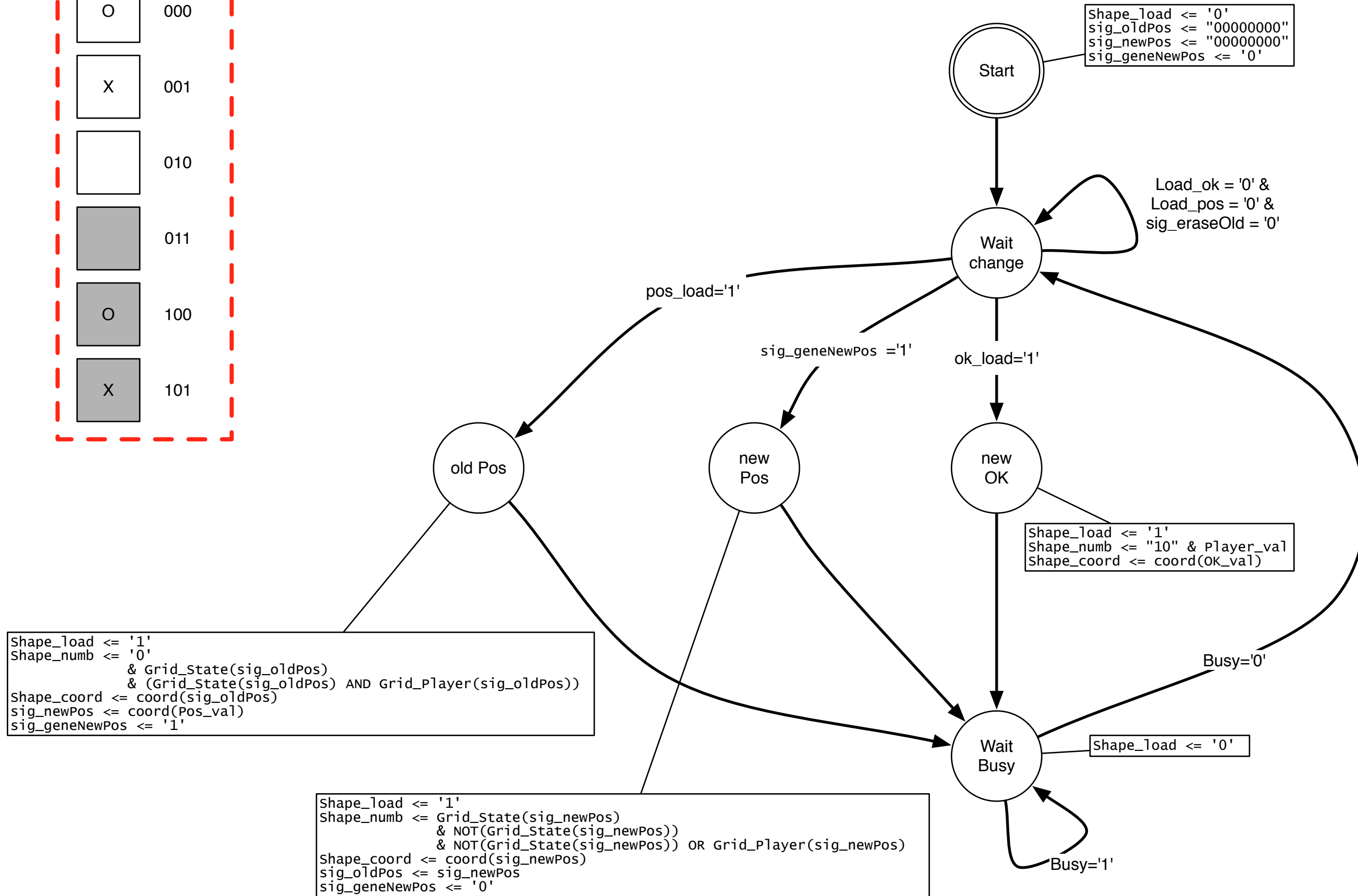
Processeur

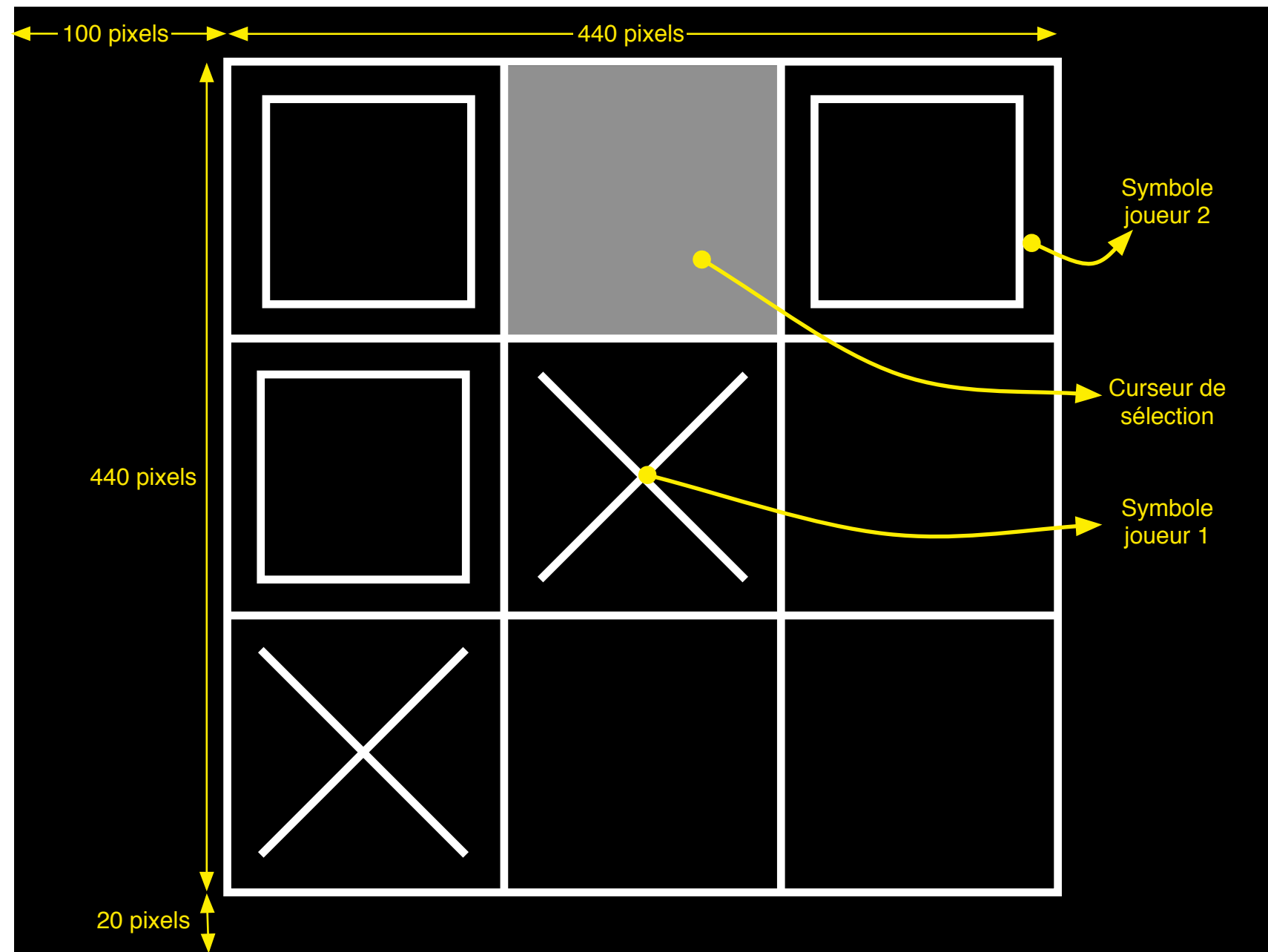






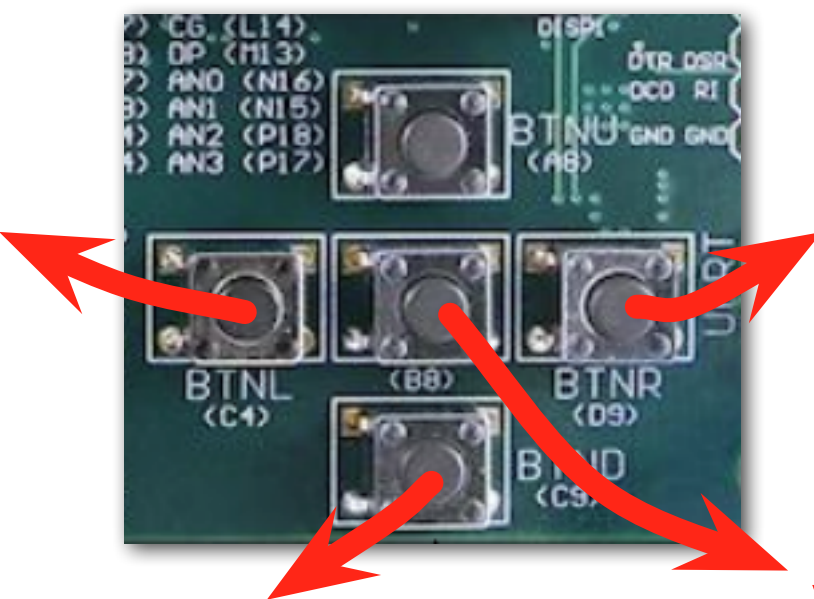
| Shape_Numb | | |
|------------|--|-----|
| O | | 000 |
| X | | 001 |
| | | 010 |
| | | 011 |
| O | | 100 |
| X | | 101 |





Case précédente

Case suivante



Reset

Validation