

1a. 0 tag bits, 3 bit byte offset, 1 bit block offset

cache size
 $1M = 2^{20} / (2^3 \cdot 2^1 \cdot 1) = 2^{16}$ ← sets

$32 - 16 - 3 - 1 = 12$ tag bits

b. 2 bit byte offset, 0 bit block offset, 2 tag bits, 28 index bits

cache size
 $1M = 2^{20} / (2^2 \cdot 4) = 2^{16}$ ← sets

$32 - 2 - 16 = 14$ tag bits

12 bit byte offset, 0 bit block offset, 16 index bits, 14 tag bits

2a. cold miss, cold miss, miss, miss, miss, miss

4 0 4
5 1 5
All misses

8 misses

b. 4 misses ← cold start

set assoc
 with 2-word block
 miss → 0, 1
 hit → 4, 5

2 miss

3a, F

b. T

c. F

(have to write to everything for write-through)

6.5