

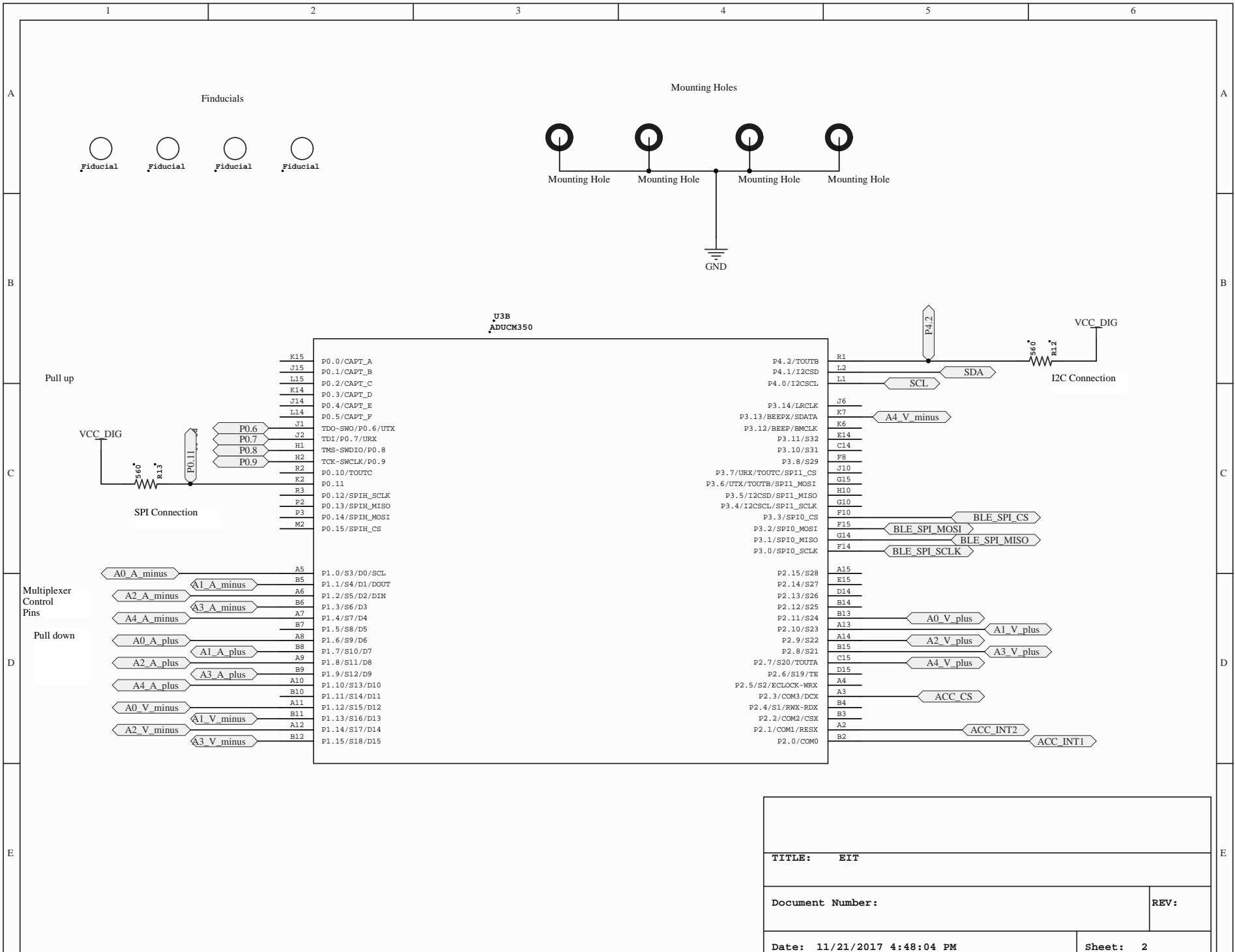
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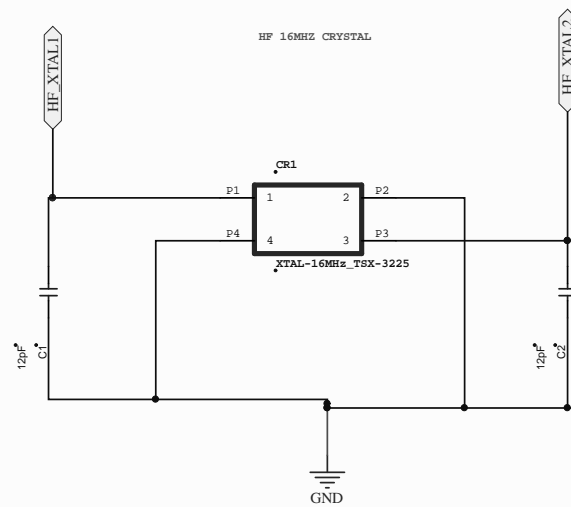
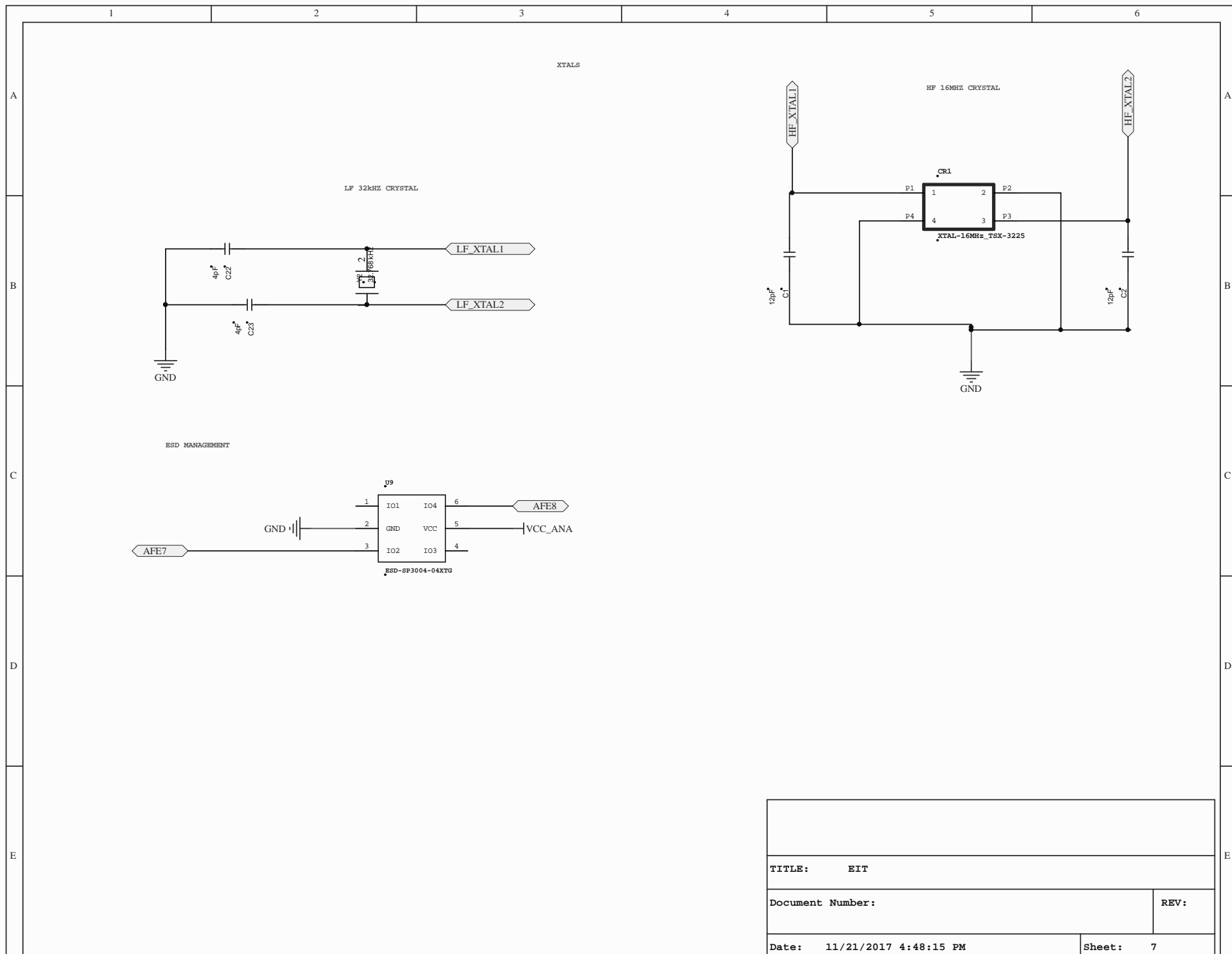
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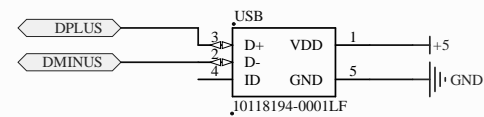
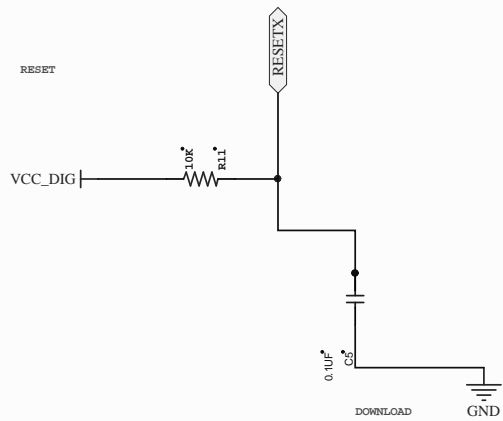
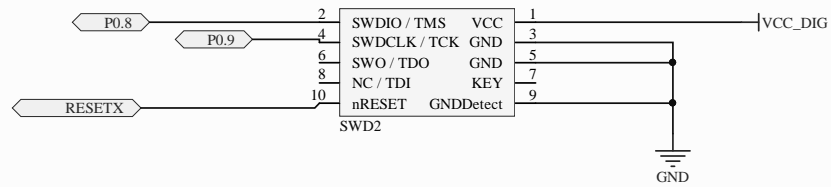
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Input/Output Breakouts for comms, SWD, UART, I2C, SPI and PWR

COMMUNICATIONS



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## A



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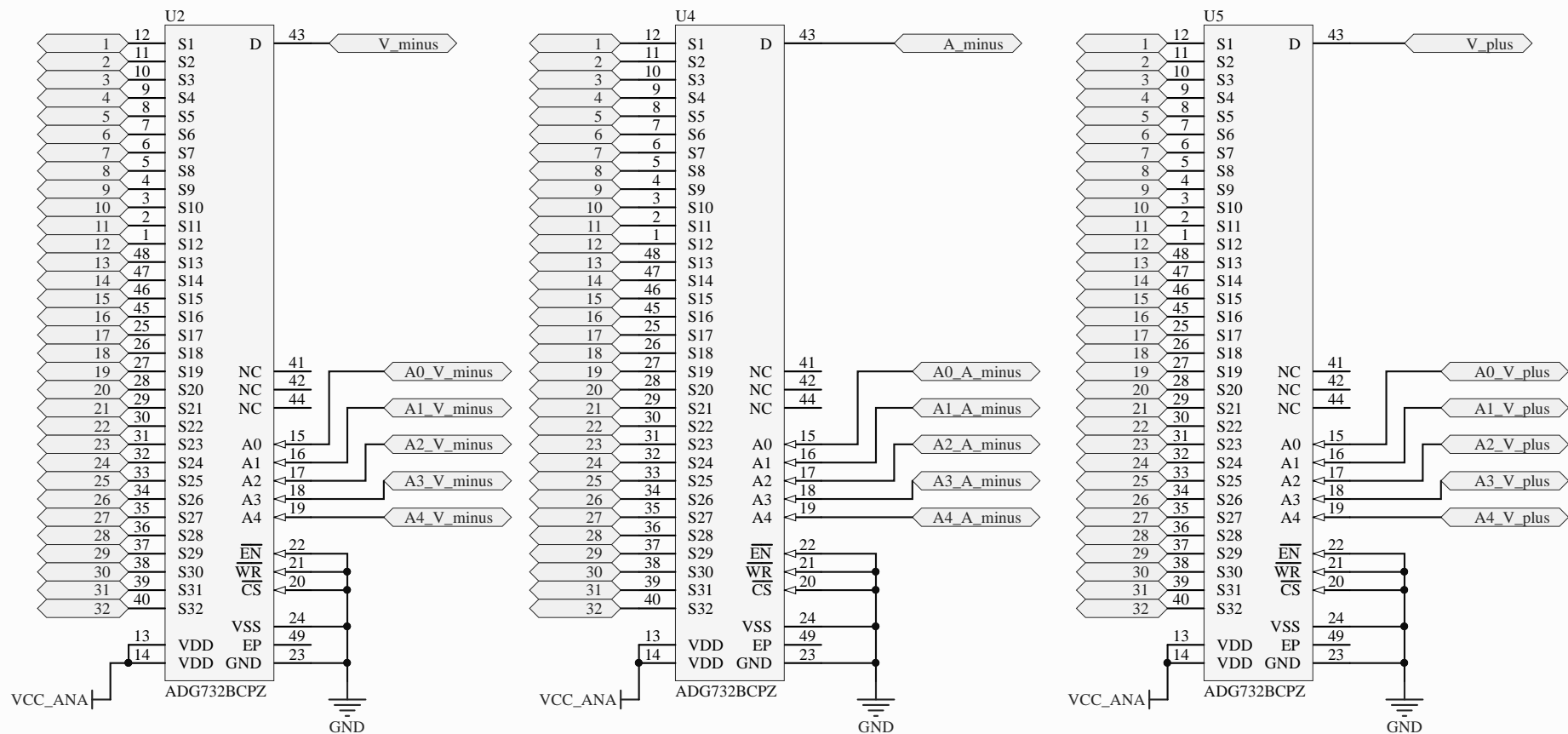
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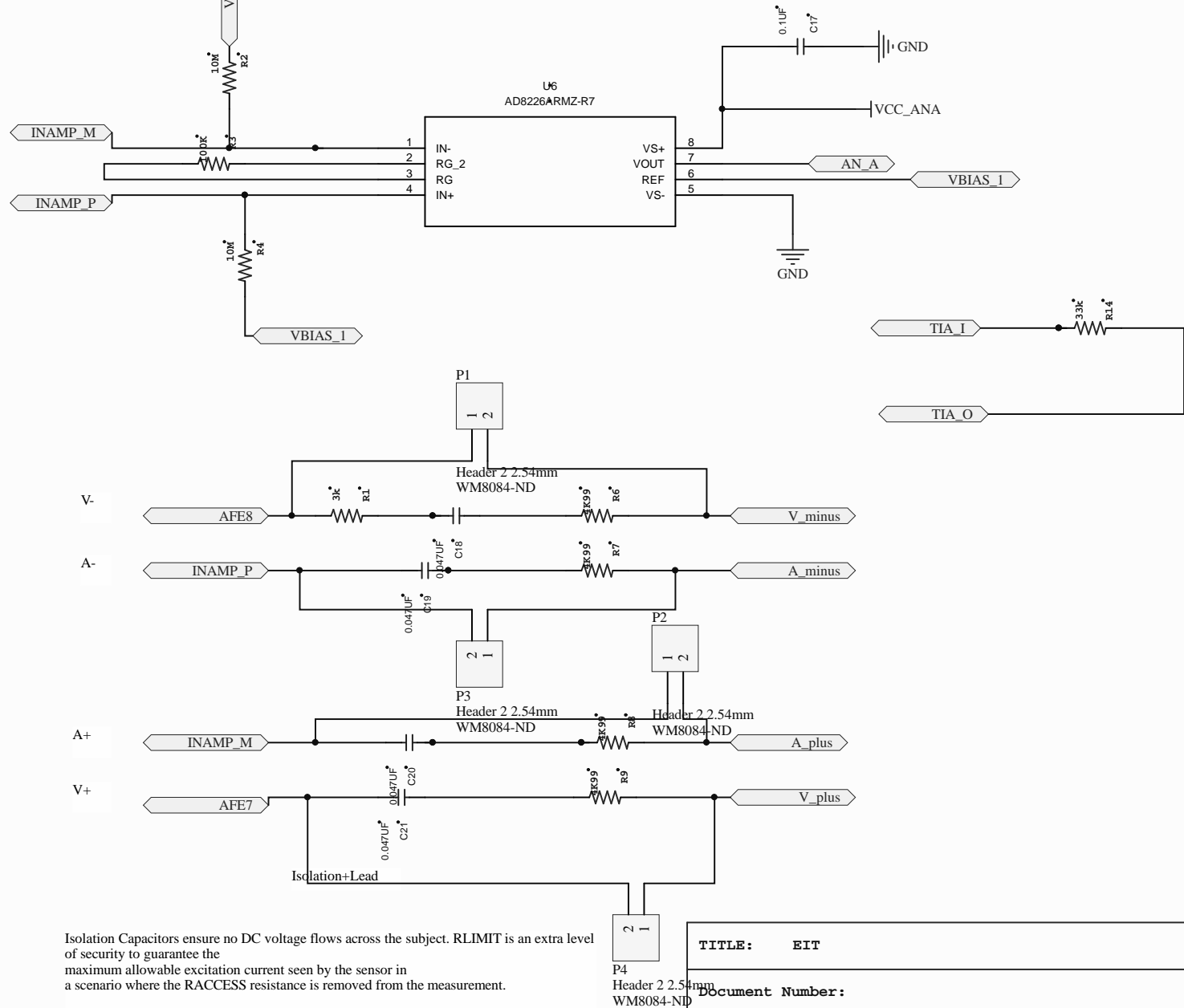
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# Differential Referencing Tetrapolar Implementation allowing adherence to IEC60601-1 Guidelines



Isolation Capacitors ensure no DC voltage flows across the subject. RLIMIT is an extra level of security to guarantee the maximum allowable excitation current seen by the sensor in a scenario where the RACCESS resistance is removed from the measurement.

Due to the isolation capacitors, this set up is not capable of measuring accurate phase information.

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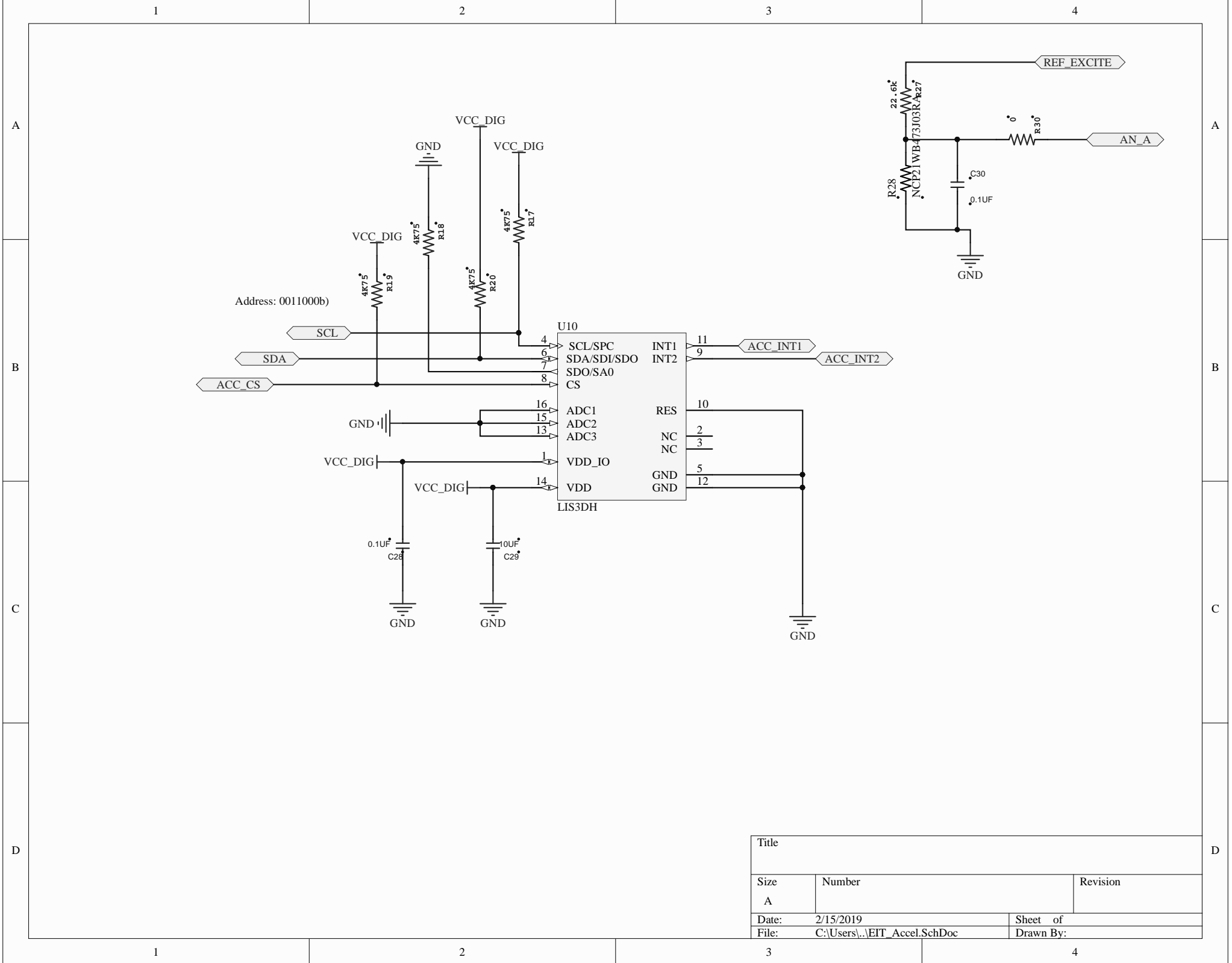
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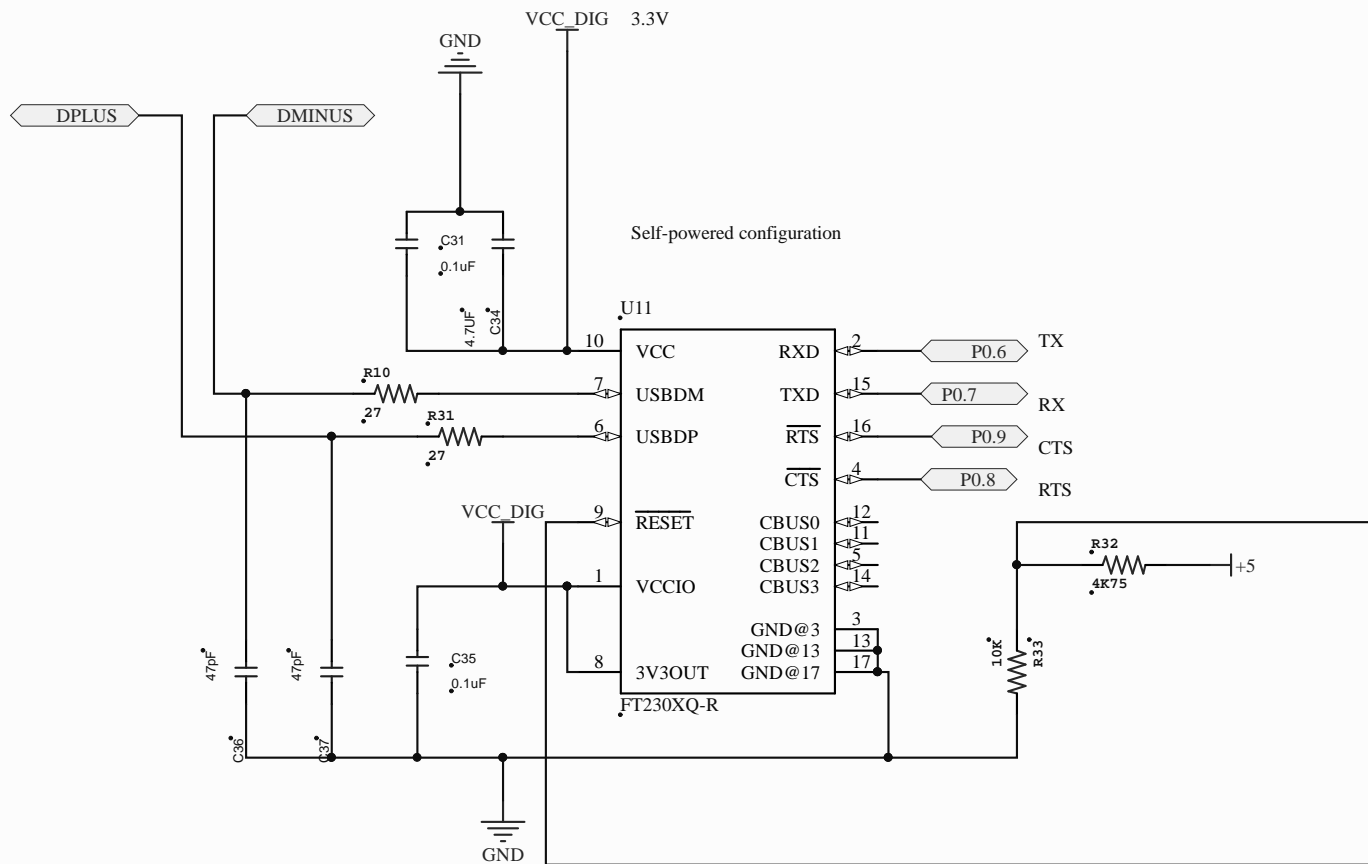
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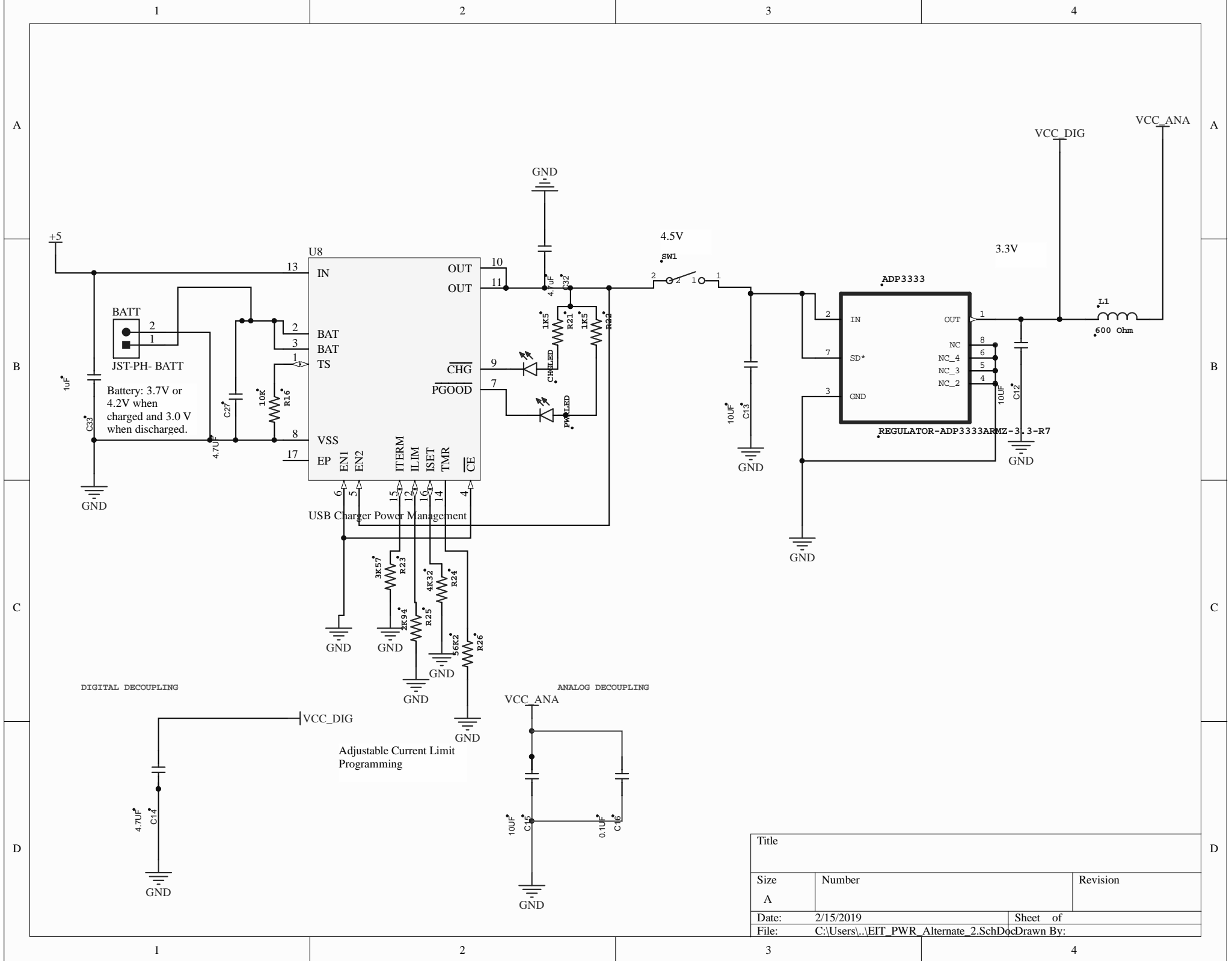








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